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REVIEW

Nonlinear Optics for High-Speed Digital Information Processing

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Recent advances in developing nonlinear optical techniques for processing serial digital information at high speed are reviewed. The field has been transformed by the advent of semiconductor nonlinear devices capable of operation at 100 gigabits per second and higher, well beyond the current speed limits of commercial electronics. These devices are expected to become important in future high-capacity communications networks by allowing digital regeneration and other processing functions to be performed on data signals "on the fly" in the optical domain.

Nonlinear optical effects are not part of our everyday experience. At the relatively low light intensities that normally occur in nature, the optical properties of materials at any instant in time are independent of the intensity of illumination. When light waves pass through a medium, there is no interaction between the waves. These are the properties

of matter that are familiar to us through our visual sense. However, if the illumination is made sufficiently intense, the optical properties of the medium begin to depend on the intensity and other characteristics of the light. For example, the refractive index n of the medium is changed by an amount $\Delta n = n_2 I$, where I is the optical intensity and n_2 is the

nonlinear refraction coefficient. The incident light waves may then interact with each other as well as with the medium. This is the realm of nonlinear optics (1, 2).

Within the past decade, optical fiber cable has been installed in vast quantities in telecommunications networks throughout the world, and the use of light for transmission of information has become ubiquitous. Already,

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nonlinear optical effects influence the design and performance of advanced high-capacity systems. The optical intensities necessary for telecommunications are often sufficient for nonlinear effects to occur readily in fiber, and these effects have been studied extensively (3). Indeed, nonlinear effects are among the dominant factors in determining the range and information carrying capacity of fiber transmission systems (4). Here we review recent advances in developing nonlinear optical techniques for processing digital information at high speed in the optical domain.

Digital optical techniques are expected to become increasingly important in future ultra-high-capacity communications networks. Optical transmission systems with capacities of hundreds of gigabits per second are available commercially today, and experimental systems with capacities of several terabits per second on a single fiber have been demonstrated in the laboratory (5). These high capacities are achieved by using wavelength division multiplexing to carry many optical channels simultaneously on the same fiber. Single-channel bit rates have also steadily increased with time, both commercially and in research laboratories, and transmission speeds approaching 1 Tbit/s have been demonstrated (6). For data rates higher than about 40 Gbit/s, experimental transmission systems often involve the use of nonlinear optical devices to access the data as direct electronic detection is limited in speed. Typically, a nonlinear device is used to demultiplex a lower bit-rate channel, suitable for electronic detection and processing, from a high-speed optical time division multiplexed data stream (Fig. 1A). Blow *et al.* (7) demonstrated the potential of nonlinear optical interferometers for this application, with recently reported extensions to very high speed by demultiplexing 10 Gbit/s from 640 Gbit/s (6). Another key application of nonlinear optical devices in advanced communication systems is to perform regeneration of signals in the optical domain (Fig. 1B). To overcome the effects of noise, dispersion, crosstalk, and other physical impairments during transmission, full digital regeneration is needed. This so-called 3R regeneration causes the signals to be reamplified, reshaped, and retimed (8, 9).

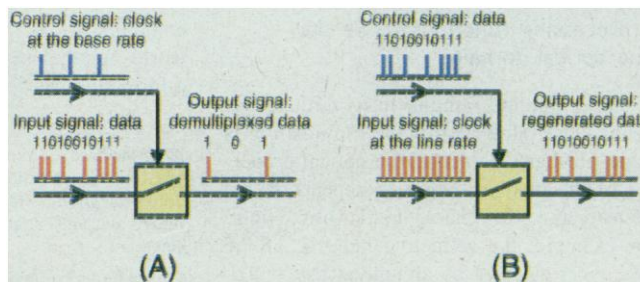
The introduction of these and other digital optical techniques will enable the future development of high-capacity digital photonic networks. Today's networks typically consist of electronic boxes (switches and routers) interconnected by pipes (point-to-point optical transmission systems). Although each pipe can provide huge capacity (several terabits per second), the current boxes-and-pipes network architecture has certain limitations: the electronic box connected at the end of each pipe must be powerful enough to process all the incoming traffic. This is inefficient because a large proportion of this traffic may be in transit to other destinations. This bottleneck will be avoided in the future by using photonic networks, in which signals are routed optically toward their destinations without requiring conversion to the electronic domain at the intermediate nodes. One approach to photonic networking, which currently is receiving the attention of many researchers, is to use the wavelength of a signal to determine its destination address and to route the signals by using wavelength-selective elements at the network nodes. In practice it is found that the restricted number of discrete wavelength channels that can be accommodated limits the extent of such a network unless wavelength conversion is used at the nodes. This has driven a large research activity in wavelength conversion techniques (10). An alternative approach is to use optical packet networks (11, 12), in which the destination address is coded within each data packet (12), which has the advantage of using logical addressing, as in electronic data networks, to give a near infinite address space. In the future, digital processing techniques could be used not only for wavelength conversion, 3R regeneration, and destination-address recognition but also to enable routing, error detection, coding, and other low-level signal-processing functions. These functions could be performed on the fly at ultrafast speed in the optical domain, thus alleviating bottlenecks at electronic packet switches and routers. In the future this approach could allow more efficient use of resources for ultra-high-capacity data networking by allowing statistical multiplexing of traffic and rapid network adaptation at the physical level.

Nonlinear Optical Switch

The basic processing element needed for high-speed digital optical processing is a fast-gating device, which allows one optical signal to control a gate that switches a second optical signal (all-optical switching). The device that has proved most successful is some form of nonlinear interferometer, and the Sagnac, Mach-Zehnder, and Michelson configurations have all been used in systems experiments (13). The basic operation is the same for all configurations. The input signal, which is to be switched, is split between the arms of the interferometer. The interferometer is balanced so that, in the absence of a control signal, the input signal emerges from one output port. The effect of applying the control signal is to induce a differential phase shift, $\Delta\Phi$, between the two arms so that the input signal is switched over to a second output port. In the case of the Sagnac interferometer, one of the output ports also serves as the input port for the switched signal (Fig. 2A).

The first demonstrations of ultrafast processing with an optical fiber interferometer used the Sagnac configuration, best known as the nonlinear optical loop mirror (NOLM) (14) (Fig. 2A), in which the nonlinear element is a length of optical fiber (the offset x can be disregarded at present). The Sagnac configuration has the advantage of greater inherent stability than other fiber interferometers. The input coupler splits the input signal pulse into two counterpropagating pulses, which subsequently combine again at the coupler, each having traveled around the loop. The usual method of breaking the symmetry of the interferometer is to insert a powerful control pulse into the loop as a unidirectional beam. The nonlinear optical effect of the control pulse is to induce a refractive index change, Δn , which is experienced fully by a copropagating signal pulse but to a much lesser degree by a counterpropagating signal pulse. This refractive index change results in a differential phase shift, $\Delta\Phi$, between the counterpropagating signal pulses as they arrive back again at the input coupler, given by $\Delta\Phi = k\Delta nL$, where k is the wavevector and L is the path length over which the induced index change Δn is effective. Typically, the control pulse should induce a differential phase shift, $\Delta\Phi = \pi$ radians, to effect complete switching. Truly ultrafast processing is possible in a fiber interferometer because the physical mechanism is the intrinsic nonlinear optical refraction of the glass, whose response and relaxation times are believed to be a few femtoseconds (3). The main drawbacks stem from the very small optical nonlinearity of glass ($n_2 \approx 3 \times 10^{-20} \text{ m}^2 \text{ W}^{-1}$ for silica), and therefore a device control power-length product of typically $\sim 1 \text{ Wkm}$ is needed to achieve $\Delta\Phi = \pi$. In practice, fiber interferometer path lengths

Fig. 1. Nonlinear optical gate used as a high-speed digital demultiplexer (A) and as a regenerator (B). Arrival of an optical control pulse causes the gate to transmit a single optical input bit. For the demultiplexer, the control signal is a regular periodic pulse train (clock) at the bit rate of the demultiplexed channel (base rate). For the regenerator, the control signal is random binary data.



of several kilometers are needed to keep the average control power to a reasonable level (<100 mW). For lengths greater than a few hundred meters, the stability of the NOLM is significantly degraded. The long path lengths make it difficult to create systems of more than one device and also introduce excessive latency for some processing applications.

Since the 1980s there has been much research on nonlinear optical materials to try to discover one having the advantages of silica (high transparency and ultrafast speed of response) but with much larger nonlinear coefficients. Stegeman and Miller (15) derived various figures of merit that gave a clear set of criteria that a material must satisfy to be of practical use. In essence, these criteria are that the nonlinear coefficients must be sufficiently large, and the optical attenuation sufficiently low, to allow $\Delta\Phi = \pi$ to be achieved at a practical optical power level. As well as these physical criteria, systems considerations dictated further requirements (16). The search for suitable materials was helped by the work of Sheik-Bahae *et al.* (17), who derived a unified model for the bound electronic nonlinearity of a wide range of dielectrics and semiconductors. However, the search proved frustrating. It gradually became clear that the necessary criteria for practical use of passive nonlinear optical materials are essentially incompatible.

Active Semiconductor-Based Switches

The breakthrough came about 6 years ago with the discovery that active semiconductor devices (with electrically injected free carriers) could be used to perform high-speed optical gating (18). At that time, it was already known that the semiconductor optical amplifier (SOA) is highly nonlinear in its optical properties. The SOA is similar to a semiconductor laser diode, except that the reflectivity of the end faces is deliberately minimized to suppress lasing. Much of the development effort on SOAs has, in fact, been directed to minimize the nonlinearity because it tended to introduce unwanted effects (such as frequency chirping and inter-channel crosstalk) in optical communications systems. The interband nonlinear effect in an SOA is an example of a resonant process, which exhibits a large but relatively slowly relaxing response. If an optical beam is incident on a semiconductor in inversion (that is, having an injection current sufficiently large to produce optical gain) and with photon energy slightly larger than the band gap, it is amplified by stimulated emission. This process involves electronic transitions from the conduction to valence bands. The amplification saturates as the conduction band is depopulated (the valence band fills). Associated with this change in gain due to saturation is a concomitant refractive index change, as de-

scribed by the Kramers-Kronig dispersion relations (2). To a good approximation, the refractive index for wavelengths near the band edge is proportional to the carrier density, and hence to the optical intensity, provided the gain is not fully depleted (19). If the saturating optical signal is removed, the refractive index relaxes to its equilibrium value with a time constant that, depending on the carrier lifetime, is typically 100 to 500 ps.

The refractive nonlinearity of the SOA is very large ($>10^8$ times larger than an equivalent length of silica fiber, for a 100-ps optical pulse). Moreover, Stegeman's criteria for the relative magnitudes of the refractive nonlinearity and optical loss of a practical switching device are automatically satisfied because the power of the control and switched signals is amplified in the device. Optical self-switching in a nonlinear Sagnac interferometer with an SOA was demonstrated (20), and the required pulse energy to achieve switching was small (~ 1 nJ in a 10-ns input pulse). However, until the early 1990s, researchers disregarded the SOA as a suitable device for processing high-speed optical data signals. The accepted wisdom at that time was that the nonlinear response must recover fully in the time period of 1 bit. For a typical carrier lifetime of 300 ps, this implied that the fastest data signal that could be processed is ~ 1 Gbit/s, too slow to be of practical interest because this does not compete effectively with electronic processing equipment. Then came two key developments that have changed the scene altogether.

The first key step was development of nonlinear Sagnac interferometers in which a short nonlinear element is positioned asymmetrically with respect to the center of the loop (offset x in Fig. 2A). This arrangement [using either self-switching (21) or controlled switching (22, 23)] could be used to obtain a very short switching window, shorter than the recovery time of the nonlinear refractive index. When the nonlinear element is an SOA, the asymmetric Sagnac interferometer is known by the acronym SLALOM (semiconductor laser amplifier in a loop mirror) (22) or TOAD (terahertz optical asymmetric demultiplexer) (23). When the interferometer is used to demultiplex a lower speed channel from a high-speed data signal (Fig. 1A), the control signal in Fig. 2A is a regular periodic pulse train (called the clock) at the base rate (the bit rate of the demultiplexed channel). The control signal is at a different wavelength (or polarization) from the data-modulated input pulse train and is injected into the SOA with a wavelength coupler (or polarization coupler). After passing through the SOA, the control pulse is coupled out of the loop mirror. The equilibrium balance of the interferometer is set so that, in the absence of a control pulse, no input signal appears at the

output port. When a data pulse arrives, it is split into clockwise (cw) and counterclockwise (ccw) traveling components by the input coupler. The fact that the SOA is positioned asymmetrically within the loop means that the cw component arrives at the SOA before the ccw component. Whether the loop is operated in the SLALOM (22) or TOAD (23) mode depends on the relative timing between the control and input data pulses. In SLALOM operation the control pulse arrives at the SOA before either of the counterpropagating input data pulses. In TOAD operation, the control pulse is timed to arrive after the cw data pulse has passed through the SOA but before the ccw pulse has arrived (Fig. 3). The control pulse rapidly sweeps out some of the gain in the SOA, creating a refractive index change that corresponds to the differential phase shift $\Delta\Phi \approx \pi$ experienced by the cw data pulse. Upon arrival back at the coupler, the phase difference between the cw and ccw pulse means that the recombined data pulse exits from the output port. Subsequent data pulses (in the absence of a clock pulse) experience a gain and refractive index that is slowly recovering; hence the cw and ccw pulses have only a very small phase difference and so after recombining do not appear at the output port. The important aspect of the asymmetric interferometer is that the offset distance x of the SOA from the loop center, rather than the relaxation time of the nonlinear effect, determines the time width of the switching window. Therefore, by suitable choice of window, the effect of a single clock pulse is to switch a single input data bit to the

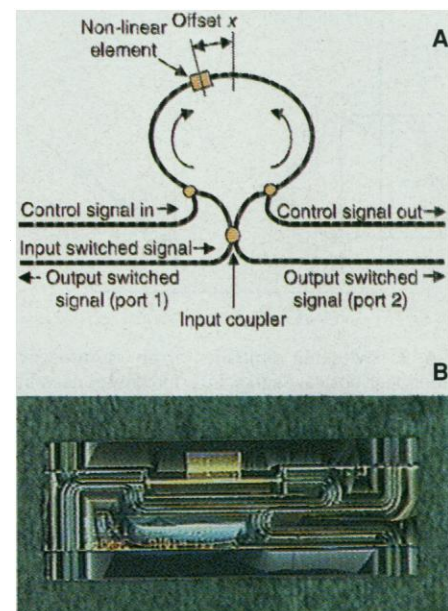


Fig. 2. (A) Optical circuit diagram of a nonlinear Sagnac interferometer configured as an optical gate. **(B)** A realization of this circuit on a single 1×2 mm indium phosphide semiconductor chip, photographed before packaging.

output port. It was demonstrated that a window width of ≤ 10 ps could be created, allowing a very high speed pulse train (~ 50 GHz) to be demultiplexed down to a base rate of ~ 1 GHz (24). However, at first it was believed that the base rate would remain limited by the time for the induced gain and refractive index changes in the SOA to decay fully.

The second key development was the discovery that the asymmetric Sagnac interferometer could be used effectively as a demultiplexer at a base rate of 10 GHz (18). This was clearly much faster than the natural recovery rate of ~ 1 GHz. Since then, several other groups have also demonstrated demultiplexing to a 10-Gbit/s base rate (25), with initial data rates as high as 160 Gbit/s (26). Manning *et al.* (27) explained these results by considering the SOA carrier dynamics in detail. Two important factors were realized. First, the carrier density change required for a differential phase shift, $\Delta\Phi = \pi$, is small compared with the inversion normally available. For a 500- μm -long SOA, and a wavelength of 1.5 μm , $\Delta\Phi = \pi$ corresponds to a change in the refractive index of 10^{-3} . Therefore, given that the rate of change of refractive index with electron-hole pair density is -2×10^{-20} , a carrier population change of only $\sim 10^{17} \text{ cm}^{-3}$ is needed, compared with a typical inversion of $\sim 10^{18} \text{ cm}^{-3}$. Hence a differential phase shift of magnitude several π can be achieved by saturating the SOA gain. Second, the rate at which carriers are replenished by the typical SOA injection current (a few tens of milliamperes) is surprisingly high. Assuming the injection current maintains a typical equilibrium carrier density

of $2 \times 10^{18} \text{ cm}^{-3}$ and the carrier lifetime is ~ 500 ps, then the rate of carrier injection via the current is $\sim 4 \times 10^{15} \text{ cm}^{-3} \text{ ps}^{-1}$. Remarkably, this implies replenishment of 10^{17} cm^{-3} carriers in only ~ 25 ps. It follows then that the injection current is sufficient to cause a phase recovery of π in a very short time. This is the rate of recovery when the gain is strongly saturated by optical pumping, so the carrier density is far from its equilibrium value. Combining these two factors—the relatively small carrier density change needed and the high rate of electrical carrier injection—means that an incremental phase change of π can decay in a time that is a small fraction of the carrier lifetime. Detailed modeling has shown that this successfully explains how the nonlinear interferometer is capable of fast switching (Fig. 3). Experimental measurements under conditions of higher current densities, and hence shorter carrier lifetimes, showed that the injection current can restore a differential phase shift of π radians in as little time as ~ 10 ps (28), implying that operation at demultiplexer base rates as high as 100 Gbit/s should be possible. Experimental support for this prediction was obtained recently when it was shown that the underlying physical mechanism, cross-gain modulation in the SOA, is capable of operation at 100 GHz (29, 30). The required energy of the control pulse is found to be as low as ~ 100 to 200 fJ (26, 31). The predicted average power of the control signal for a demultiplexer at a 100-Gbit/s base rate is therefore ~ 10 to 20 mW, well within the range of practical devices.

In practical terms, another important re-

cent advance has been the development of optical interferometric switches comprising SOAs, waveguides, and optical couplers fully integrated on a single semiconductor chip (Fig. 2B). Several types of interferometer, such as the Sagnac (31), Michelson (32), and Mach-Zehnder (25), have been fabricated in this way and used successfully in optical systems experiments (33).

Optical Digital Regenerator

The various developments up to the mid-1990s showed that compact semiconductor-based nonlinear interferometers are useful as demultiplexing gates for very high speed data signals. However, difficulties were encountered at first when these gates were tested for use in another very important application: high-speed digital optical 3R regeneration. The method for optical regeneration of a return-to-zero digital data stream (8, 9) (Fig. 1B) is that the incoming data bits from a distant source are used to modulate a continuous train of high-quality pulses produced by a synchronous local source, thus regenerating the original data. Each data bit "1" in the incoming data causes the gate to switch to transmission mode for a fixed time (the gate window), allowing a single pulse from the local source to pass through. In this way, the regenerated bits have essentially the same pulse shape, spectral quality, amplitude, and timing stability as the local source. Moreover, the regenerator can tolerate a degree of jitter in the arrival time of the data bits, determined by the gate window width. All-optical 3R regeneration was successfully demonstrated with a fiber NOLM as the switching gate (8). However, when the semiconductor-based nonlinear interferometer was used as the gate, it proved difficult at first to obtain error-free operation.

The reason for this difficulty again can be understood from the carrier dynamics in the SOA. The crucial point is that when the nonlinear interferometer is used as a demultiplexer, the control signal is a regular train of clock pulses. In that case the induced phase changes are regular and periodic (Fig. 3). In contrast, the phase shifts are entirely irregular when the control signal consists of random binary data (as in the regenerator). Numerical calculation of the variation in phase when a random data sequence travels through an SOA under typical conditions (Fig. 4A) shows that large-phase excursions occur whenever several data bits "0" appear consecutively, because there is then sufficient time for the gain to recover substantially. A "1" bit following a sequence of zeros will experience a larger gain than the previous "1," causing a larger phase change. A way of overcoming this data-pattern dependency to some degree is to enhance the recovery rate so that substantial gain recovery occurs in the

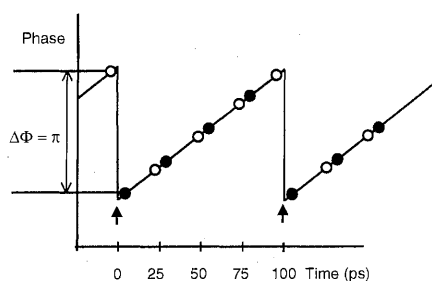


Fig. 3. Switching dynamics of an asymmetric nonlinear optical Sagnac interferometer used in the TOAD configuration. Results of a numerical simulation show the phase change induced by the control signal and the resulting differential phase for the counterpropagating components of the input data signal. Arrival times for the various optical signals at the SOA are indicated by different symbols: \uparrow , control pulse; \circ , clockwise input signal; \bullet , counterclockwise input signal. Difference in arrival times or counterpropagating input signals is determined by offset x shown in Fig. 2. The control signal here is assumed to be a regular periodic pulse train (clock) at a base rate of 10 GHz, and the counterpropagating pairs of data bits occur at 40 Gbit/s (25-ps bit period).

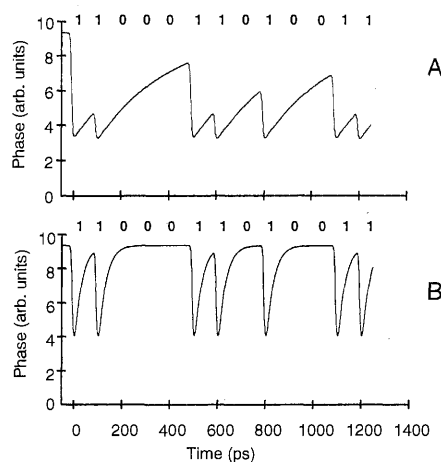


Fig. 4. Data pattern dependency of the induced phase of the SOA when the control signal is a random binary data sequence (1100011010011) at the rate 10 Gbit/s. Curves are results of numerical simulations assuming a carrier lifetime of 300 ps (A) and 30 ps (B) and show effective suppression of pattern effects for the shorter carrier lifetime.

time of one bit period. This can be done by increasing the injection current and also by deliberately reducing the carrier lifetime by stimulated emission at the wavelength of the control pulse or another optical signal (27). By reducing the carrier lifetime to ~ 30 ps (Fig. 4B), any control data bit "1" will cause essentially the same phase change in the SOA. However, the effect of reducing the carrier lifetime is also to reduce the available phase change. Therefore, some compromise is necessary between the levels of injection current and optical input powers. It is also found beneficial to use a relatively long SOA (up to 2-mm active region) and to optimize the design of the multi-quantum well gain region to maximize the optical nonlinearity (34).

Successful operation of nonlinear semiconductor interferometers with very high speed control signals consisting of random binary data has been achieved recently. Wavelength conversion at 40 Gbit/s (35) and 100 Gbit/s (36) and 3R regeneration at 20 Gbit/s (37) and 40 Gbit/s (38) have been reported. In the case of the wavelength converters, one of the inputs is a continuous-wave signal, which has the effect of reducing the carrier lifetime by stimulated emission, as explained above. For the regenerators, good operation is found by using a relatively high-power clock input (typically only 3 dB lower than the control signal power), and this again produces the desired shortening of the carrier lifetime. The good cascability of the devices was demonstrated by incorporating a 40-Gbit/s 3R regenerator in a recirculating loop (39). It was shown that adequate switching windows are attainable to allow 100-Gbit/s digital optical logic (29). Recently Kelly *et al.* (30) confirmed error-free operation at these high bit rates with demonstration of 80-Gbit/s optical regeneration using a nonlinear Mach-Zehnder interferometer containing an SOA, with a control pulse energy of ~ 200 fJ. Figure 5 is an "eye" diagram (a probability histogram of the amplitude of a random data signal plotted as a function of time) of the 80-Gbit/s signal before and after regeneration. Measurements of the bit-error rate at the output of the regenerator after demultiplexing to a 10-Gbit/s base rate showed a bit-error probability of $<10^{-9}$ and a power penalty of 2.7 dB, for a pseudorandom binary data sequence with pattern length $2^{31} - 1$.

Ultimate Limits to Speed

The highest speed of operation of a semiconductor nonlinear interferometer is obtained by judicious choice of the injection current, optical powers, and other device parameters. The ultimate limiting speed is the time to establish an equilibrium Fermi-Dirac distribution of the carrier population, which is believed to be ~ 5 ps (40). This means that

switching rates as high as 200 GHz are theoretically possible. The 80-Gbit/s regenerator recently demonstrated (30) might be approaching the fastest speed that can be achieved in practice with a single gate controlled by a random data signal, the main limitation being data-pattern effects. However, a gate used as a demultiplexer is relatively free from pattern effects and can be operated at higher data rates. This suggests a simple way of extending the maximum speed of an optical regenerator system (Fig. 6). For example, a combination of two 160-Gbit/s demultiplexing gates (26) with two 80-Gbit/s regenerative gates (30) could provide a digital regenerator operating at 160 Gbit/s.

All the semiconductor devices we have described until now rely on optical nonlinearity (either gain/loss or refractive) caused by the real transfer of carriers between conduction and valence bands in the SOA—in other words the real exchange of energy between the optical fields and the material. Yet higher speed of operation can be obtained by nonlinear processes that involve intraband transitions. These consist of a virtual exchange of energy between the field and the medium and allow ultrafast parametric effects such as four-wave mixing (2).

The time scale of these virtual processes in SOAs is known to be in the subpicosecond regime (40), and therefore operation at speeds up to 1 Tbit/s is theoretically possible. Typically, four-wave mixing involves injecting two or more optical signals into the SOA, resulting in generation of optical sidebands. These sidebands may be isolated by filtering and then used for applications such as optical time-domain sampling (41), wavelength conversion (42), or dispersion compensation (43), or to provide an optical AND logic gate (44). For wavelength conversion or dispersion compensation the data signal is mixed with a continuous-wave pump wave. For the AND gate or optical time-domain sampling, two pulsed signals are mixed together in the presence of a pump wave to produce a fourth output signal; this output is present only when the two input pulsed signals are coincident in time. It has been demonstrated recently that these processing devices can operate without errors at 100 Gbit/s (41, 45), and devices at that speed have been used to recognize the destination addresses of optical packets (46), achieve bit-level synchronization (47), and perform clock recovery (48).

First proof-of-principle demonstrations of a variety of digital optical processing func-

Fig. 5. Measured color-coded "eye" diagram (probability histogram of signal amplitude versus time) of an 80-Gbit/s pseudorandom binary data signal (12.5-ps bit period), shown before (upper) and after (lower) optical regeneration in a nonlinear Sagnac interferometer containing a semiconductor optical amplifier. Diagrams were recorded with a photodetector with 30-GHz bandwidth and displayed on a 50-GHz sampling oscilloscope. Although these high-speed data signals are poorly resolved with this electronic display system, considerable nonuniformity between the eight 10-Gbit/s base channels is clearly visible. Also visible is equalization of the channel amplitudes at the output of the regenerator.

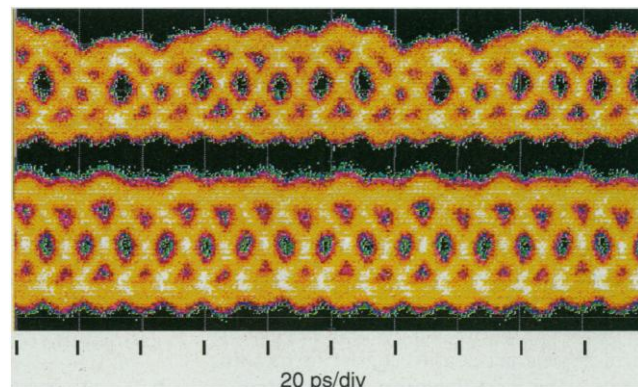
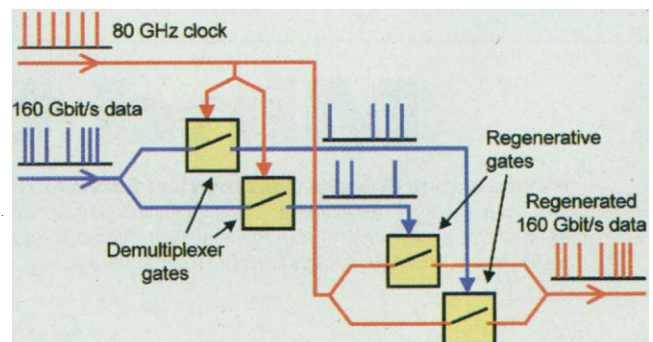


Fig. 6. Concept for an ultra-high-speed digital optical regenerator system consisting of four nonlinear optical gates. Two gates are used as demultiplexers to produce two orthogonal data channels at lower speed, and two gates are used to regenerate these data signals before bit interleaving to produce the regenerated output at the full line rate. In principle, the regenerator could be extended to yet higher bit rates by increasing the number of gates.



tions have been made recently by combining two or three interferometric gates in various simple optical circuits (albeit so far at the relatively low speed of ~ 1 Gbit/s). These have included a shift register with inverter, a regenerative memory with logic level restoration, an optical memory with read/write ability, a binary half-adder, a pseudorandom number generator, and a parity checker (49).

Prospects

Until now, the main challenge for researchers has been to determine what is physically possible and practical for high-speed digital optical logic gates. The field has been largely transformed by the advent of semiconductor devices capable of operation at 100 Gbit/s and potentially higher speeds. Work will continue apace to develop and refine new devices and circuits and to increase the degree of component and device integration. Techniques such as wavelength conversion and optical 3R regeneration are likely to reach commercial application within 1 to 3 years. On a similar time scale, the first prototype devices capable of regenerating data in multiple wavelength channels simultaneously probably will emerge in research laboratories. Demultiplexing gates and elementary logic will be used to enable recognition of flags, symbols, and words at the full-line transmission rate. Later, digital optical devices could allow higher-level processing on the fly in the optical domain, such as error detection, header translation, coding, and encryption.

The most interesting and difficult challenge facing researchers will be to determine not so much whether and how high-speed digital optical devices and processing systems can be made but rather how they can best be applied to useful effect in communications networks. A fundamental difference between electronics and photonics is that optical signals necessarily consist of traveling waves. The most important manifestation of this difference in information systems is the lack of an optical equivalent of electronic static random-access memory. The photonic systems designer is therefore forced to invent new network architectures and protocols rather than simply imitate the designs developed

by electronics engineers. For example, new strategies are needed to resolve contention (which arises when two data packets are routed toward the same pipe simultaneously) in a way that overcomes the lack of static optical memory.

It is unlikely that photonics will compete with the dense integration and rich functionality of electronics, at least for very many years to come. Nevertheless, the digital optical devices being developed in research laboratories today can already offer advantages in terms of speed and compatibility with optical transmission systems. We envisage that within a few years modest numbers of high-speed nonlinear optical devices, processing optical signals on the fly, will be used in tandem with electronics to increase greatly the capacity and throughput of communications networks.

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