



## POWER AMPLIFIER PENTODE

Heater Cathode Unipotential Cathode					
Heater Cathode Unipotential Cathode Heater Arrangement <u>Series</u> <u>Parallel</u>					
Voltage	12.6	6.3	∸ a–cord-	- 4-1+-	
Current	0.3	0.6	a-c 01 0	amp.	
Maximum Overall Length	0.7	0.0	4	3/16"	
Maximum Seated Height				9/16"	
Maximum Diameter			9/16"		
IBu1b			ST-12		
Base		<b>(A)</b>	_	1 7-Pin	
Pin 1-Heater	3/	C/C	Pin 5 - Cathod		
Pin 2 - Plate	_(X	<b></b>	Pin 6 - Heater		
Pin 3 – Screen	<b>Q</b> (7	~~~/®	Pin 7-Heater		
Pin 4 - Grid	ŏ	<del>-76</del>	in / - neater		
Mounting Position	10 T T 10 L	) (   may   1.7mm)		Any	
I would will be a second and a second a second and a second a second and a second a	SOLIOM	VIEW (7F)		7119	
AMPLIFIER					
Plate Voltage			180 max.	volts	
Screen Voltage			180 max.	volts	
Plate Dissipation			8.25 max.	watts	
Screen Dissipation			2.5 max.	watts	
Typical Operation and C	haract	eristics -	Class A. Ambi	ifier:	
Plate Voltage		100	180	volts	
Screen Voltage		100	180	volts	
Grid Voltage <sup>TO</sup>		15	-25	volts	
Peak A-F Grid Volt.		15	25	volts	
Zero-Sig. Plate Cur.		17	45	ma.	
MaxSig. Plate Cur.		19	48	ma.	
Zero-Sig. Screen Cur.		3	8	ma.	
MaxSig. Screen Cur.		6.5	14	ma.	
Plate Resistance		50000	35000 approx	. ohms	
Transconductance		1700	2400	umhos	
Load Resistance		4500	3300	ohms	
Total Harm. Dist.		12	11	%	
Second Harm. Dist.		8.5	6.5	%	
Third Harm. Dist.		. 8	. 8	%	
Max.—Sig. Power Outpu	it	0.8	3.4	watts	
l _					

In circuits where the cathode is not directly connected to the heater, the potential difference between heater and cathode should be kept as low as possible. Heater voltage applied acros: the two sections in series between pins \$1 and \$7.

<sup>#1</sup> and #7.

Meater voltage applied across the two sections in parallel between pin #6 and pins #1 and #7 connected together.

The type of coupling used should not introduce too much resistance in the grid circuit. Transformer— or impedance—coupling devices are recommended. When the grid circuit has a resistance not higher than 0.1 megohm, fixed bias may be used; for higher values, cathode bias is required. With cathode bias, the grid circuit may have a resistance not to exceed 0.5 megohm.