

Vishay Semiconductors

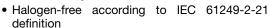
Schottky Rectifier, 3.0 A



PRODUCT SUMMARY				
Package	SMC			
I _{F(AV)}	3.0 A			
V_{R}	15 V			
V _F at I _F	0.3 V			
I _{RM}	50 mA at 100 °C			
T _J max.	125 °C			
Diode variation	Single die			
E _{AS}	1.5 mJ			

FEATURES

- Ultralow forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability





- 125 °C T_J operation (V_R < 5 V)
- · Optimized for OR-ing applications
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS directive 2002/95/EC

DESCRIPTION

The VS-30BQ015-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	3.0	А		
V _{RRM}		15	V		
I _{FSM}	t _p = 5 μs sine	650	А		
V _F	1.0 Apk, T _J = 75 °C	0.30	V		
T _J	Range	- 55 to 125	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-30BQ015-M3	UNITS	
Maximum DC reverse voltage	V_{R}	15	V	
Maximum working peak reverse voltage	V_{RWM}	25	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum avarage forward aurrent		50 % duty cycle at T _L = 83 °C, rectangular waveform		3.0	
Maximum average forward current	kimum average forward current $I_{F(AV)}$		50 % duty cycle at T _L = 78 °C, rectangular waveform		
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	650	А
non-repetitive surge current	I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	75	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 0.5 A, L = 12 mH		1.5	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 0.5		0.5	Α

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	3 A	- T _J = 25 °C	0.35	V
Maximum forward valtage drap		6 A		0.43	
Maximum forward voltage drop		3 A	- T _J = 75 °C	0.30	
		6 A		0.38	
Marian and a land and a land	-	T _J = 25 °C	V _B = Rated V _B	4	mA
Maximum reverse leakage current	I _{RM}	T _J = 100 °C	V _R = nateu V _R	50	IIIA
Maximum junction capacitance	C _T	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		1120	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		3.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

⁽¹⁾ Pulse width = 300 μ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T _J ⁽¹⁾		- 55 to 125	°C
Maximum storage temperature range	T _{Stg}		- 55 to 150	C
Maximum thermal resistance, junction to lead	R _{thJL} (2)	DC operation	12	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	46	O/W
Approximate weight			0.24	g
Approximate weight			0.008	oz.
Marking device		Case style SMC (similar to DO-214AB)	30	C

Notes

⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB



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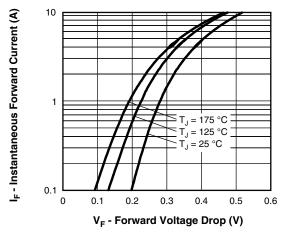


Fig. 1 - Typical Forward Voltage Drop Characteristics (Per Leg)

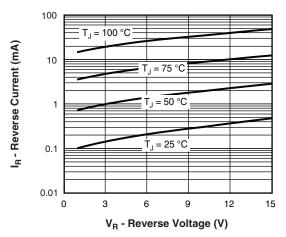


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

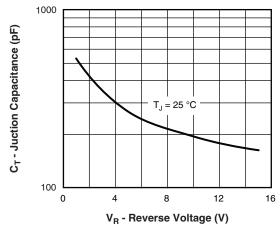


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

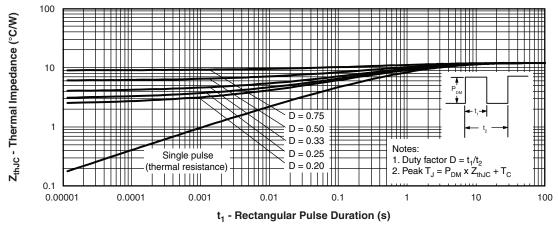


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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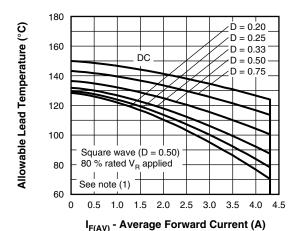


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

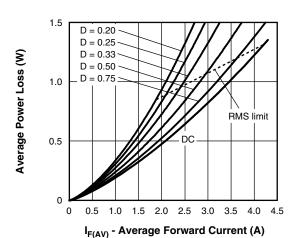


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

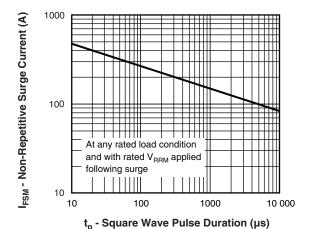


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

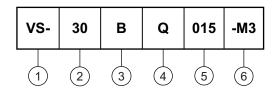
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

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ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product suffix

2 - Current rating

3 - B = SMC

4 - Q = Schottky "Q" series

Voltage rating (015 = 15 V)

6 - Environmental digit:

-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	EFERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-30BQ015-M3/9AT	9AT	3500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95402</u>				
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			



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