

Vishay Siliconix

## N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ )	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)		
30	0.021 at V <sub>GS</sub> = 10 V	12 <sup>a</sup>	3.7 nC		
30	$0.033$ at $V_{GS} = 4.5 \text{ V}$	6	3.7 110		

### **FEATURES**

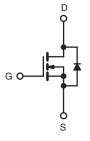
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



ROHS COMPLIANT HALOGEN FREE

#### **APPLICATIONS**

- · Notebook System Power
- Low Current DC/DC



N-Channel MOSFET

	SO-8	
S 1 S 2 S 3 G 4		8 D 7 D 6 D 5 D
	Top View	<b>-</b>

Ordering Information: Si4178DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30	V	
Gate-Source Voltage		V <sub>GS</sub>	± 25		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	$T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 70 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{\Delta} = 70 ^{\circ}\text{C}$	- I <sub>D</sub>	12 <sup>a</sup> 9.7 <sup>a</sup> 8.3 <sup>b, c</sup> 6.7 <sup>b, c</sup>		
Pulsed Drain Current		I <sub>DM</sub>	40	A	
Continuous Source-Drain Diode Current $ T_C = 25 \text{ °C} $ $T_A = 25 \text{ °C} $		I <sub>S</sub>	4.2 2 <sup>b, c</sup>		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	10		
Single Pulse Avalanche Energy	L=0.1 mn	E <sub>AS</sub>	5	mJ	
Maximum Power Dissipation	$T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 70 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{A} = 70 ^{\circ}\text{C}$	- P <sub>D</sub>	5 3.2 2.4 <sup>b, c</sup> 1.5 <sup>b, c</sup>	W	
Operating Junction and Storage Temperatur	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	$R_{thJA}$	42	53	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>th.IF</sub>	19	25	J 5/ V V	

#### Notes:

- a. Package Limited.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s
- d. Maximum under Steady State conditions is 85 °C/W.

## **Si4178DY**

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				7	1		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V, } I_{D} = 250 \mu\text{A}$	30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$			25		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 6			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_{D} = 250 \mu A$	1.4		2.8	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$			± 100	nA	
· ·	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 10 V	20			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8.4 A		0.017	0.021		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V, } I_D = 2 \text{ A}$		0.027	0.033	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 8.4 A		22		S	
Dynamic <sup>b</sup>						<u> </u>	
Input Capacitance	C <sub>iss</sub>			405			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		110		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			56			
Total Gate Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 8.4 \text{ A}$		7.5	12	nC	
Total date onlarge				3.7	5.6		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 8.4 \text{ A}$		1.6			
Gate-Drain Charge	$Q_{gd}$			1.3			
Gate Resistance	$R_g$	f = 1 MHz	0.5	2.6	5.2	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			20	30	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 2.2 $\Omega$		15	25		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 6.7 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		11	20		
Fall Time	t <sub>f</sub>			10	15		
Turn-On Delay Time	t <sub>d(on)</sub>			7	15		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 2.2 $\Omega$		10	15		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ 6.7 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		12	20		
Fall Time	t <sub>f</sub>			10	15		
<b>Drain-Source Body Diode Characteristi</b>	cs						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			4.2	А	
Pulse Diode Forward Current	I <sub>SM</sub>				40		
Body Diode Voltage	V <sub>SD</sub>	$I_S = 6.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.85	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			15	30	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 6.7 A, dI/dt = 100 A/μs, T <sub>J</sub> = 25 °C		8	16	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	i <sub>F</sub> = 0.7 A, αί/αι = 100 A/μs, 1 <sub>J</sub> = 25 °C		8.5		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			6.5			

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %

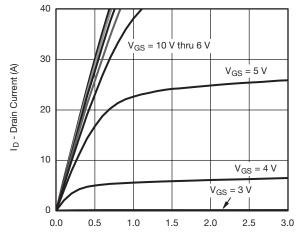
b. Guaranteed by design, not subject to production testing.

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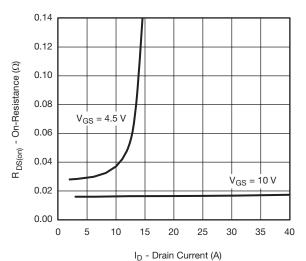
## Vishay Siliconix

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

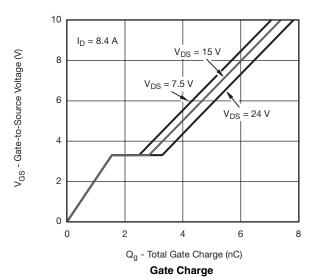


V<sub>DS</sub> - Drain-to-Source Voltage (V)

### **Output Characteristics**



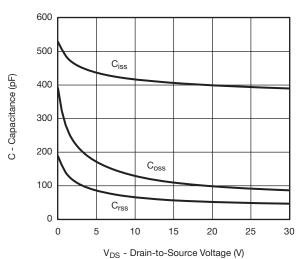
On-Resistance vs. Drain Current



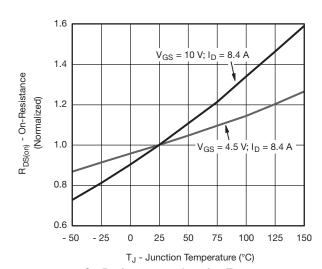
10 8 I<sub>D</sub> - Drain Current (A) 6 T<sub>C</sub> = 25 °C 4 2 T<sub>C</sub> = 125 °C - 55 °C T<sub>C</sub> = 1 2 0

V<sub>GS</sub> - Gate-to-Source Voltage (V)

### **Transfer Characteristics**



### Capacitance



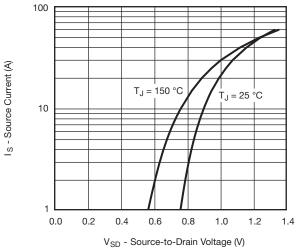
On-Resistance vs. Junction Temperature

## **Si4178DY**

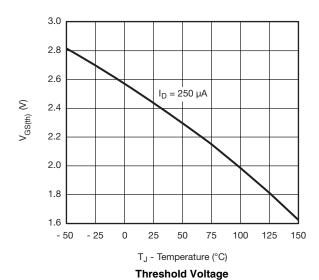
## Vishay Siliconix

# VISHAY.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

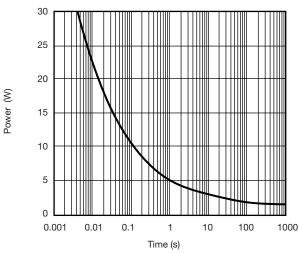


### Source-Drain Diode Forward Voltage

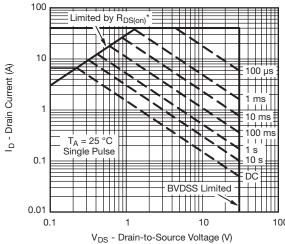


0.050 0.045 R<sub>DS(on)</sub> - On-Resistance (Ω) 0.040 I<sub>D</sub> = 2 A; T<sub>J</sub> = 125 °C 0.035 = 8.4 A; T<sub>J</sub> = 125 °C 0.030  $I_D = 8.4 \text{ A}; T_J = 25 \degree \text{C}$ 0.025 0.020  $I_D = 2 A; T_J = 25 °C$ 0.015 0.010 0 8 10 V<sub>GS</sub> - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power



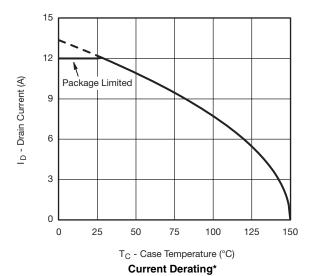
\* V<sub>GS</sub> > minimum V<sub>GS</sub> at which R<sub>DS(on)</sub> is specified

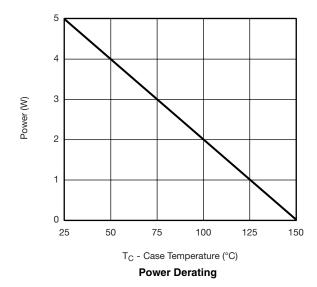
Safe Operating Area, Junction-to-Ambient



## Si4178DY Vishay Siliconix

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





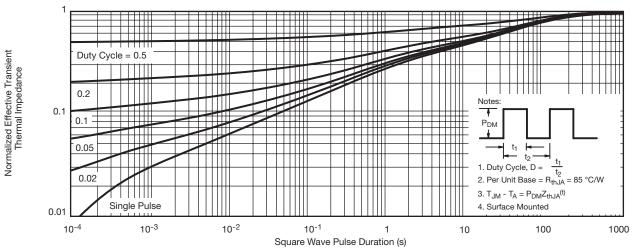
<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

### **Si4178DY**

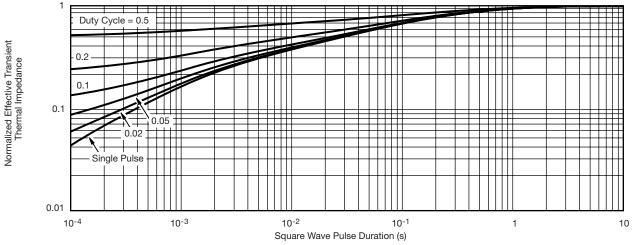
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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppq?65718">www.vishay.com/ppq?65718</a>.



SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIMETERS		INC	HES	
DIM	Min	Max	Min	Max	
Α	1.35	1.75	0.053	0.069	
A <sub>1</sub>	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
Е	3.80	4.00	0.150	0.157	
е	1.27	BSC	0.050 BSC		
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I. 11-Sep-06					

DWG: 5498

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### **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)

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