# BAV19WS-V, BAV20WS-V, BAV21WS-V

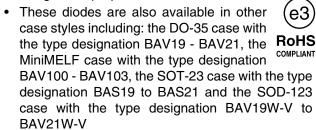


**Vishay Semiconductors** 

# **Small Signal Switching Diodes, High Voltage**

#### **Features**

- · Silicon Epitaxial Planar Diodes
- For general purpose





 Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC



### **Mechanical Data**

Case: SOD-323
Weight: approx. 5 mg
Packaging Codes/Options:

GS18/10K per 13" reel (8 mm tape), 10K/box GS08/3K per 7" reel (8 mm tape), 15K/box

#### **Parts Table**

Part	Type differentiation	Ordering code	Type Marking	Remarks
BAV19WS-V	V <sub>R</sub> = 100 V	BAV19WS-V-GS18 or BAV19WS-V-GS08	A8	Tape and reel
BAV20WS-V	V <sub>R</sub> = 150 V	BAV20WS-V-GS18 or BAV20WS-V-GS08	A9	Tape and reel
BAV21WS-V	V <sub>R</sub> = 200 V	BAV21WS-V-GS18 or BAV21WS-V-GS08	AA	Tape and reel

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# BAV19WS-V, BAV20WS-V, BAV21WS-

## **Vishay Semiconductors**



## **Absolute Maximum Ratings**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Value	Unit
		BAV19WS-V	V <sub>R</sub>	100	V
Continuous reverse voltage		BAV20WS-V	V <sub>R</sub>	150	V
		BAV21WS-V	V <sub>R</sub>	200	V
		BAV19WS-V	V <sub>RRM</sub>	120	V
Repetitive peak reverse voltage		BAV20WS-V	V <sub>RRM</sub>	200	V
		BAV21WS-V	V <sub>RRM</sub>	250	V
Forward continuous current			I <sub>F</sub>	250 <sup>1)</sup>	mA
Rectified current (average) half wave rectification with resist. load			I <sub>F(AV)</sub>	200 <sup>1)</sup>	mA
Repetitive peak forward current	$f \ge 50 \text{ Hz},  \theta = 180^{\circ}$		I <sub>FRM</sub>	625 <sup>1)</sup>	mA
Surge forward current	t < 1 s, T <sub>j</sub> = 25 °C		I <sub>FSM</sub>	1	Α
Power dissipation			P <sub>tot</sub>	200 <sup>1)</sup>	mW

#### Note

### **Thermal Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

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Parameter	Test condition	Symbol	Value	Unit	
Thermal resistance junction to ambient air		R <sub>thJA</sub>	650 <sup>1)</sup>	K/W	
Junction temperature		T <sub>j</sub>	150 <sup>1)</sup>	°C	
Storage temperature range		T <sub>stg</sub>	- 65 to + 150 <sup>1)</sup>	°C	

#### Note

### **Electrical Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Min.	Тур.	Max.	Unit
Forward voltage	I <sub>F</sub> = 100 mA		V <sub>F</sub>			1	V
	I <sub>F</sub> = 200 mA		V <sub>F</sub>			1.25	V
	V <sub>R</sub> = 100 V	BAV19WS-V	I <sub>R</sub>			100	nA
	V <sub>R</sub> = 100 V, T <sub>j</sub> = 100 °C	BAV19WS-V	I <sub>R</sub>			15	μΑ
Leakage current	V <sub>R</sub> = 150 V	BAV20WS-V	I <sub>R</sub>			100	nA
Leakage current	V <sub>R</sub> = 150 V, T <sub>j</sub> = 100 °C	BAV20WS-V	I <sub>R</sub>			15	μΑ
	V <sub>R</sub> = 200 V	BAV21WS-V	I <sub>R</sub>			100	nA
	V <sub>R</sub> = 200 V, T <sub>j</sub> = 100 °C	BAV21WS-V	I <sub>R</sub>			15	μΑ
Dynamic forward resistance	I <sub>F</sub> = 10 mA		r <sub>f</sub>		5		Ω
Diode capacitance	V <sub>R</sub> = 0, f = 1 MHz		C <sub>D</sub>		1.5		pF
Reverse recovery time	$I_F = 30 \text{ mA}, I_R = 30 \text{ mA},$ $I_R = 3 \text{ mA}, R_L = 100 \Omega$		t <sub>rr</sub>			50	ns

<sup>1)</sup> Valid provided that leads are kept at ambient temperature

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## **Vishay Semiconductors**

## **Typical Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

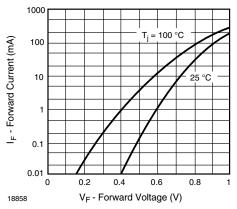


Figure 1. Forward Current vs. Forward Voltage

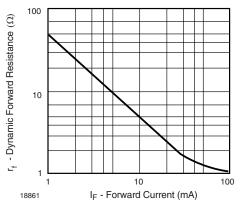


Figure 4. Dynamic Forward Resistance vs. Forward Current

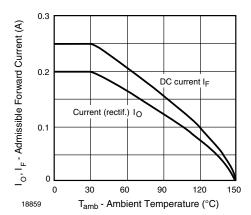


Figure 2. Admissible Forward Current vs. Ambient Temperature

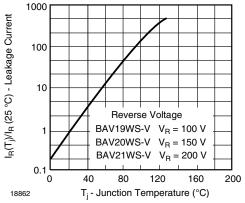


Figure 5. Leakage Current vs. Junction Temperature

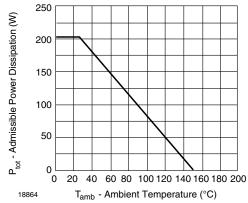


Figure 3. Admissible Power Dissipation vs. Ambient Temperature

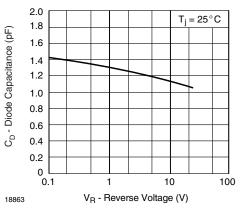


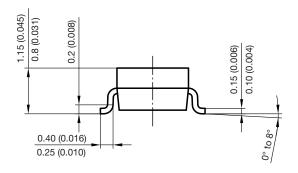
Figure 6. Capacitance vs. Reverse Voltage

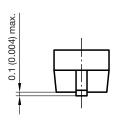
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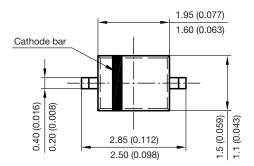
## **Vishay Semiconductors**



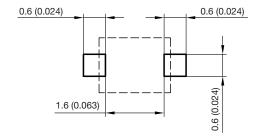
## Package Dimensions in millimeters (inches): SOD-323







Foot print recommendation:



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