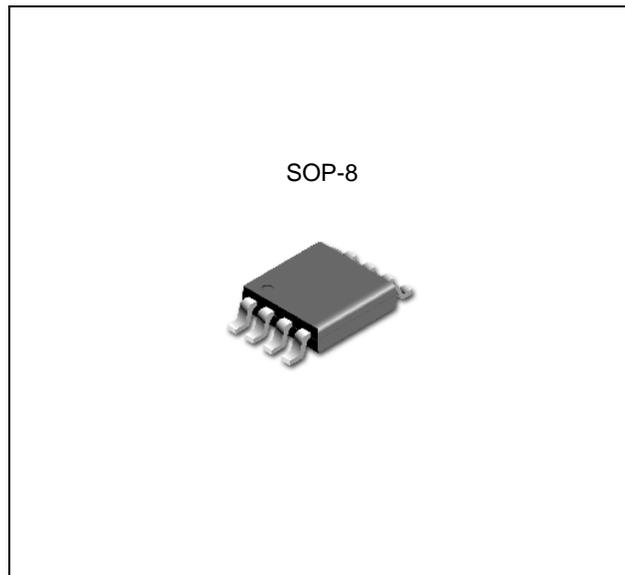


DESCRIPTION

The TJ3485 is a low-power half-duplex transceiver that meets the specifications of RS-485 and RS-422. IC contains one driver and one receiver. The driver slew rates is not limited, allowing them to transmit up to 5Mbps. These transceivers draw between 120uA and 500uA of supply current when unloaded or fully loaded with disabled drivers. All parts operate from a single 3.3V supply. Drivers are short-circuit current limited and are protected against excessive power dissipation by thermal shutdown circuitry that places the driver outputs into a high-impedance states. The receiver input has a fail-safe feature that guarantees a logic-high output if the input is open circuit. The ESD tolerance is more than ±8kV for both Human Body Model and ±15kV for IEC61000-4-2 Air Discharge Method on this device.



FEATURES

- Single 3.3V Supply
- Low Quiescent Current: 300uA
- -7V to +12V Common-Mode Input Voltage Range
- Three-state Outputs
- Half-Duplex Versions Available
- Allows up to 32 Transceivers on the Bus
- Data rate: 5 Mbps
- Current-Limiting and Thermal Shutdown for Driver Overload Protection
- ESD Specifications
 - ±15kV IEC61000-4-2 Air Discharge
 - ±8kV Human Body Model

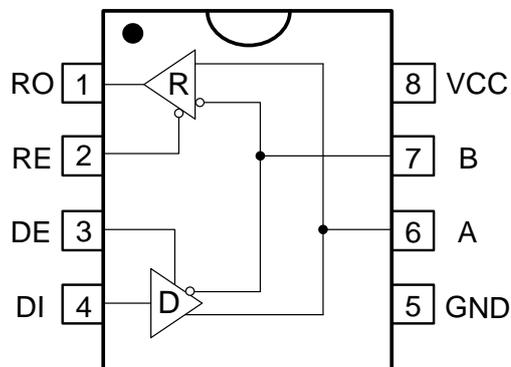
TRUTH TABLE

Transmission				
Inputs			Outputs	
RE	DE	DI	A	B
X	1	1	1	0
X	1	0	0	1
0	0	X	Z	Z
1	0	X	Z	Z
Receiver				
Inputs			Outputs	
RE	DE	A-B	RO	
0	0	≥ +0.2V	1	
0	0	≤ -0.2V	0	
0	0	Open	1	
1	0	X	Z	

APPLICATIONS

- Low Power RS-485 Systems
- DTE-DCE Interface
- Packet Switching
- Local Area Networks
- Data Concentration
- Data Multiplexers
- Integrated Services Digital Network (ISDN)

Pin Configuration and Logic Diagram

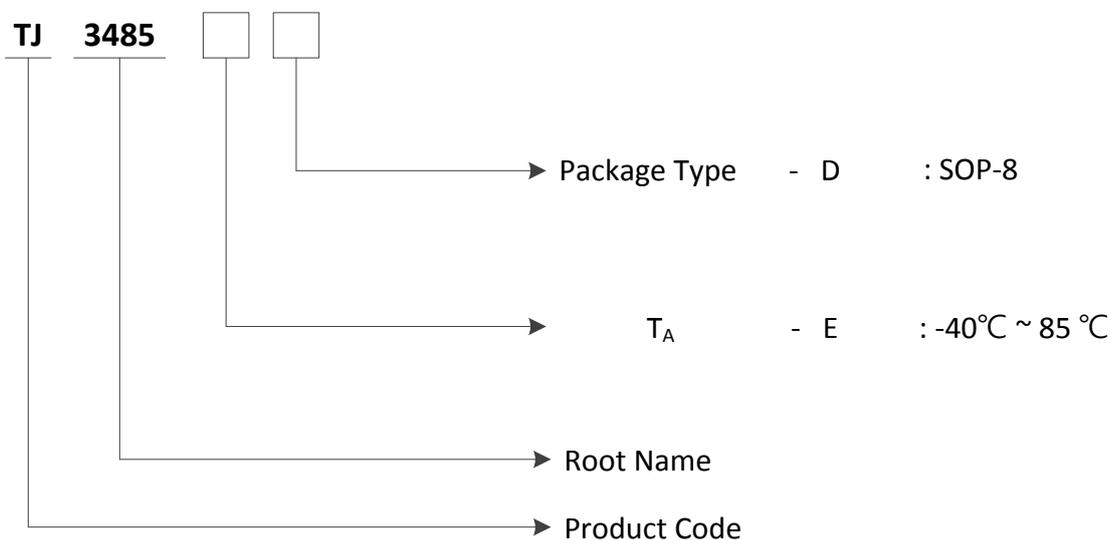


Absolute Maximum Ratings

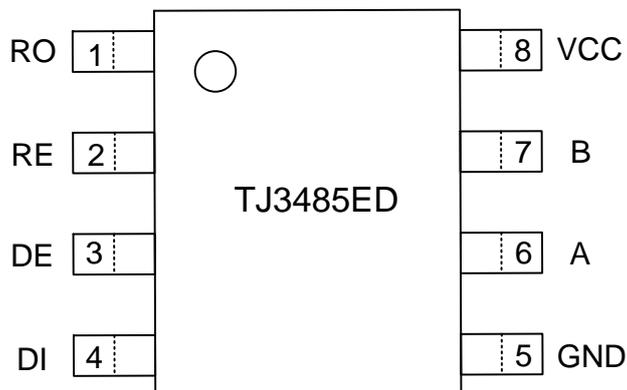
Characteristic	Symbol	Min	Max	Unit
Supply Voltage	V_{CC}		7	V
Control Input Voltage	V_{DE}, V_{RE}	-0.3	7	V
Driver Input Voltage	V_{DI}	-0.3	7	V
Driver Output Voltage	A, B	-7.5	12.5	V
Receiver Input Voltage	A, B	-7.5	12.5	V
Receiver Output Voltage	V_{RO}	-0.3	$V_{CC} + 0.3$	V
Storage Temperature Range	T_{STG}	-65	150	°C

Ordering Information

Package	Oder No.	Description	Marking	Compliance	Status
SOP-8	TJ3485ED	RS-485/RS-422 Transceivers	TJ3485E	RoHS, Green	Active



PIN CONFIGURATION



PIN DESCRIPTION

Pin No.	Symbol	Pin Descriptions
1	RO	Receiver Output
2	RE*	Receiver Output Enable Active Low
3	DE	Driver Output Enable Active High
4	DI	Driver Input
5	GND	Ground
6	A	Non-inverting Driver Output and Receiver Input
7	B	Inverting Driver Output and Receiver Input
8	VCC	Power Supply: 3.3V±5%

DC ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: $V_{CC} = 3.3V \pm 5\%$, $T_A = T_{MIN}$ to T_{MAX})

PARAMETER	Symbol	CONDITIONS	MIN	TYP	MAX	UNITS	
No-Load Supply Current	ICC	RE = 0V or V _{CC}	DE=V _{CC}		500	800	uA
			DE=0V		300	400	
DRIVER DC Characteristics							
Differential Driver Output (no load)	V _{OD1}	R _L = ∞, Figure 1	GND		V _{CC}	V	
Differential Driver Output (with load)	V _{OD2}	R _L = 50Ω (RS-422), Figure 1	1		V _{CC}	V	
		R _L = 27Ω (RS-485), Figure 1	0.8		V _{CC}		
Change in Magnitude of Driver Differential Output Voltage for Complementary Output States	ΔV _{OD}	R _L = 27Ω or 50Ω, Figure 1			0.2	V	
Driver Common-Mode Output Voltage	V _{OC}	R _L = 27Ω or 50Ω, Figure 1			2	V	
Change in Magnitude of Driver Common-Mode Output Voltage for Complementary Output States	ΔV _{OC}	R = 27Ω or 50Ω, Figure 1			0.2	V	
Input High Voltage	V _{IH}	DE, DI, RE*	2.0			V	
Input Low Voltage	V _{IL}	DE, DI, RE*			0.8	V	
Input Current	I _{IN1}	DE, DI, RE*			±2	uA	
Driver Short-Circuit Current, V _O = High	I _{OSD1}	-7V ≤ V _O ≤ 12V			±250	mA	
Driver Short-Circuit Current, V _O = Low	I _{OSD2}	-7V ≤ V _O ≤ 12V			±250	mA	
RECEIVER DC Characteristics							
Receiver Differential Threshold Voltage	V _{TH}	-7V ≤ V _{CM} ≤ 12V	-0.2		0.2	V	
Receiver Input Hysteresis	ΔV _{TH}	V _{CM} = 0V		70		mV	
Receiver Output High Voltage	V _{OH}	I _O = -4mA, V _{ID} = +200mV	2.5			V	
Receiver Output Low Voltage	V _{OL}	I _O = +4mA, V _{ID} = -200mV			0.4	V	
Three-State (High Impedance) Output Current at Receiver	I _{OZR}	0.4V ≤ V _O ≤ 2.4V, RE* = 3V			±1	uA	
Receiver Input Resistance	R _{IN}	-7V ≤ V _{CM} ≤ 12V	12			kΩ	
Input Current (A, B)	I _{IN2}	DE = 0V	V _{IN} = 12V		1.0	mA	
		V _{CC} = 0V or 3.3V	V _{IN} = -7V		-0.8		
Receiver Short-Circuit Current	I _{OSR}	0V ≤ V _O ≤ V _{CC}	±6.5		95	mA	

SWITCHING CHARACTERISTICS

(Unless otherwise specified: $V_{CC} = 3.3V \pm 5\%$, $T_A = T_{MIN}$ to T_{MAX})

PARAMETER	Symbol	CONDITIONS	MIN	TYP	MAX	UNITS
Maximum Data Rate	f_{MAX}		2.5	5	10	Mbps
Driver Input to Output	t_{DPLH}	Figure 3 & 5 $R_L = 54\Omega, C_{L1} = C_{L2} = 100pF$	10	80	100	ns
	t_{DPHL}		10	80	100	ns
Driver Output Skew to Output	t_{SKEW}			5	10	ns
Driver Enable to Output High	t_{ZH}	Figure 4 & 6 $C_L=100pF$		40	70	ns
Driver Enable to Output Low	t_{ZL}			40	70	ns
Driver Disable Time from High	t_{HZ}	Figure 4 & 6 $C_L=15pF$		40	70	ns
Driver Disable Time from Low	t_{LZ}			40	70	ns
Receiver Input to Output	t_{PLH}	Figure 2 & 7 S_1, S_2 open $C_L = 100pF$	20	120	200	ns
	t_{PHL}		20	120	200	ns
$t_{PLH} - t_{PHL}$ Differential Receiver Skew	t_{SKD}			5	10	ns
Receiver Enable to Output Low	t_{ZL}	Figure 2 & 8 $C_L = 15pF$		50	90	ns
Receiver Enable to Output High	t_{ZH}			50	90	ns
Receiver Disable Time from Low	t_{LZ}			40	80	ns
Receiver Disable Time from High	t_{HZ}			40	80	Ns

TEST CIRCUITS

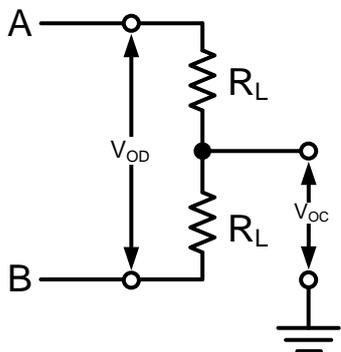


Figure 1.

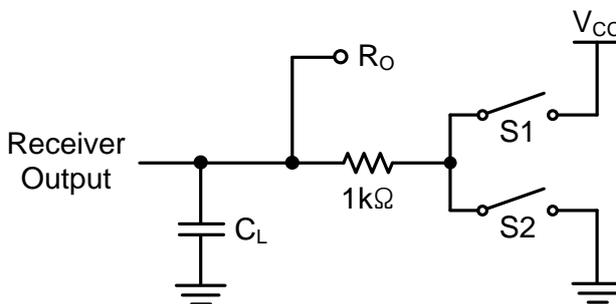


Figure 2.

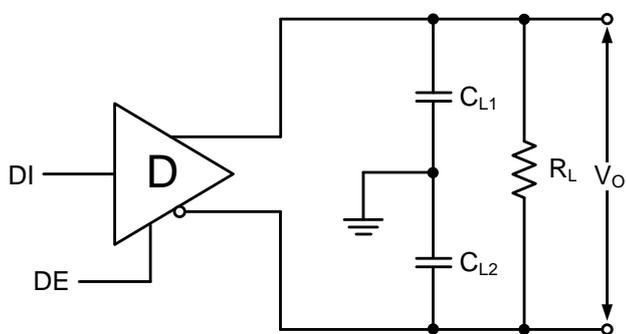


Figure 3.

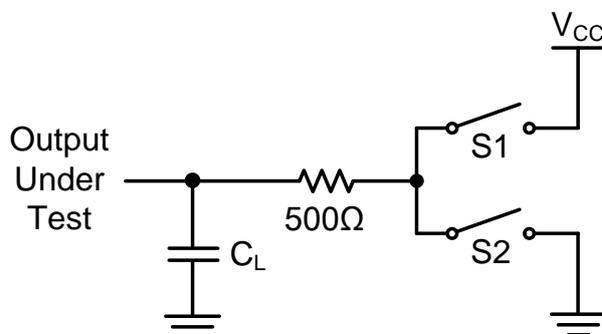


Figure 4.

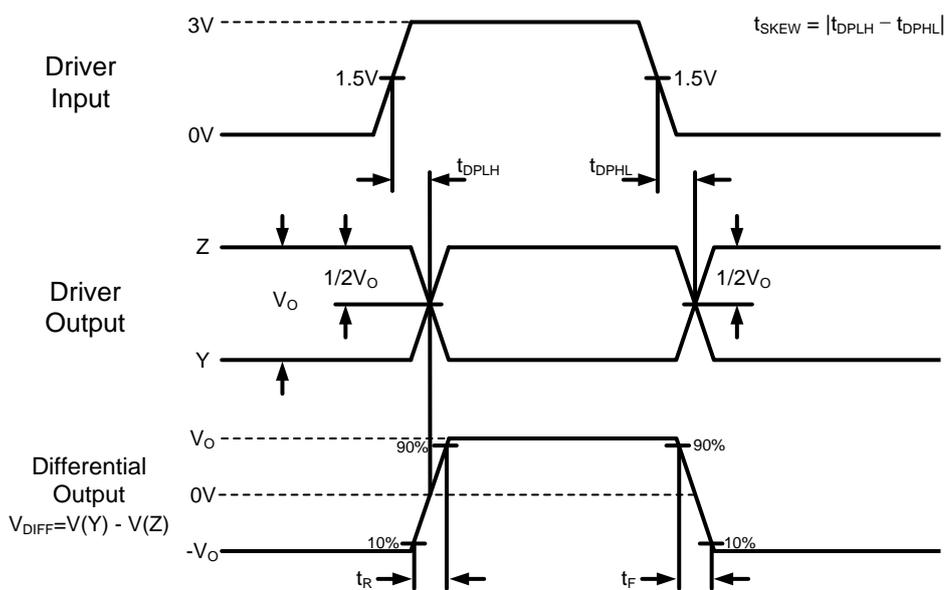


Figure 5.

TEST CIRCUITS

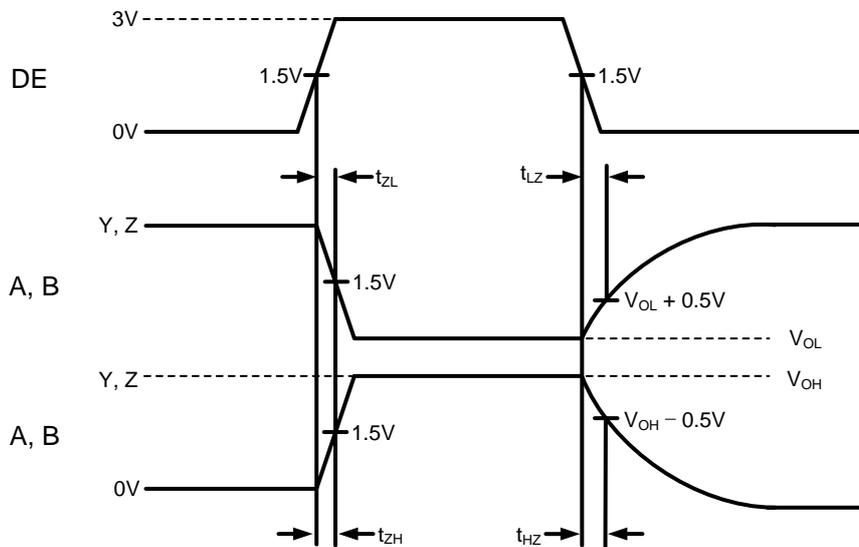


Figure 6.

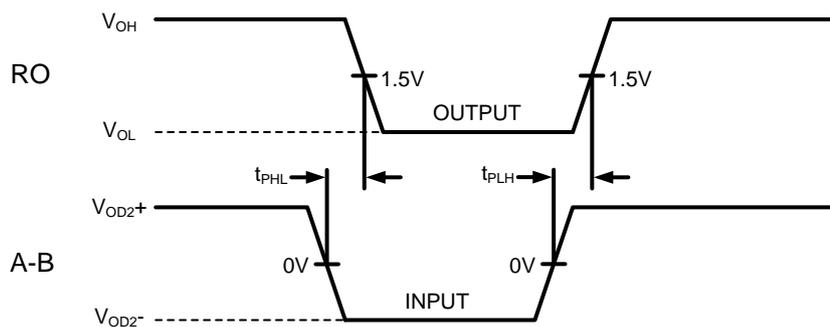


Figure 7.

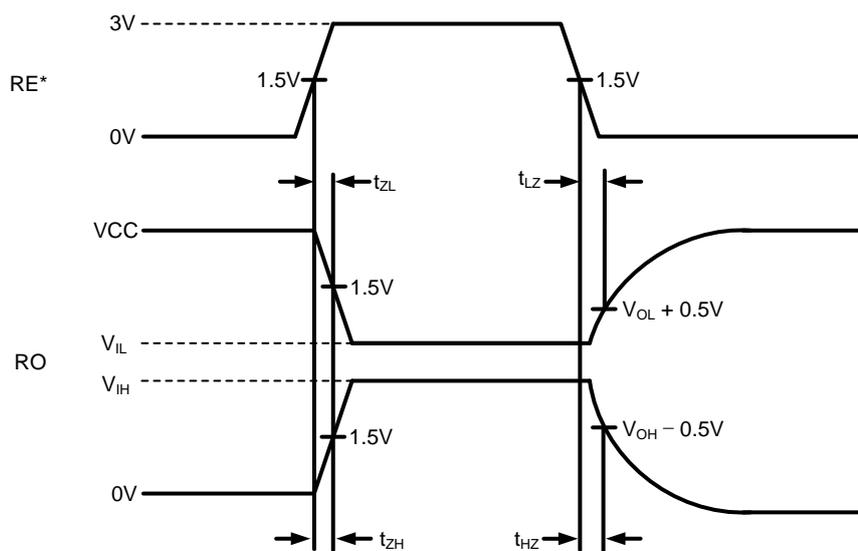


Figure 8.

APPLICATION INFORMATION

FUNCTIONAL DESCRIPTION

The TJ3485 is half-duplex differential transceiver that meets the requirements of RS-485 and RS-422.

The RS-485 standard is ideal for multi-drop applications and for long-distance interfaces. RS-485 allows up to 32 drivers and 32 receivers to be connected to a data bus, making it an ideal choice for multi-drop applications. Since the cabling can be as long as 4,000 feet, RS-485 transceivers are equipped with a wide (-7V to +12V) common mode range to accommodate ground potential differences. Because RS-485 is a differential interface, data is virtually immune to noise in the transmission line.

DRIVERS

The driver outputs of the TJ3485 are differential outputs meeting the RS-485 and RS-422 standards. The typical voltage output swing with no load will be 0 Volts to +3.3 Volts. With worst case loading of 54 Ω across the differential outputs, the drivers can maintain greater than 0.8V voltage levels. The drivers of the TJ3485 have an enable control line which is active HIGH. A logic HIGH on DE (pin 3) will enable the differential driver outputs. A logic LOW on the DE(pin 3) will tri-state the driver output.

The transmitters of the TJ3485 will operate up to at least 5Mbps.

RECEIVERS

The TJ3485 receiver has differential inputs with an input sensitivity as low as $\pm 200\text{mV}$. Input impedance of the receivers is greater than 12k Ω . A wide common mode range of -7V to +12V allows for large ground potential differences between systems. The receivers of the TJ3485 have a tri-state enable control pin. A logic LOW on RE* (pin 2) will enable the receiver, a logic HIGH on RE*(pin 2) will disable the receiver

The receiver for the TJ3485 will operate up to at least 5Mbps. The receiver is equipped with the fail-safe feature. Fail-safe guarantees that the receiver output will be in a HIGH state when the input is left unconnected.