

Silicon Diffused Power Transistor**BU4508AX****GENERAL DESCRIPTION**

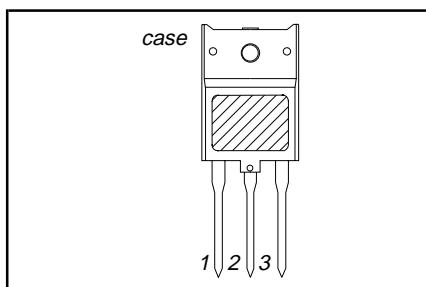
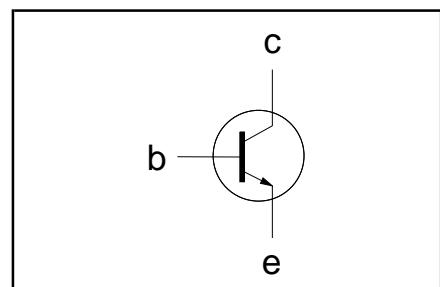
Enhanced performance, new generation, high-voltage, high-speed switching npn transistor in a plastic full-pack envelope intended for use in horizontal deflection circuits of colour television receivers and p.c. monitors. Features exceptional tolerance to base drive and collector current load variations resulting in a very low worst case dissipation.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V_{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0 \text{ V}$	-	1500	V
V_{CEO}	Collector-emitter voltage (open base)		-	800	V
I_C	Collector current (DC)		-	8	A
I_{CM}	Collector current peak value		-	15	A
P_{tot}	Total power dissipation	$T_{hs} \leq 25 \text{ }^{\circ}\text{C}$	-	45	W
V_{CEsat}	Collector-emitter saturation voltage	$I_C = 5.0 \text{ A}; I_B = 1.25 \text{ A}$	-	3.0	V
I_{Csat}	Collector saturation current	$f = 16\text{kHz}$	5.0	-	A
t_f	Fall time	$f = 64\text{kHz}$	4.0	-	A
		$I_{Csat} = 5\text{A}; f = 16\text{kHz}$	0.35	0.48	μs
		$I_{Csat} = 4\text{A}; f = 64\text{kHz}$	0.17	-	μs

PINNING - SOT399

PIN	DESCRIPTION
1	base
2	collector
3	emitter
case	isolated

PIN CONFIGURATION**SYMBOL****LIMITING VALUES**

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

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V_{CEO}	Collector-emitter voltage (open base)		-	800	V
I_C	Collector current (DC)		-	8	A
I_{CM}	Collector current peak value		-	15	A
I_B	Base current (DC)		-	4	A
I_{BM}	Base current peak value		-	6	A
$-I_{BM}$	Reverse base current peak value ¹		-	5	A
P_{tot}	Total power dissipation	$T_{hs} \leq 25 \text{ }^{\circ}\text{C}$	-	45	W
T_{stg}	Storage temperature		-55	150	$^{\circ}\text{C}$
T_j	Junction temperature		-	150	$^{\circ}\text{C}$

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$R_{th,j-hs}$	Junction to heatsink	with heatsink compound	-	2.8	K/W
$R_{th,j-a}$	Junction to ambient	in free air	35	-	K/W

¹ Turn-off current.

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ISOLATION LIMITING VALUE & CHARACTERISTIC $T_{hs} = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{isol}	Repetitive peak voltage from all three terminals to external heatsink	R.H. $\leq 65\%$; clean and dustfree	-	-	2500	V
C_{isol}	Capacitance from T2 to external heatsink	$f = 1 \text{ MHz}$	-	22	-	pF

STATIC CHARACTERISTICS $T_{hs} = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{CES}	Collector cut-off current ²	$V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax}$	-	-	1.0	mA
I_{CES}		$V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax}$	-	-	2.0	mA
I_{EBO}	Emitter cut-off current	$T_j = 125^\circ\text{C}$	-	-	100	μA
BV_{EBO}	Emitter-base breakdown voltage	$V_{EB} = 6.0 \text{ V}; I_C = 0 \text{ A}$	7.5	13.5	-	V
$V_{CEO}sust$	Collector-emitter sustaining voltage	$I_B = 1 \text{ mA}$	800	-	-	V
V_{CESat}	Collector-emitter saturation voltages	$I_B = 0 \text{ A}; I_C = 100 \text{ mA}; L = 25 \text{ mH}$	-	-	3.0	V
V_{BESat}	Base-emitter saturation voltage	$I_C = 5.0 \text{ A}; I_B = 1.25 \text{ A}$	0.85	0.94	1.03	V
h_{FE}	DC current gain	$I_C = 5.0 \text{ A}; I_B = 1.25 \text{ A}$	-	12	-	
h_{FE}		$I_C = 100 \text{ mA}; V_{CE} = 5 \text{ V}$	4.2	5.7	7.3	
		$I_C = 5.0 \text{ A}; V_{CE} = 5 \text{ V}$				

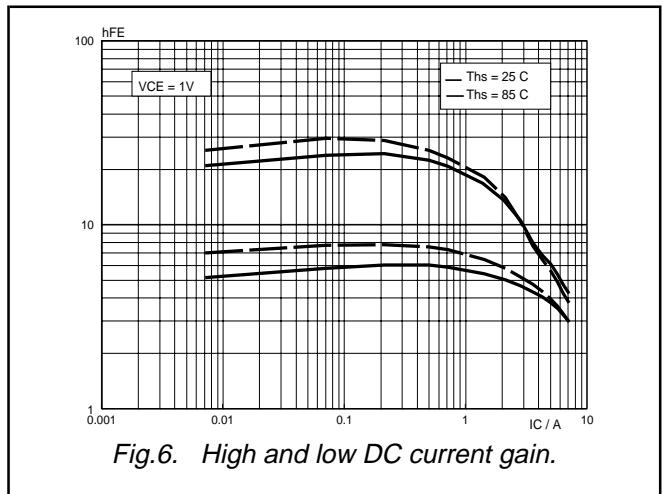
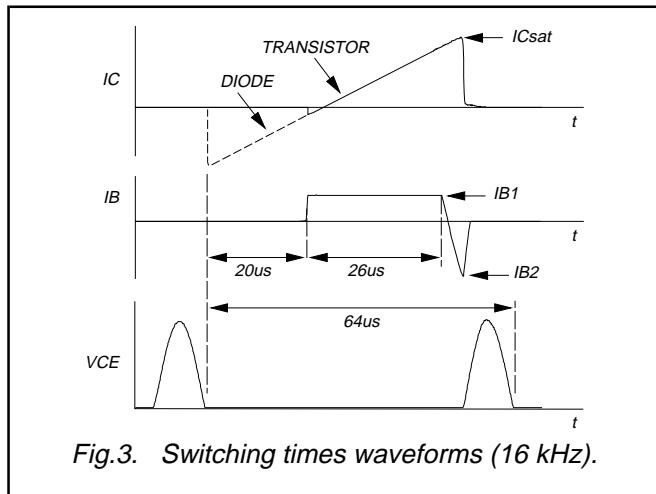
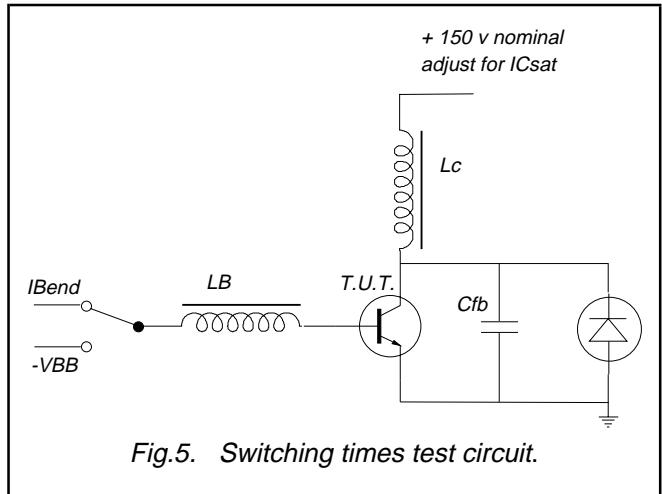
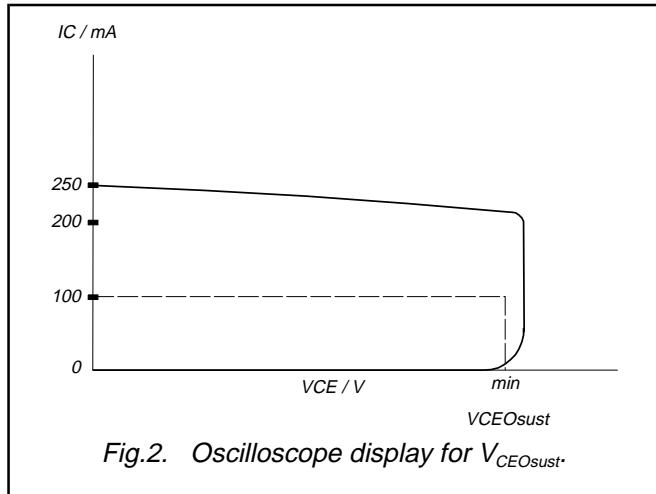
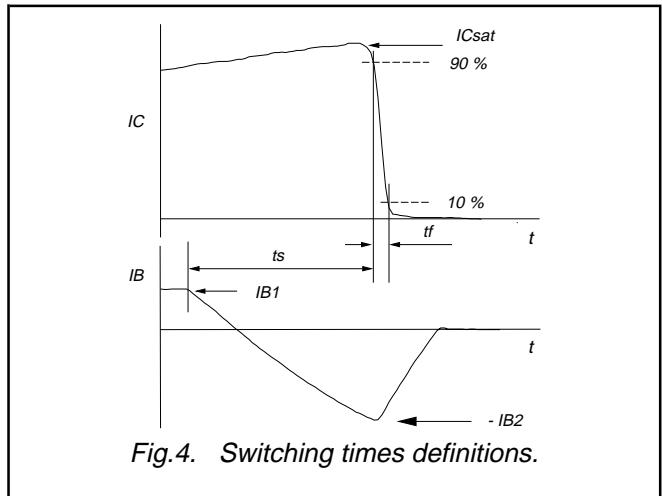
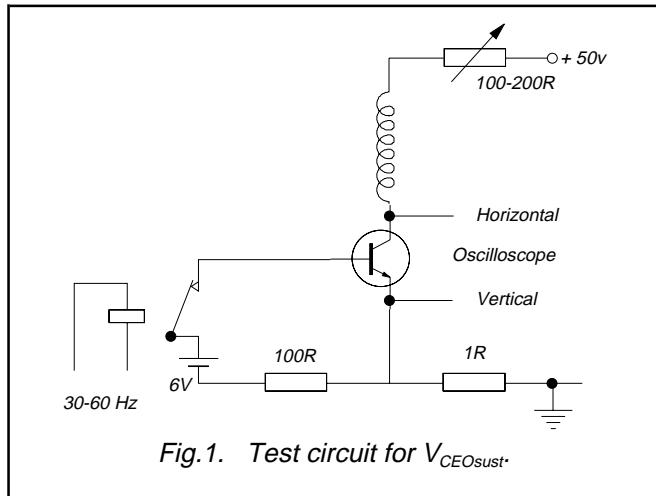
DYNAMIC CHARACTERISTICS $T_{hs} = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
C_c	Collector capacitance	$I_E = 0 \text{ A}; V_{CB} = 10 \text{ V}; f = 1 \text{ MHz}$	80	-	pF
t_s	Switching times (16 kHz line deflection circuit)	$I_{Csat} = 5.0 \text{ A}; I_{B1} = 1.0 \text{ A}$ $(I_{B2} = -2.5 \text{ A})$			
t_f	Turn-off storage time Turn-off fall time		3.2 0.35	4.3 0.48	μs μs
t_s	Switching times (64 kHz line deflection circuit)	$I_{Csat} = 4.0 \text{ A}; I_{B1} = 0.8 \text{ A}$ $(I_{B2} = -2.0 \text{ A})$			
t_f	Turn-off storage time Turn-off fall time		1.9 0.17	-	μs μs

² Measured with half sine-wave voltage (curve tracer).

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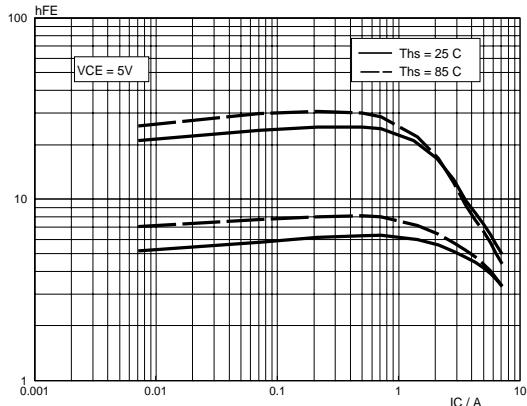


Fig.7. High and low DC current gain.

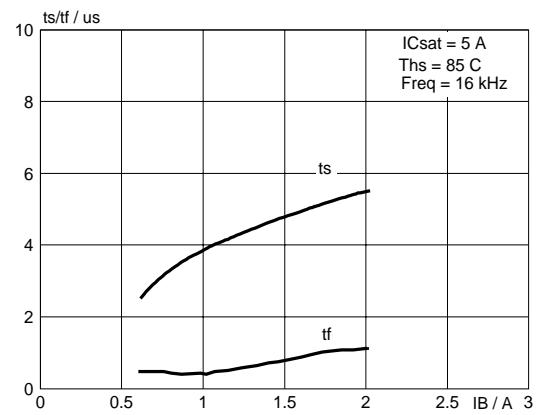
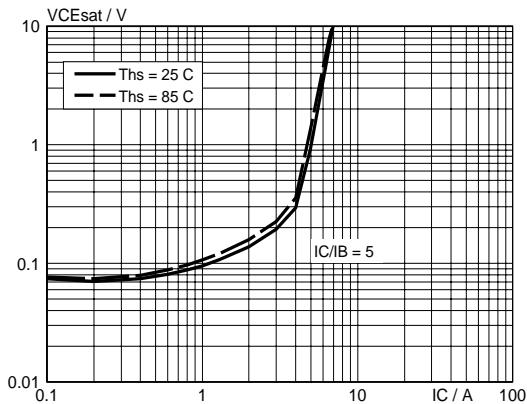
Fig.10. Typical collector storage and fall time.
 $I_C = 5 \text{ A}$; $T_j = 85^\circ\text{C}$; $f = 16\text{kHz}$ 

Fig.8. Typical collector-emitter saturation voltage.

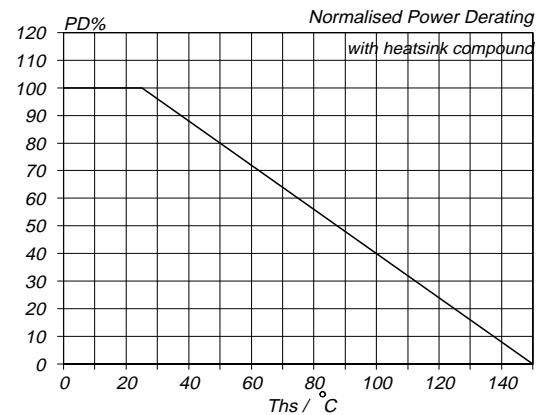
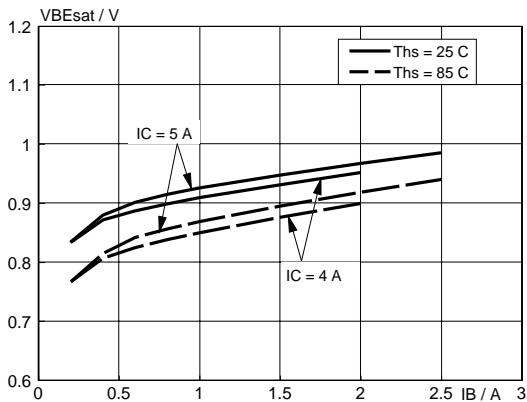
Fig.11. Normalised power dissipation.
 $PD\% = 100 \cdot P_D / P_{D, 25^\circ\text{C}}$ 

Fig.9. Typical base-emitter saturation voltage.

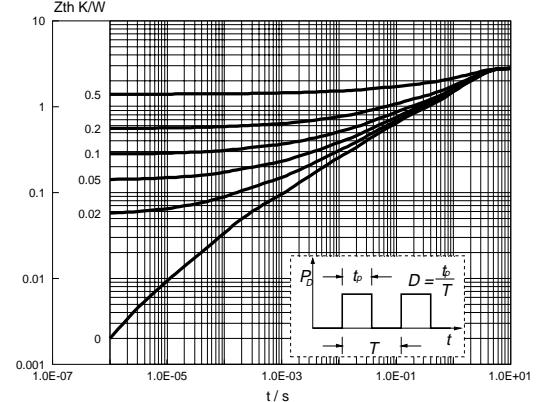


Fig.12. Transient thermal impedance.

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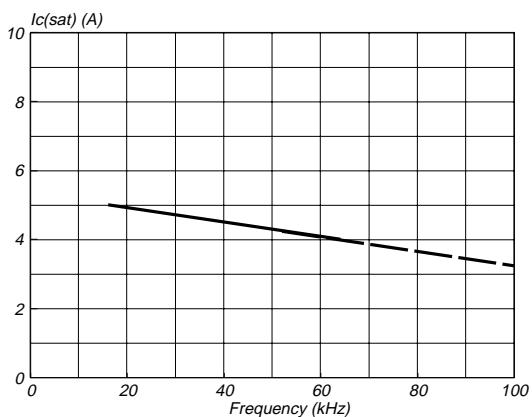


Fig.13. $I_{C(sat)}$ during normal running vs. frequency of operation for optimum performance

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MECHANICAL DATA*Dimensions in mm*

Net Mass: 5.88 g

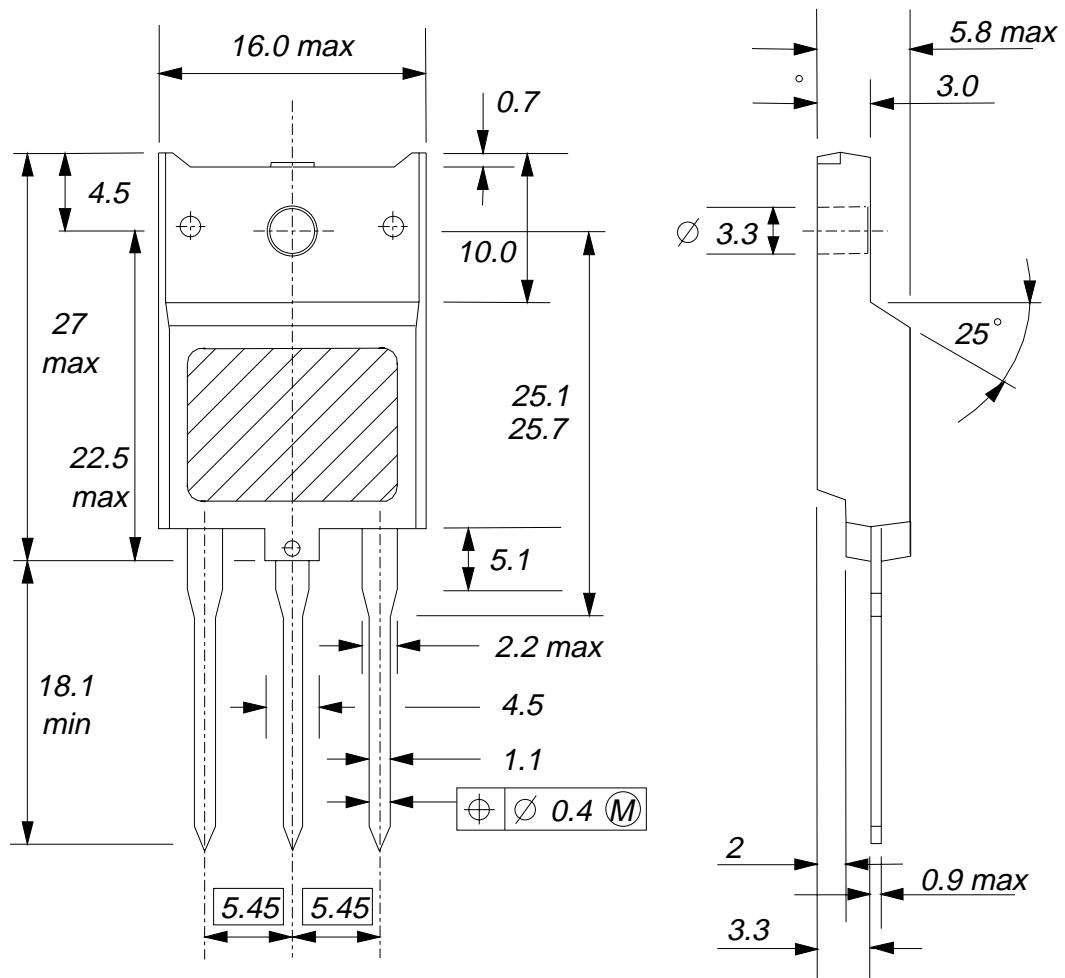


Fig.14. SOT399; The seating plane is electrically isolated from all terminals.

Notes

1. Refer to mounting instructions for F-pack envelopes.
2. Epoxy meets UL94 V0 at 1/8".