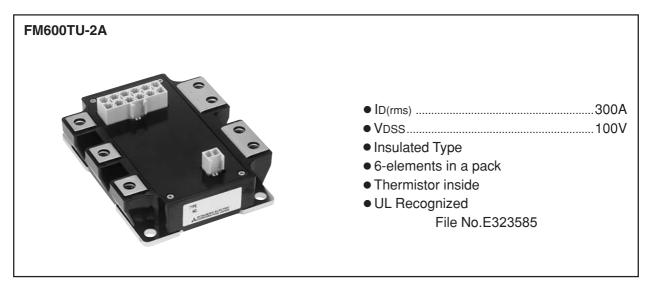
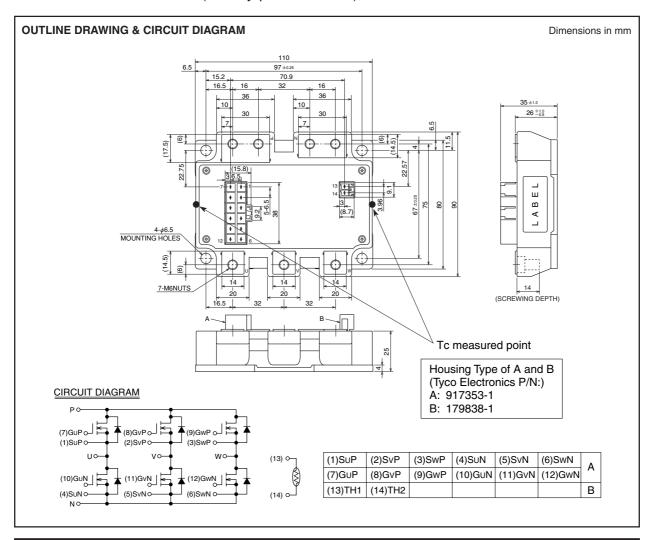
HIGH POWER SWITCHING USE INSULATED PACKAGE



### **APPLICATION**

AC motor control of forklift (battery power source), UPS



### **HIGH POWER SWITCHING USE INSULATED PACKAGE**

### ABSOLUTE MAXIMUM RATINGS (Tj = 25°C unless otherwise specified.)

Symbol	Item	Conditions	Rating	Unit
VDSS	Drain-source voltage	G-S Short	100	V
VGSS	Gate-source voltage	D-S Short	±20	V
ID	Drain current	Tc' = 133°C*3	300	Α
IDM	Drain current	Pulse*2	600	Α
IDA	Avalanche current	$L = 10\mu H Pulse^{*2}$	300	Α
Is*1	Source current		300	Α
Ism*1	Source current	Pulse*2	600	Α
PD*4	Maximum namer dissination	Tc = 25°C	960	W
Po*4	Maximum power dissipation	$Tc' = 25^{\circ}C^{*3}$	1300	W
Tch	Channel temperature		-40 ~ +150	°C
Tstg	Storage temperature		<b>−</b> 40 ~ +125	°C
Visol	Isolation voltage	Main terminal to base plate, AC 1 min, f=60Hz, RMS	2500	V
_	Mounting torque	Main Terminal M6	3.5 ~ 4.5	N∙m
		Mounting to heat sink M6	3.5 ~ 4.5	N∙m
_	Weight	Typical value	600	g

### **ELECTRICAL CHARACTERISTICS** (Tj = $25^{\circ}$ C unless otherwise specified.)

Cumphal	Item	Conditions		Limits			Unit	
Symbol				Min.	Тур.	Max.	Unit	
IDSS	Drain cutoff current	VDS = VDSS, VGS = 0V		_	_	1	mA	
VGS(th)	Gate-source threshold voltage	ID = 30mA, VDS = 10V		4.7	6	7.3	V	
Igss	Gate leakage current	VGS = VGSS, VDS = 0V		_	_	1.5	μΑ	
rDS(on)	Static drain-source	ID = 300A		_	0.8	1.1	0	
(chip)	On-state resistance	VGS = 15V	Tj = 125°C	_	1.37	_	mΩ	
VDS(on)	Static drain-source	ID = 300A	Tj = 25°C	_	0.24	0.33	V	
(chip)	On-state voltage	VGS = 15V	Tj = 125°C	_	0.41	_		
RDD'-SS'	Internal lead resistance	ID = 300A	Tj = 25°C	_	0.7	_	mΩ	
UDD:-99.		terminal-chip	Tj = 125°C	_	1.0	_		
Ciss	Input capacitance	VDS = 10V VGS = 0V VDD = 48V, ID = 300A, VGS = 15V		_	_	110	nF	
Coss	Output capacitance			_	_	15		
Crss	Reverse transfer capacitance			_	_	10		
QG	Total gate charge			_	1800	_	nC	
td(on)	Turn-on delay time	$VDD = 48V, ID = 300A, VGS1 = VGS2 = 15V$ $RG = 4.2\Omega, Inductive load switching operation$ $IS = 300A$		_	_	400	- ns	
tr	Rise time			_	_	600		
td(off)	Turn-off delay time			_	_	600		
tf	Fall time			_	_	400		
trr*1	Reverse recovery time			_	_	250	ns	
Qrr*1	Reverse recovery charge			_	6.2	_	μС	
Vsp*1	Source-drain voltage	Is = 300A, VGS = 0V		_	_	1.3	V	
Rth(j-c)	Thermal resistance	MOSFET part (1/6 module)*7		_	_	0.13	K/W	
Rth(j-c')	Thermal resistance	MOSFET part (1/6 module)*3		_	_	0.096		
Rth(c-s)	Contact thermal resistance	Case to fin, Thermal grease Applied*8 (1/6 module)		_	0.1	_	'`	
Rth(c'-s')	Case to fin, Thermal grease Applied*3, *8 (1/6 module)		_	0.09	_			

### NTC THERMISTOR PART

Symbol	Parameter	Conditions	Limits			Unit
			Min.	Тур.	Max.	Unit
R25*6	Resistance	$TTH = 25^{\circ}C^{*5}$	_	100	_	kΩ
B*6	B Constant	Resistance at TTH = 25°C, 50°C*5	_	4000	_	K

<sup>\*7:</sup> To measured point is shown in page OUTLINE DRAWING. \*8: Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9 W/(m·K).



<sup>\*1:</sup> It is characteristics of the anti-parallel, source to drain free-wheel diode (FWDi).
\*2: Pulse width and repetition rate should be such that the device junction temperature (Tj) does not exceed Tj max rating.

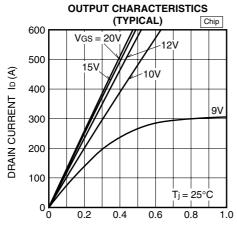
<sup>\*3:</sup> Tc' measured point is just under the chips. If use this value, Rth(s-a) should be measured just under the chips. \*4: Pulse width and repetition rate should be such as to cause negligible temperature rise.

<sup>\*5:</sup> TTH is thermistor temperature.

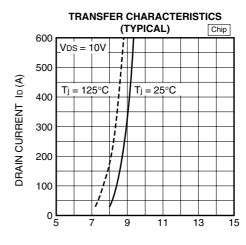
<sup>\*6:</sup> B = (InR1-InR2)/(1/T1-1/T2) R1: Resistance at T1(K), R2: Resistance at T2(K)

HIGH POWER SWITCHING USE INSULATED PACKAGE

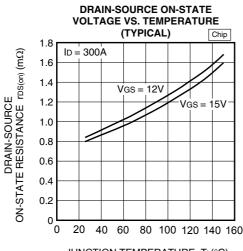
#### **PERFORMANCE CURVES**



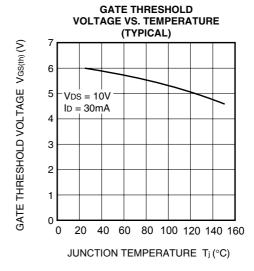
DRAIN-SOURCE VOLTAGE VDS (V)

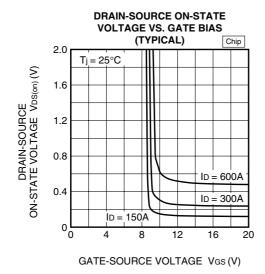


GATE-SOURCE VOLTAGE Vgs (V)



JUNCTION TEMPERATURE  $T_{j}$  (°C)

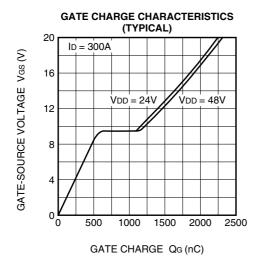


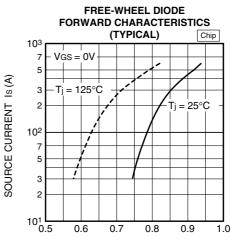


DRAIN-SOURCE VOLTAGE VDS (V)

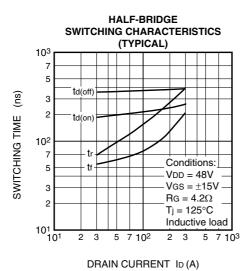


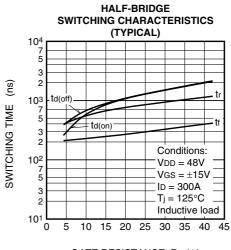
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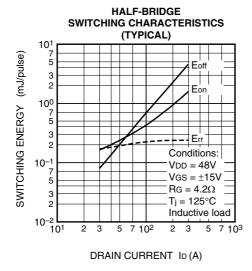


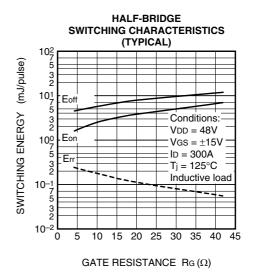
SOURCE-DRAIN VOLTAGE VSD (V)





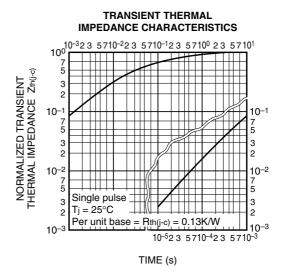
GATE RESISTANCE  $Rg(\Omega)$ 



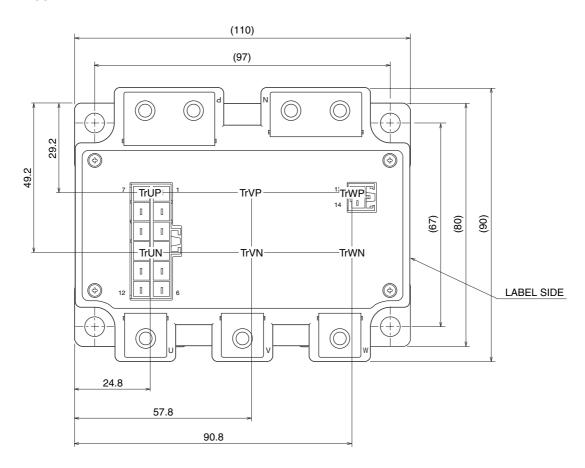


HIGH POWER SWITCHING USE INSULATED PACKAGE

#### **REVERSE RECOVERY CHARACTERISTICS** OF FREE-WHEEL DIODE (TYPICAL) 10<sup>3</sup> 7 5 3 10<sup>2</sup> 7 5 Irr (A), trr (ns) Irr 3 Conditions: 10<sup>1</sup> VDD = 48V $VGS = \pm 15V$ $RG = 4.2\Omega$ 3 $T_j = 25^{\circ}C$ Inductive load 100 L 2 5 7 10<sup>3</sup> 2 3 5 7 102 SOURCE CURRENT Is (A)



### **CHIP LAYOUT**



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