

## T-1(3mm) BI-COLOR INDICATOR LAMP

Part Number: L-3VEGW

High Efficiency Red

### **Features**

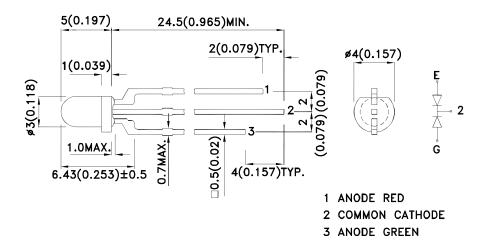
- Uniform light output.
- Low power consumption.
- 3 leads with one common lead.
- Long life solid state reliability.
- RoHS compliant.

### Description

The High Efficiency Red source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Orange Light Emitting Diode.

The Green source color devices are made with Gallium Phosphide Green Light Emitting Diode.

## **Package Dimensions**



- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is  $\pm 0.25(0.01")$  unless otherwise noted.
- 3. Lead spacing is measured where the leads emerge from the package.4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

SPEC NO: DSAA6515 **REV NO: V.15** DATE: APR/20/2010 **PAGE: 1 OF 7** APPROVED: WYNEC **CHECKED: Allen Liu** DRAWN: Z.Q.NI ERP: 1101003928





### **Selection Guide**

| Part No. | Dice                            | Lens Type       | lv (mcd) [2]<br>@ 20mA |      | Viewing<br>Angle [1] |
|----------|---------------------------------|-----------------|------------------------|------|----------------------|
|          |                                 | 2.              | Min.                   | Тур. | 201/2                |
| L-3VEGW  | High Efficiency Red (GaAsP/GaP) | WHITE DIFFLICED | 10                     | 40   | 60°                  |
|          | Green (GaP)                     | WHITE DIFFUSED  | 10                     | 35   |                      |

- Notes: 1.  $\theta$ 1/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value. 2. Luminous intensity/ luminous Flux: +/-15%.

## Electrical / Optical Characteristics at TA=25°C

| Symbol | Parameter                | Device                       | Тур.       | Max.       | Units | Test Conditions |
|--------|--------------------------|------------------------------|------------|------------|-------|-----------------|
| λpeak  | Peak Wavelength          | High Efficiency Red<br>Green | 627<br>565 |            | nm    | Ir=20mA         |
| λD [1] | Dominant Wavelength      | High Efficiency Red<br>Green | 625<br>568 |            | nm    | IF=20mA         |
| Δλ1/2  | Spectral Line Half-width | High Efficiency Red<br>Green | 45<br>30   |            | nm    | I==20mA         |
| С      | Capacitance              | High Efficiency Red<br>Green | 15<br>15   |            | pF    | VF=0V;f=1MHz    |
| VF [2] | Forward Voltage          | High Efficiency Red<br>Green | 2<br>2.2   | 2.5<br>2.5 | V     | I=20mA          |
| lr     | Reverse Current          | High Efficiency Red<br>Green |            | 10<br>10   | uA    | VR = 5V         |

### Notes:

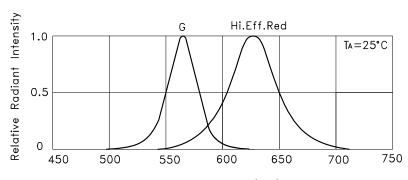
- 1.Wavelength: +/-1nm. 2. Forward Voltage: +/-0.1V.

## Absolute Maximum Ratings at TA=25°C

| Parameter                       | High Efficiency Red | Green | Units |  |  |
|---------------------------------|---------------------|-------|-------|--|--|
| Power dissipation               | 75                  | 62.5  | mW    |  |  |
| DC Forward Current              | 30                  | 25    | mA    |  |  |
| Peak Forward Current [1]        | 160                 | 140   | mA    |  |  |
| Reverse Voltage                 | 5                   |       |       |  |  |
| Operating / Storage Temperature | -40°C To +85°C      |       |       |  |  |
| Lead Solder Temperature [2]     | 260°C For 3 Seconds |       |       |  |  |
| Lead Solder Temperature [3]     | 260°C For 5 Seconds |       |       |  |  |

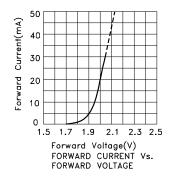
- Notes: 1. 1/10 Duty Cycle, 0.1ms Pulse Width.
- 2. 2mm below package base.
   5mm below package base.

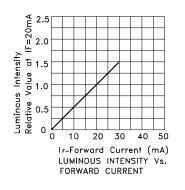
SPEC NO: DSAA6515 **REV NO: V.15** DATE: APR/20/2010 PAGE: 2 OF 7 APPROVED: WYNEC **CHECKED: Allen Liu** DRAWN: Z.Q.NI ERP: 1101003928

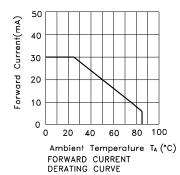


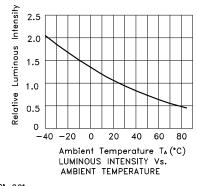
wavelength > (nm)
RELATIVE INTENSITY Vs. WAVELENGTH

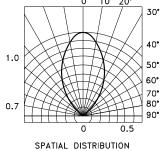
## L-3VEGW High Efficiency Red







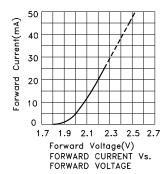


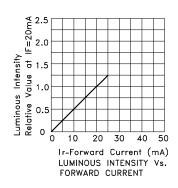


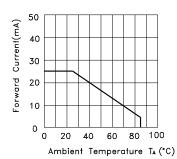
 SPEC NO: DSAA6515
 REV NO: V.15
 DATE: APR/20/2010
 PAGE: 3 OF 7

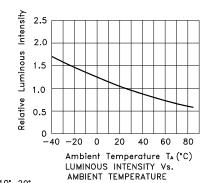
 APPROVED: WYNEC
 CHECKED: Allen Liu
 DRAWN: Z.Q.NI
 ERP: 1101003928

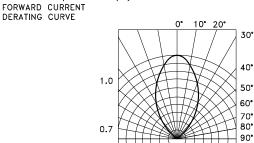
### Green







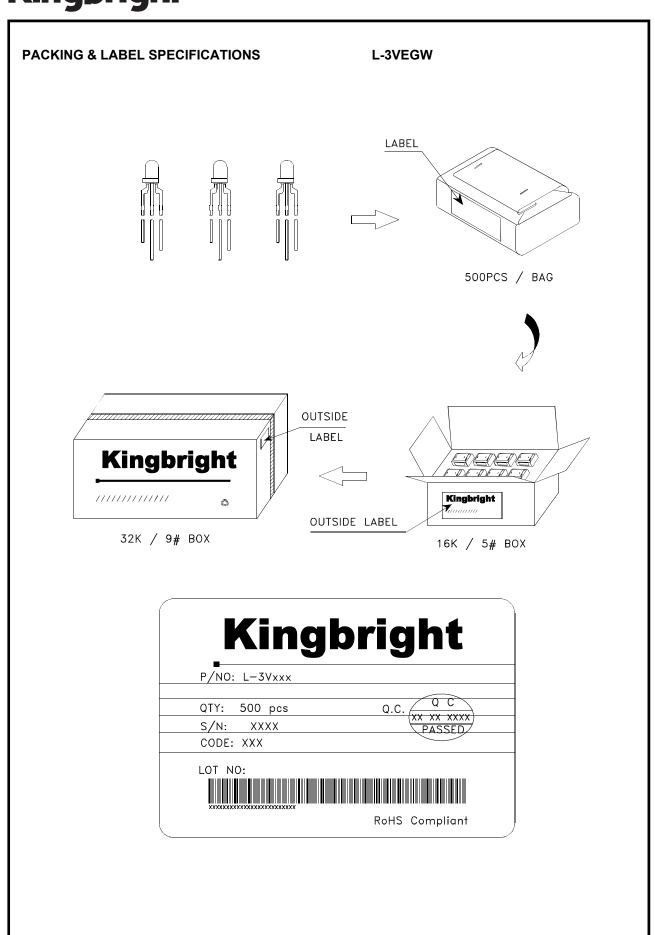




SPATIAL DISTRIBUTION

 SPEC NO: DSAA6515
 REV NO: V.15
 DATE: APR/20/2010
 PAGE: 4 OF 7

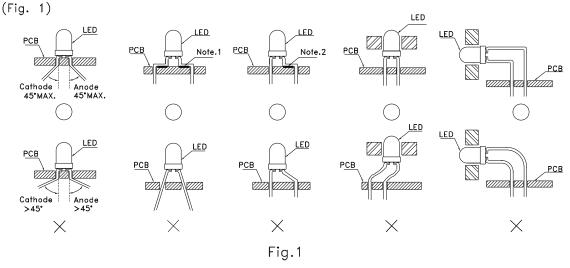
 APPROVED: WYNEC
 CHECKED: Allen Liu
 DRAWN: Z.Q.NI
 ERP: 1101003928



SPEC NO: DSAA6515 APPROVED: WYNEC REV NO: V.15 CHECKED: Allen Liu DATE: APR/20/2010 DRAWN: Z.Q.NI PAGE: 5 OF 7 ERP: 1101003928

### LED MOUNTING METHOD

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead—forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures.



" $\bigcirc$ " Correct mounting method " $\times$ " Incorrect mounting method Note 1-2: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

2. When soldering wire to the LED, use individual heat—shrink tubing to insulate the exposed leads to prevent accidental contact short—circuit. (Fig. 2)

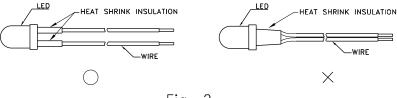
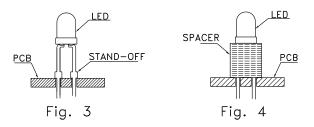


Fig. 2

3. Use stand—offs (Fig. 3) or spacers (Fig. 4) to securely position the LED above the PCB.

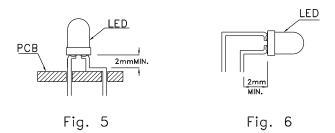


 SPEC NO: DSAA6515
 REV NO: V.15
 DATE: APR/20/2010
 PAGE: 6 OF 7

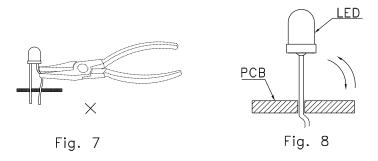
 APPROVED: WYNEC
 CHECKED: Allen Liu
 DRAWN: Z.Q.NI
 ERP: 1101003928

## LEAD FORMING PROCEDURES

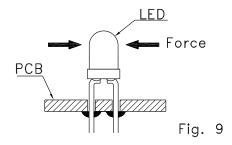
1. Maintain a minimum of 2mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)



- 2. Lead forming or bending must be performed before soldering, never during or after Soldering.
- 3. Do not stress the LED lens during lead—forming in order to fractures in the lens epoxy and damage the internal structures.
- 4. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)
- 5. Do not bend the leads more than twice. (Fig. 8)



6. After soldering or other high—temperature assembly, allow the LED to cool down to 50°C before applying outside force (Fig. 9). In general, avoid placing excess force on the LED to avoid damage. For any questions please consult with Kingbright representative for proper handling procedures.



 SPEC NO: DSAA6515
 REV NO: V.15
 DATE: APR/20/2010
 PAGE: 7 OF 7

 APPROVED: WYNEC
 CHECKED: Allen Liu
 DRAWN: Z.Q.NI
 ERP: 1101003928