



Non-isolated Buck APFC Offline LED Driver



晶丰明源半导体

BP2325AJ

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Ordering Information

Part Number	Package	Operating Temperature	Packing Method	Marking
BP2325AJ	SOP8	-40 °C to 105 °C	Tape 4,000 Pcs/Reel	BP2325A XXXXXY WXYJJ

Pin Configuration and Marking Information

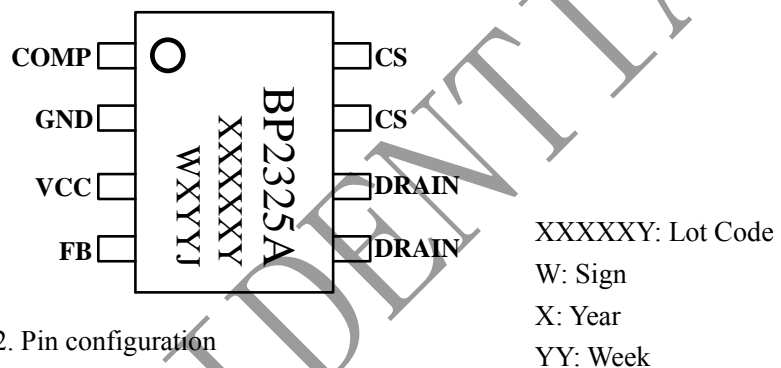


Figure 2. Pin configuration

Pin Definition

Pin No.	Name	Description
1	COMP	Loop Compensation Node. This pin connects a capacitor to GND for stabilization of the control loop, achieving accurate LED current, high Power Factor and low THD.
2	GND	Ground.
3	VCC	Power Supply Pin. Connect a bypass capacitor from this pin to GND.
4	FB	Feedback Voltage Input Pin. This pin detects the inductor demagnetization signal and the output voltage.
5,6	DRAIN	Internal HV Power MOSFET Drain.
7,8	CS	Current Sense Pin. Connect a resistor to GND to sense the inductor current.