

Emitter common (dual transistors)

UMS1N / FMS1A

●Features

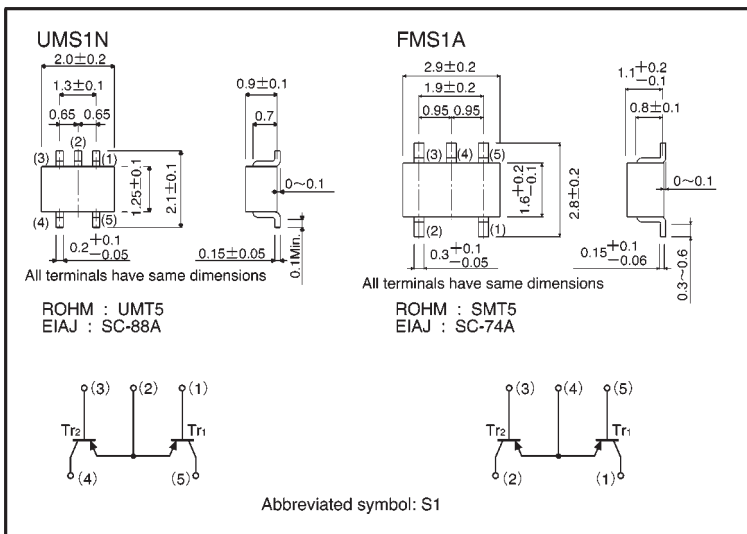
- 1) Two 2SA1037AK chips in a UMT or SMT package.
- 2) Mounting cost and area can be cut in half.

●Structure

Epitaxial planar type
PNP silicon transistor

The following characteristics apply to both Tr₁ and Tr₂.

●External dimensions (Units: mm)



●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	V_{CB0}	-60	V
Collector-emitter voltage	V_{CE0}	-50	V
Emitter-base voltage	V_{EB0}	-6	V
Collector current	I_C	-150	mA
Collector power dissipation	UMS1N	150 (TOTAL)	mW
	FMS1A	300 (TOTAL)	
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-55~+150	°C

*1 120mW per element must not be exceeded.

*2 200mW per element must not be exceeded.

●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV _{CBO}	−60	—	—	V	I _c = −50 μA
Collector-emitter breakdown voltage	BV _{CEO}	−50	—	—	V	I _c = −1mA
Emitter-base breakdown voltage	BV _{EBO}	−6	—	—	V	I _E = −50 μA
Collector cutoff current	I _{CBO}	—	—	−0.1	μA	V _{CB} = −60V
Emitter cutoff current	I _{EBO}	—	—	−0.1	μA	V _{EB} = −5V
Collector-emitter saturation voltage	V _{CE(sat)}	—	—	−0.5	V	I _c /I _B = −50mA/−5mA
DC current transfer ratio	h _{FE}	120	—	560	—	V _{CE} = −6V, I _c = −1mA
Transition frequency	f _T	—	140	—	MHz	V _{CE} = −12V, I _E = 2mA, f = 100MHz
Output capacitance	C _{ob}	—	3	4.5	PF	V _{CB} = −12V, I _E = 0A, f = 1MHz

●Packaging specifications

Part No.	Packaging type	Taping	
	Code	TR	T148
	Basic ordering unit (pieces)	3000	3000
UMS1N		○	—
FMS1A		—	○

●Electrical characteristic curves

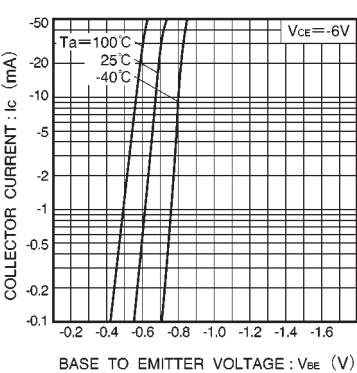


Fig.1 Grounded emitter propagation characteristics

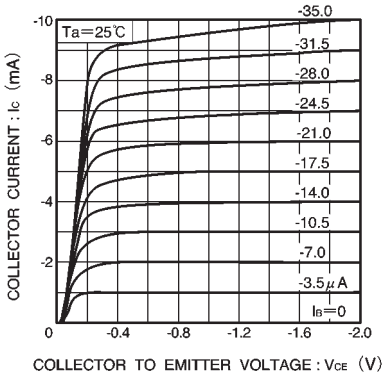


Fig.2 Grounded emitter output characteristics (I)

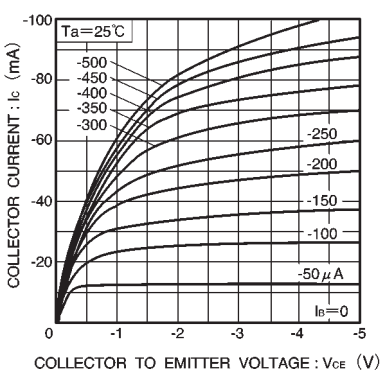


Fig.3 Grounded emitter output characteristics(II)

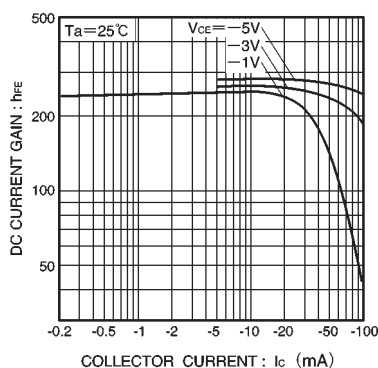


Fig.4 DC current gain vs. collector current (I)

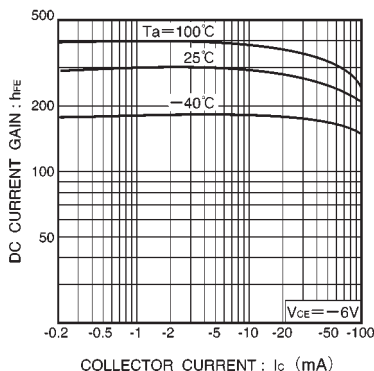


Fig.5 DC current gain vs. collector current (II)

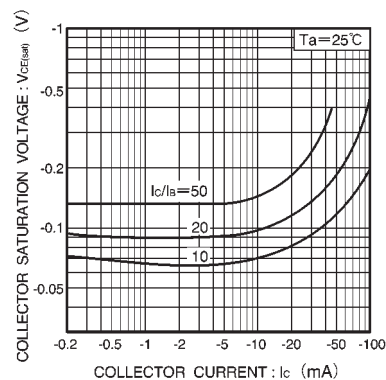


Fig.6 Collector-emitter saturation voltage vs. collector current (I)

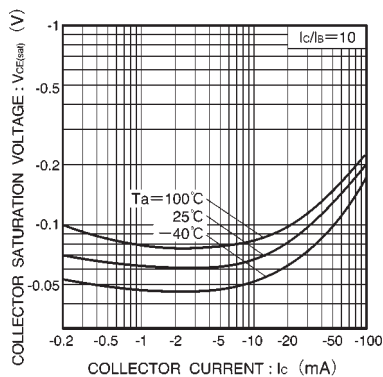


Fig.7 Collector-emitter saturation voltage vs. collector current (II)

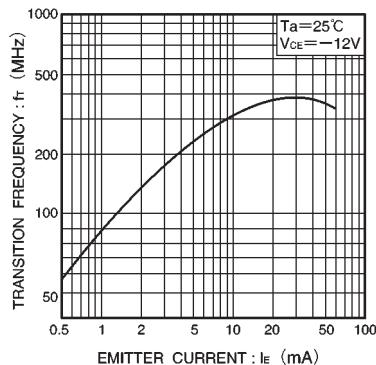


Fig.8 Gain bandwidth product vs. emitter current

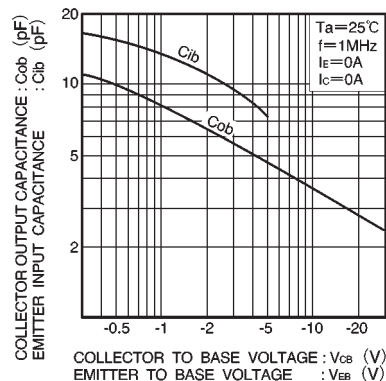


Fig.9 Collector output capacitance vs. collector-base voltage
Emitter input capacitance vs. emitter-base voltage