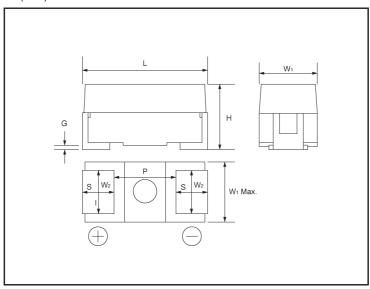
# Chip tantalum capacitors with built—in open—function TCFG series

Semiconductor manufacturing technology has been used to include a temperature fuse in TCFG series capacitors. These capacitors feature low impedance and are ideal for digital circuits and low–voltage circuits in portable electronic equipment.

#### Features

- 1) Open-function built into every package.
- 2) High capacitance in a small package.
- 3) Low impedance.
- 4) Use of semiconductor manufacturing technology provides high reliability.
- 5) Superb solderability.

#### External dimensions (mm)

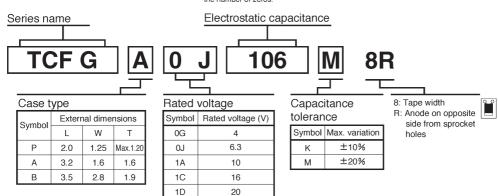


Case code	L	W <sub>1</sub>	W <sub>2</sub>	Н	S	G	Р
P (2012)	2.0±0.2	1.25±0.2	0.9±0.2	Max.1.20	0.45±0.3	Max. 0.15	1.3±0.3
A (3216)	3.2±0.2	1.6±0.2	1.2±0.2	1.6±0.2	0.8±0.3	Max. 0.15	1.6±0.3
B (3528)	3.5±0.2	2.8±0.2	1.9±0.2	1.9±0.2	0.8±0.3	Max. 0.15	1.9±0.3

#### Product designation

- · When ordering, please specify the part No.
- · Please check to be sure of what combination of features you wish to order.
- · Fill in the blanks from left to right.

Three-digit number indicates the number of picofarads. The first two digits are the significant digits; the last digit gives the number of zeros.



#### Capacitance range

#### TCFG series

	Rated voltage (V. DC)							
$\mu$ F	4	6.3	10	16	20			
	0G	0J	1A	1C	1D			
0.68								
1.0			Р	РΑ	Α			
1.5		Р	Α	Α				
2.2	Р	Р	Α	Α				
3.3	Р	РΑ	РΑ	ΑВ				
4.7	РΑ	РΑ	PAB	ΑВ				
6.8	PΑ	РΑ	ΑВ	ΑВ				
10	РΑ	PAB	ΑВ	В				
15	PAB	ΑВ	ΑВ					
22	ΑВ	ΑВ	В					
33	ΑВ	В	В					
47	В	В						
68	В							
100								

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### Characteristics

Item			Performance					Test methods / conditions (based on JIS C 5102, 5143)		
Operating temperature		-!	_55℃~+125℃							
Max. operating at rated voltage		+8	35℃							
Rated voltage (	V. DC)	4	4 6.3 10 16 20							
Derated voltage	e (V. DC)	2.5	2.5 4 6.3 10 13		at 125	5℃				
Surge voltage (	V. DC)	5.2	8 13							
Leakage currer	t	0.0	ss than )1CV. D andard:	etail	s ar	to the larger of 0.5 μA or e given in Table 1, List".		ured value 60s after ed voltage.	application	
Capacitance ra	nge	1.0	)~47 μ	F			Meas	ured frequency: 120 ured voltage: 0.5Vrn ured circuit: equivale t	ns+1.5V. DC	
tan δ	P case A case B case	1 µ	0.08 max. 1 $\mu$ F max.: 0.04 max. 1.5 $\mu$ F to 22 $\mu$ F max.: 0.06 max.				Meas	ured frequency: 120 ured voltage: 0.5Vrn ured circuit: equivale	ns+1.5V. DC	
Impedance	P case		.5Ω ma			ax 0.00 max.		ured frequency: 100	±10Hz	
	A case		.0Ω ma				Measured voltage: 0.5Vrms max.			
	B case	15	.0Ω ma	X.			Measured circuit: equivalent series circuit			
Resistance to solder heat	Appearance		No noticeable irregularities, and the markings must be easy to read.				Direct immersion into solder bath Solder bath temperature: 260±5℃			
	L.C	Ми	ıst satis	fy th	e ini	tial specified value.	Immersion time: 5s Immersion cycles: 1 time			
	△C/C		thin ±5 case=±		%			, or or of order in the		
	$\tan \delta$					less or initial specified tolerance. sfy the initial specified value.				
Fuse operation		32	0°C for £	20s (	or le	ss	Direct	t immersion into sold Ł5℃)	ler bath	
Temperature cycle	Appearance					ularities, and the easy to read.		our cycles in the tabl ted five times in suc		
	L.C					tial specified value. 0% of initial limit.	1	Temperature -55±3°C	Time 30±3mins.	
	△C/C		ase wit				3	Room temperature 125±2℃	3mins. max. 30±3mins.	
	$\tan\delta$		P case 1.5 times or less or initial specified tolerance.  A · B case must satisfy the initial specified value.				4	Room temperature	3mins. max.	
Resistance to humidity	Appearance		No noticeable irregularities, and the markings must be easy to read.			· · · · · · · · · · · · · · · · · · ·	±12h	ured after being left nrs. at 60±2℃ and 9	90 to 95%	
(steady state)	L.C	Mι	Must satisfy the initial specified value.			hen 1 to 2 hrs. at nor erature and humidity				
	△C/C		ase wit				15.11pt	a.o a.i.a iiaiiiaiiy	•	
	$\tan \delta$					less or initial specified tolerance. ofy the initial specified value.				



Iter	m	Performance	Test methods / conditions (based on JIS C 5102,5143)		
Temperature	Temperature	-55℃			
characteristics	△C/C	P case within +0% and -15% of the value before testing. A·B case within +10% and -0% of the value before testing.			
	$ an \delta$	P case within 1.5 times of the value before testing. A·B case must satisfy the initial specified value.			
	L.C	_			
	Temperature	+85°C			
	△C/C	P case within +0% and -15% of the value before testing. A $\cdot$ B case within +10% and -0% of the value before testing.			
	$\tan\delta$	Must satisfy the initial specified value.			
	L.C	Less than or equal to the larger of 5 $\mu$ A or 0.1CV.			
	Temperature	+125℃			
	△C/C	P case within +20% and -0% of the value before testing. A·B case within +15% and -0% of the value before testing.			
	tan ∂	P case within 1.5 times of the value before testing. A·B case must satisfy the initial specified value.			
	L.C	Less than or equal to the larger of 6.3 $\mu$ A or 0.125CV.			
Surge resistance	Appearance	A·B case no noticeable irregularities, and the markings must be easy to read.	Apply the rated surge voltage for 30 ±5s at intervals of 5±.05mins. 1000		
	L.C	Must satisfy the initial specified value.	times, with the temperature at 85±2°C		
	△C/C	P case within $\pm 10\%$ A·B case within $\pm 5\%$			
	tan δ	P case within 1.5 times of the value before testing. A·B case must satisfy the initial specified value.			
High- temperature	Appearance	No noticeable irregularities, and the markings must be easy to read.	Apply the rated voltage continuously fo 2000 to 2072hrs. via a series resister o		
load	L. C	Must satisfy the initial specified value.	$3\Omega$ max., then make the measurement after leaving the device at room		
	△C/C	Within ±10%	temperature and humidity for 1 to 2hrs.		
	tan δ	P case within 1.5 times of the value before testing. A·B case must satisfy the initial specified value.			
Terminal	Capacitance	Value must be stable during measurement.	Apply pressure to the device using the		
strength Appearance		No noticeable irregularities.	specified tool for 5s so that the center deflection is 1mm (see below).  (Units: mm) F (Direction of force)		

Ite	m	Performance	Test conditions
Adhesion		Terminals must not detach.	With the device mounted on the printed circuit board, apply a force of 0.5kg • f from each side for a period of 10±1s.  Device  Force  Printed circuit board
External dimen	sions	Refer to Fig. 1, "External dimensions"	Measure using slide calipers that meet the requirements of JIS B7507 Class 2.
Markings	Resistance to solvents	Marking must be easy to read.	Immerse in isopropyl alcohol for 30±5s.
Solderability Inspect the solder cover of the terminals using a solder immersion test		At least 3 / 4 of the surface of the immersed terminals must be covered with new solder.	Immersion speed: 25±2.5mm / s Pre-processing (accelerated aging): leave for 1hr over boiling distilled water Solder temperature: 235±5°C Immersion time: 2±0.5s Solder type: H63A Flux: rosin 25%, IPA 75%
Resistance to	Capacitance	Value must be stable during measurement.	Vibrate in the X / Y axis at frequencies of
vibration	Appearance	No noticeable irregularities.	10Hz, 55Hz and 10Hz for two hours each, with a total vibration amplitude of 1.5mm.
Reverse polarity	Appearance	No noticeable irregularities, and the markings must be easy to read.	Apply either 0.1 times the rated voltage, or 3V, whichever is smaller, via a series
withstanding L.C voltage		Must be less than or equal to twice the initial specified value.	resister of $3\Omega$ max. and $0.1\Omega$ min. at a temperature of $85\pm2^{\circ}$ C.
	△C/C	Within $\pm 10\%$ of the value before the test.	
	$\tan \delta$	Must be less than or equal to 1.5 times the initial specified value.	

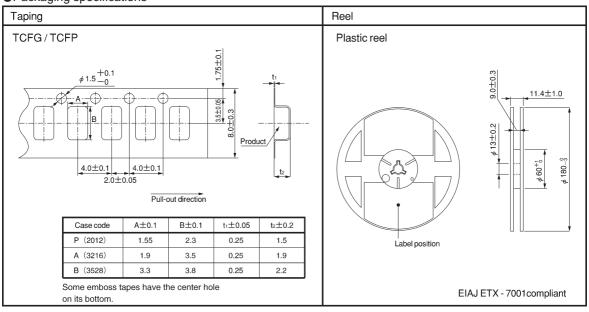


# ●Table 1 Standard parts list, TCFG series

Part No.	Rated voltage at 85°C	Derated voltage at 125°C	Surge voltage at 85°C	Capacitance	Tolerance	Leakage current at 25°C 1WV.60s	DF 120Hz 25°C
	(V)	(V)	(V)	(μF)	(%)	( μ A)	(%)
TCF GP 0G 225_	4	2.5	5.2	2.2	±20,10	0.5	8
TCF GP 0G 335_	4	2.5	5.2	3.3	±20,10	0.5	8
TCF GP 0G 475_	4	2.5	5.2	4.7	±20,10	0.5	6
TCF GA 0G 685_		2.5	5.2	6.8	±20,10	0.5	6
TCF GA 0G 106_	4	2.5	5.2	10	±20,10	0.5	6
TCF GA 0G 156_	4	2.5	5.2	15	±20,10	0.6	6
TCF GB 0G 156_	4	2.5	5.2	15	±20,10	0.6	6
TCF GA 0G 226_	4	2.5	5.2	22	±20,10	0.9	6
TCF GB 0G 226_	4	2.5	5.2	22	±20,10	0.9	6
TCF GB 0G 336_	4	2.5	5.2	33	±20,10	1.3	6
TCF GB 0G 476_	4	2.5	5.2	47	±20,10	1.9	6
TCF GP 0J 155_	6.3	4	8	1.5	±20,10	0.5	6
TCF GP 0J 225_	6.3	4	8	2.2	±20,10	0.5	6
TCF GA 0J 335_	6.3	4	8	3.3	±20,10	0.5	6
TCF GA 0J 475_	6.3	4	8	4.7	±20,10	0.5	6
TCF GA 0J 685_	6.3	4	8	6.8	±20,10	0.5	6
TCF GA 0J 106_	6.3	4	8	10	±20,10	0.6	6
TCF GA 0J 156_	6.3	4	8	15	±20,10	0.9	6
TCF GB 0J 106_	6.3	4	8	10	±20,10	0.6	6
TCF GB 0J 156_	6.3	4	8	15	±20,10	0.8	6
TCF GB 0J 226_	6.3	4	8	22	±20,10	1.4	6
TCF GB 0J 336_	6.3	4	8	33	±20,10	2.1	6
TCF GP 1A 105_	10	6.3	13	1.0	±20,10	0.5	8
TCF GA 1A 225_	10	6.3	13	2.2	±20,10	0.5	6
TCF GA 1A 335_	10	6.3	13	3.3	±20,10	0.5	6
TCF GA 1A 475_	10	6.3	13	4.7	±20,10	0.5	6
TCF GB 1A 475_	10	6.3	13	4.7	±20,10	0.5	6
TCF GA 1A 685_	10	6.3	13	6.8	±20,10	0.7	6
TCF GB 1A 685_	10	6.3	13	6.8	±20,10	0.7	6
TCF GA 1A 106_	10	6.3	13	10	±20,10	1.0	6
TCF GB 1A 106_	10	6.3	13	10	±20,10	1.0	6
TCF GB 1A 156_	10	6.3	13	15	±20,10	1.5	6
TCF GA 1C 105_	16	10	20	1.0	±20,10	0.5	4
TCF GA 1C 155_	16	10	20	1.5	±20,10	0.5	6
TCF GA 1C 225	16	10	20	2.2	±20,10	0.5	6

Part No.	Rated voltage at 85°C	Derated voltage at 125℃	Surge voltage at 85°C	Capacitance	Tolerance	Leakage current at 25°C 1WV.60s	DF 120Hz 25℃
	(V)	(V)	(V)	(μF)	(%)	$(\mu A)$	(%)
TCF GA 1C 335_	16	10	20	3.3	±20,10	0.5	6
TCF GB 1C 335_	16	10	20	3.3	±20,10	0.5	6
TCF GB 1C 475_	16	10	20	4.7	±20,10	0.8	6
TCF GB 1C 685_	16	10	20	6.8	±20,10	1.1	6
TCF GB 1C 106_	16	10	20	10	±20,10	1.6	6
TCF GA 1D 105_	20	16	26	1.0	±20,10	0.5	4

# Packaging specifications

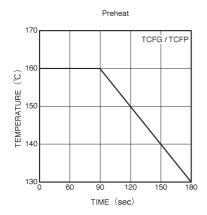


# Packaging style

Part no.	Package type	Packag	ing style	Symbol	Basic ordering unit (pcs)
TCFG	Taping	Plastic taping	φ 180 mm reel	R	2,000
TCFP	Taping	Plastic taping	φ 180 mm reel	R	2,000

#### Electrical characteristics and operation notes

#### (1) Soldering conditions (soldering temperature and soldering time)



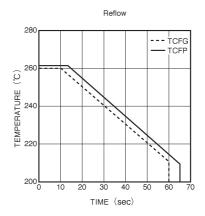


Fig.1 Reflow (Infrared Ray, Hot Plate, Hot Air)

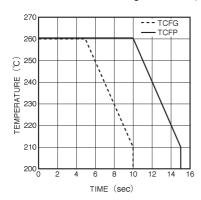


Fig.2 Flow (Dipping wave soldering)

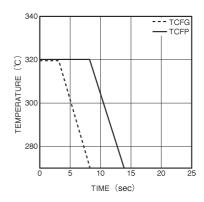


Fig.3 Hand soldering (soldering gun output: 30W or less)

#### (2) Leakage current-to-voltage ratio

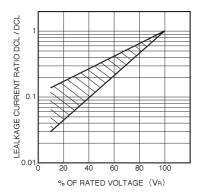
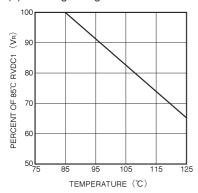


Fig.4

#### (3) Derating voltage as function of temperature



85	C	125°C		
Rated Voltage	Surge Voltage	Category Voltage	Surge Voltage	
(V.DC)	(V.DC)	(V.DC)	(V.DC)	
4	5.2	2.5	3.4	
6.3	8	4	5	
10	13	6.3	9	
16	20	10	12	
20	26	13	16	

Fig.5

#### (4) Reliability

The malfunction rate of tantalum solid state electrolytic capacitors varies considerably depending on the conditions of usage (ambient temperature, applied voltage, circuit resistance).

#### Formula for calculating malfunction rate

 $\lambda p = \lambda b \times (\pi_E \times \pi_{SR} \times \pi_Q \times \pi_{CV})$ 

λ<sub>P</sub>: Malfunction rate stemming from operation

 $\lambda_b$ : Basic malfunction rate  $\pi_E$ : Environmental factors  $\pi_{SR}$ : Series resistance

 $\pi_Q$ : Level of malfunction rate

πcv: Capacitance

For details on how to calculate the malfunction rate stemming from operation, see the tantalum solid state electrolytic capacitors column in MIL-HDBK-217.

# Malfunction rate as function of operating temperature and rated voltage

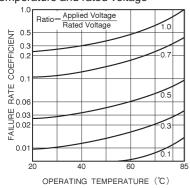


Fig.6

#### Malfunction rate as function of circuit resistance ( $\Omega$ /V)

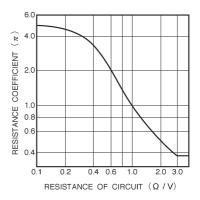
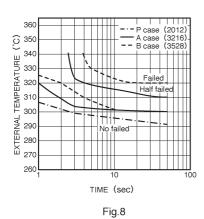


Fig.7

#### (5) External temperature vs. fuse blowout



Note: Solder the chip at  $300^{\circ}$  C or less. If it is soldered using a temperature higher than  $300^{\circ}$  C, the built–in temperature fuse may blow out.

#### (7) Maximum power dissipation

Warming of the capacitor due to ripple voltage balances with warming caused by Joule heating and by radiated heat. Maximum allowable warming of the capacitor is to  $5^{\circ}$ C above ambient temperature. When warming exceeds  $5^{\circ}$ C, it can damage the dielectric and cause a short circuit.

Power dissipation (P) =I<sup>2</sup> ■ R

Ripple current

P: As shown in table at right

R: Equivalent series resistance

#### (8) Impedance frequency characteristics

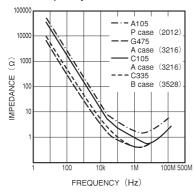


Fig.10

#### (6) Power vs. fuse blowout characteristics / Product surface temperature

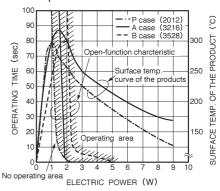


Fig.9

#### Notes:

- 1. Please be aware that when case size is changed, maximum allowable power dissipation is reduced.
- 2. Maximum power dissipation varies depending on the package. Be sure to use a case which will keep warming within the limits shown in the table below.

Allowable power dissipation (W) and maximum temperature rising

Ambient temp.	+25°C	+55°C	+85℃	+125°C
P case (2012)	0.025	0.022	0.020	0.010
A case (3216)	0.070	0.063	0.056	0.028
B case (3528)	0.080	0.072	0.064	0.032
Max. temp rise	5	5	5	2

#### (9) ESR frequency characteristics

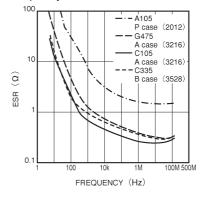


Fig.11

#### (10) Temperature characteristics

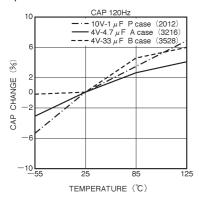


Fig.12

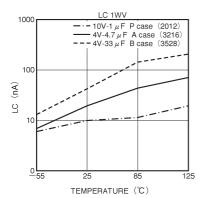


Fig.14 Inrush current

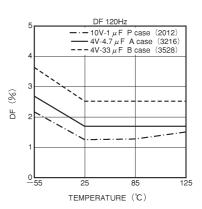


Fig.13

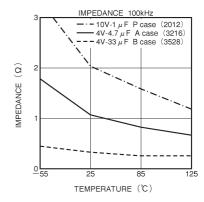


Fig.15

Inrush currents are inversely proportional to ESR. Large inrush currents can cause component failure.

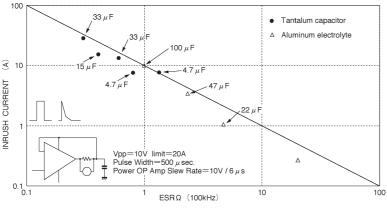


Fig.16 Maximum inrush current and ESR

Inrush current can be limited by means of a protective resistor.

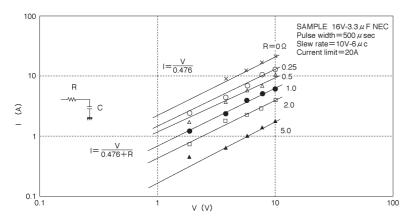


Fig.17 Imax change due to protective resistor R

#### (11) Ultrasonic cleaning

Carry out cleaning under the mildest conditions possible.

The internal element of a tantalum capacitor are larger

than those of a transistor or diode, so it is not as resistant

to ultrasonic waves.

150μm or less

G dimension: 150 μm or less

Example: water

Propagation speed Solvent density

1500 m/s 1g/cm<sup>3</sup>

Frequency and wavelength				
Frequency	Wavelength			
20kHz	7.5cm			
28kHz	5.3cm			
50kHz	3.0cm			

# Precautions

Design G dimension :

- 1) Do not allow solvent to come to a boil (kinetic energy increases).
  - · Ultrasonic output
- 0.5W/cm<sup>2</sup> or less
- · Use a solvent with a high boiling point.
- · Lower solvent temperature.
- 2) Ultrasonic cleaning frequency

28 kHz or less

- 3) Keep cleaning time as short as possible.
- 4) Move item being cleaned.

Standing waves caused by the ultrasonic waves can cause stress to build up in part of the item being cleaned.

#### Reference

Kinetic energy = 
$$2 \times \pi \times \text{frequency } \times \sqrt{\frac{2 \times \text{ultrasonic output}}{\text{propagation speed x solvent density}}}$$