

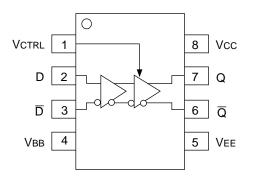
5V/3.3V VARIABLE OUTPUT SWING DIFFERENTIAL RECEIVER

SY100EL16VS FINAL

FEATURES

- 3.3V and 5V power supply options
- High bandwidth output transitions
- Internal 75K Ω pull-down resistors on inputs
- Functionally equivalent to SY100EL16V with variable output swing
- **■** Improved output waveform characteristics
- Available in 8-pin SOIC and 8-pin (3mm) MSOP

PIN CONFIGURATION/BLOCK DIAGRAM



DESCRIPTION

The SY100EL16VS are differential receivers with variable output swing. The devices are functionally equivalent to the EL16V devices with an input that control the amplitude of the outputs.

The operational range of the EL16VS control input is from VBB (max. swing) to VCC (min. swing). Simple control of the output swing can be obtained by a variable resistor between the VBB pin and VCC with the wiper driving VCTRL.

The EL16VS provides a VBB output for either single-ended use or as a DC bias for AC coupling to the device. The VBB pin should be used only as a bias for the EL16VS as its current sink/source capability is limited. Whenever used, the VBB pin should be bypassed to ground via a $0.01\mu F$ capacitor.

Under open input conditions (pulled to VEE), internal input clamps will force the Q output LOW.

PIN NAMES

Pin	Function
D	Data Inputs
Q	Data Outputs
Vвв	Reference Voltage Output
VCTRL	Output Swing Control

TYPICAL VOLTAGE OUTPUT SWING

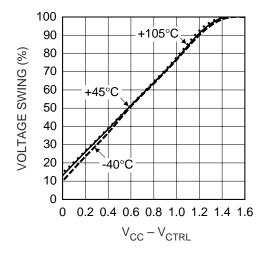


Figure 1. Typical Voltage Output Swing Vcc = 3.3V or 5V

Rev.: C Amendment:/0 Issue Date: February 2003

DC ELECTRICAL CHARACTERISTICS(1)

VEE = VEE (Min.) to VEE (Max.); VCC = GND

		T/	A = -40°	°C	7	ΓA = 0°C	;	T	A = +25°	C	T	4 = +85°	°C	
Symbol	Parameter	Min.	Тур.	Max.	Unit									
IEE	Power Supply Current	_	18	22	9	18	22	9	18	22	9	21	26	mA
Vвв	Output Reference Voltage	-1.38	_	-1.26	-1.38	_	-1.26	-1.38	_	-1.26	-1.38	_	-1.26	V
Іін	Input HIGH -D, \overline{D} Current -VCTRL	_	_	150 40	_		150 40	_	_	150 40			150 40	μА
Vol	Output LOW Voltage ⁽²⁾ VCTRL = VBB	-1890	_	-1620	-1870		-1680	-1870	-1775	-1680	-1870		-1680	mV
VoL	Output LOW Voltage ⁽²⁾ VCTRL = VCC	-1180		-975	-1135		-990	-1135	-1065	-990	-1135		-990	mV
Vон	Output HIGH Voltage ⁽³⁾	-1085	_	-880	-1025	_	-880	-1025	-955	-880	-1025	_	-880	mV

NOTES:

1. Parametric values specified at: 100EL16VS Series: -3.0V to -5.5V.

2. If VCTRL is an open circuit, use the VoH (max. & min.) and VoL (VCTRL = VBB: max only) limits.

3. $VCC \le VCTRL \le VEE$.

AC ELECTRICAL CHARACTERISTICS(1)

VEE = VEE (Min.) to VEE (Max.); VCC = GND

		Ta = -40°C		TA = 0°C			TA = +25°C			TA = +85°C				
Symbol	Parameter	Min.	Тур.	Max.	Unit									
tPLH tPHL	Propagation D (Diff) Delay to Output D (SE)	175 125	 250	325 425	175 125	 250	325 375	175 125	 250	325 375	205 155	 280	355 405	ps
tskew	Duty Cycle Skew ⁽²⁾ (Diff)	_	5	_	_	5	20	_	5	20	_	5	20	ps
VPP	Minimum Input Swing(3)	150	_	_	150	_	_	150	_	_	150	_	_	mV
Vcmr	Common Mode Range ⁽⁴⁾	-1.3	_	-0.4	-1.4	_	-0.4	-1.4	_	-0.4	-1.4	_	-0.4	V
tr tf	Output Rise/Fall Times Q (20% to 80%)		160	260	_	160	260	_	160	260	_	160	260	ps

NOTES:

1. Parametric values specified at: 100EL16VS Series: -3.0V to -5.5V.

- 2. Duty cycle skew is the difference between a tPLH and tPHL propagation delay through a device.
- 3. Minimum input swing for which AC parameters are guaranteed. The device has a DC gain of ≈40 when output has a full swing.
- 4. The CMR range is referenced to the most positive side of the differential input signal. Normal operation is obtained if the HIGH level falls within the specified range and the peak-to-peak voltage lies between VPP min. and 1V. The lower end of the CMR range varies 1:1 with VEE. The numbers in the spec table assume a nominal VEE = -3.3V. Note for PECL operation, the VCMR (min) will be fixed at 3.3V |VCMR (min)|.

PRODUCT ORDERING CODE

Ordering Code	Package Type	Operating Range	Marking Code				
SY100EL16VSKC	K8-1	Commercial	XLEL16VS				
SY100EL16VSKCTR*	K8-1	Commercial	XLEL16VS				
SY100EL16VSZC	Z8-1	Commercial	XEL16VS				
SY100EL16VSZCTR*	Z8-1	Commercial	XEL16VS				

Ordering Code	Package Type	Operating Range	Marking Code		
SY100EL16VSKI ⁽¹⁾	K8-1	Industrial	XLEL16VS		
SY100EL16VSKITR*(1)	K8-1	Industrial	XLEL16VS		
SY100EL16VSZI ⁽¹⁾	Z8-1	Industrial	XEL16VS		
SY100EL16VSZITR*(1)	Z8-1	Industrial	XEL16VS		

Note 1. Recommended for new designs.

^{*}Tape and Reel

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APPLICATION IMPLEMENTATION

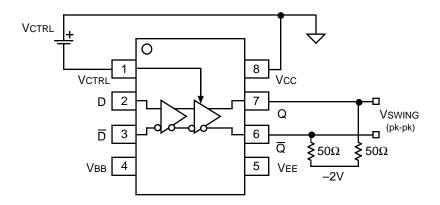


Figure 2. Voltage Source Implementation

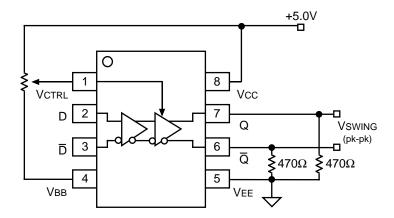
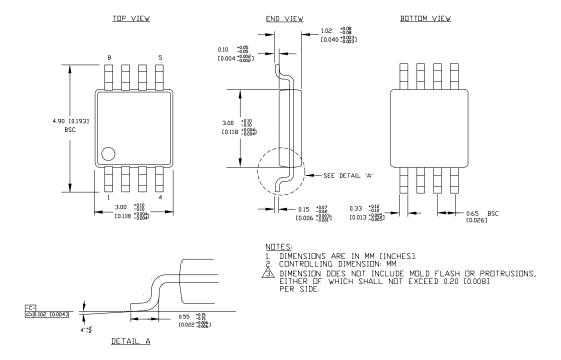
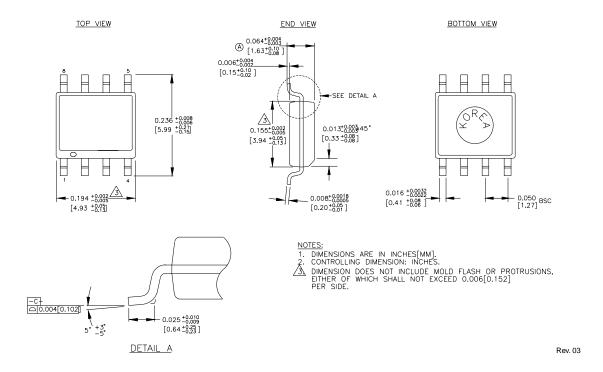


Figure 3. Alternative Implementation

8 LEAD MSOP (K8-1)



8 LEAD SOIC .150" WIDE (Z8-1)



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