

FEATURES

- Differential D and Q
- Extended 100E VEE range of $-4.2V$ to $-5.5V$
- 700ps max. propagation delay
- High frequency outputs
- Internal 75K Ω input pull-down resistors
- Fully compatible with Motorola 10E/100E404
- Available in 28-pin PLCC package

DESCRIPTION

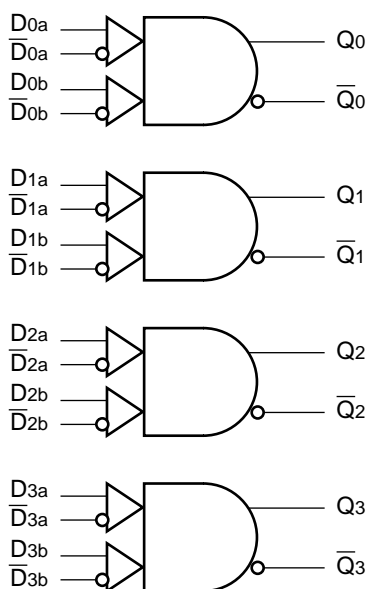
The SY10/100E404 are 4-bit differential AND/NAND devices. The differential operation of these devices make them ideal for pulse shaping applications where duty cycle skew is critical. Special design techniques were incorporated to minimize the skew between the upper and lower level gate inputs.

Because a negative 2-input NAND function is equivalent to a 2-input OR function, the differential inputs and outputs of the devices also allow for their use as fully differential 2-input OR/NOR functions.

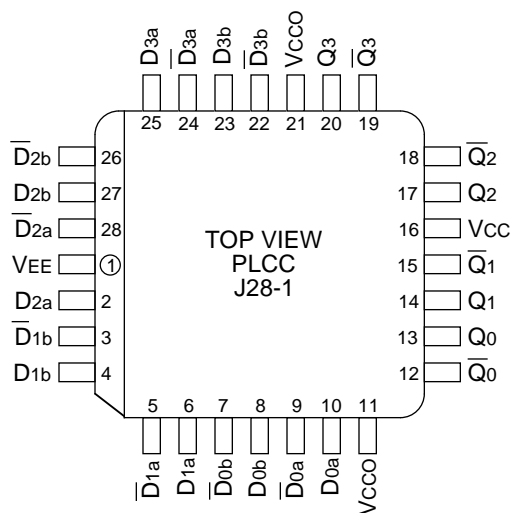
The output RISE/FALL times of these devices are significantly faster than most other standard ECLinPS devices, resulting in an increased bandwidth.

The differential inputs have clamp structures which will force the Q output of a gate in an open input condition to go to a LOW state. Thus, inputs of unused gates can be left open and will not affect the operation of the rest of the device.

BLOCK DIAGRAM



PIN CONFIGURATION



PIN NAMES

Pin	Function
D[0:4], $\bar{D}[0:4]$	Differential Data Inputs
Q[0:4], $\bar{Q}[0:4]$	Differential Data Outputs
Vcco	Vcc to Output

TRUTH TABLE

Da	Db	Q	\overline{Da}	\overline{Db}	\overline{Q}
L	L	L	L	L	L
L	H	L	L	H	H
H	L	L	H	L	H
H	H	H	H	H	H

DC ELECTRICAL CHARACTERISTICS

$V_{EE} = V_{EE} \text{ (Min.) to } V_{EE} \text{ (Max.)}; V_{CC} = V_{CCO} = \text{GND}$

Symbol	Parameter	$T_A = 0^\circ\text{C}$			$T_A = +25^\circ\text{C}$			$T_A = +85^\circ\text{C}$			Unit	Condition
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.		
I _{IH}	Input HIGH Current	—	—	150	—	—	150	—	—	150	μA	—
I _{EE}	Power Supply Current	—	—	—	—	—	—	—	—	—	mA	—
		10E	106	127	106	127	106	106	122	127		
		100E	106	127	106	127	106	106	122	146		
V _{PP} (DC)	Input Sensitivity	50	—	—	50	—	—	50	—	—	mV	1
V _{CMR}	Common Mode Range	−1.5	—	0	−1.5	—	0	−1.5	—	0	V	2

NOTES:

- Differential input voltage required to obtain a full ECL swing on the outputs.
- V_{CMR} is referenced to the most positive side of the differential input signal. Normal operation is obtained when the input signals are within the V_{CMR} range and the input swing is greater than V_{PP} (min.) and <1V.

AC ELECTRICAL CHARACTERISTICS

$V_{EE} = V_{EE} \text{ (Min.) to } V_{EE} \text{ (Max.)}; V_{CC} = V_{CCO} = \text{GND}$

Symbol	Parameter	$T_A = 0^\circ\text{C}$			$T_A = +25^\circ\text{C}$			$T_A = +85^\circ\text{C}$			Unit	Condition
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.		
t _{PLH} t _{PHL}	Propagation Delay to Output Da (Diff) Da (SE) Db (Diff) Db (SE)	350 300 375 325	475 475 500 500	650 700 675 725	350 300 375 325	475 475 500 500	650 700 675 725	350 300 375 325	475 475 500 500	650 700 675 725	ps	—
t _{skew}	Within-Device Skew	—	50	—	—	50	—	—	50	—	ps	1
V _{PP} (AC)	Minimum Input Swing	150	—	—	150	—	—	150	—	—	mV	2
t _r t _f	Rise/Fall Time 20–80%	150	—	400	150	—	400	150	—	400	ps	—

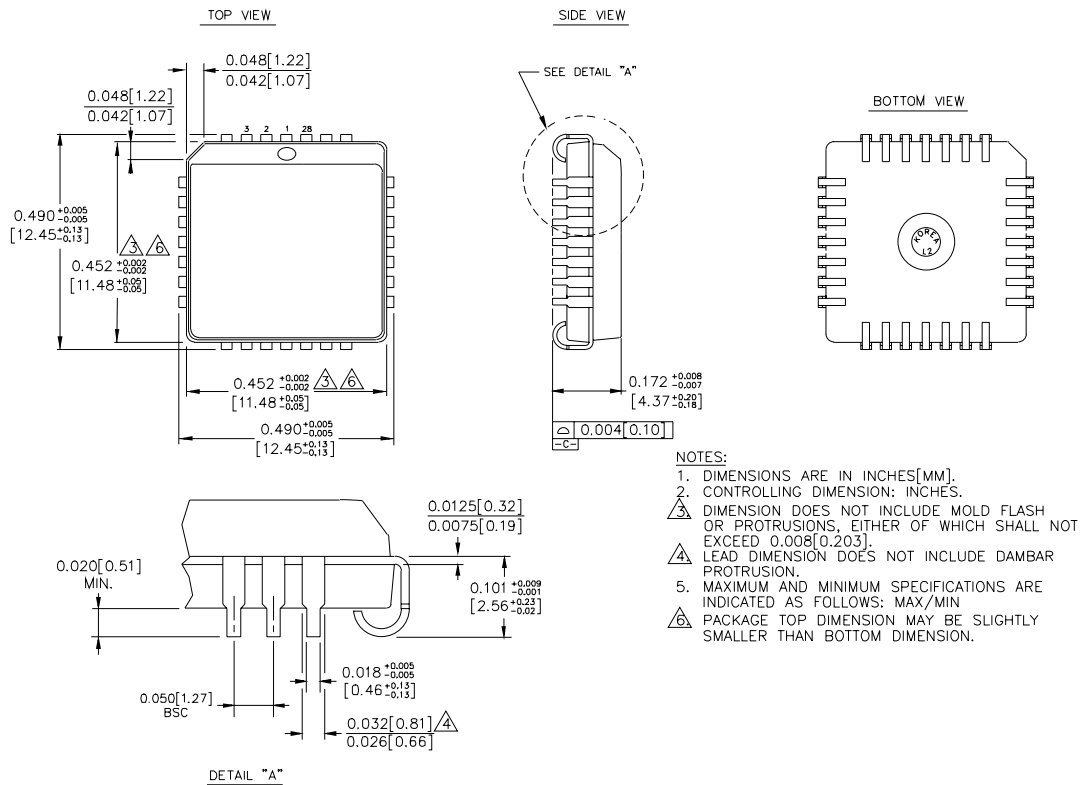
NOTES:

- Within-device skew is defined as identical transitions on similar paths through a device.
- Minimum input swing for which AC parameters are guaranteed.

PRODUCT ORDERING CODE

Ordering Code	Package Type	Operating Range
SY10E404JC	J28-1	Commercial
SY10E404JCTR	J28-1	Commercial
SY100E404JC	J28-1	Commercial
SY100E404JCTR	J28-1	Commercial

28 LEAD PLCC (J28-1)



Rev. 03

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