

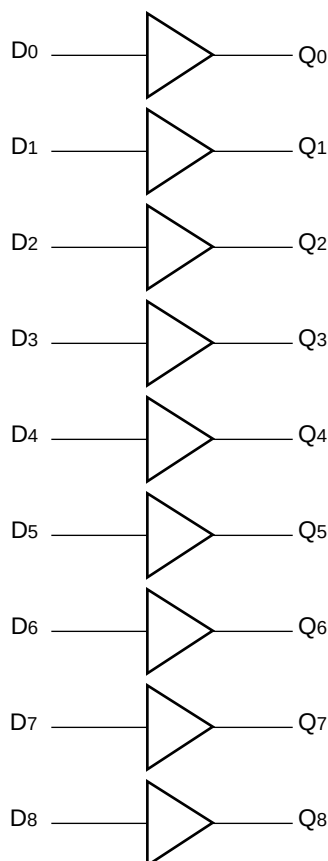
FEATURES

- 500ps max. propagation delay
- Extended 100E VEE range of -4.2V to -5.5V
- Fully compatible with industry standard 10KH, 100K I/O levels
- Internal 75K Ω input pulldown resistors
- Fully compatible with Motorola MC10E/100E122
- Available in 28-pin PLCC package

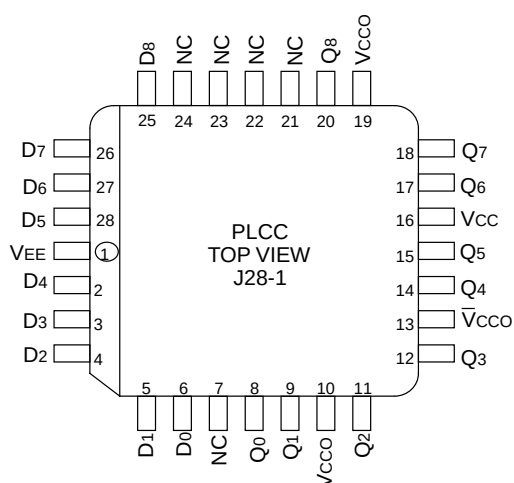
DESCRIPTION

The SY10/100E122 are 9-bit buffers designed for use in new, high-performance ECL systems. The E122 provides nine non-inverting buffers.

BLOCK DIAGRAM



PIN CONFIGURATION



PIN NAMES

Pin	Function
D0-D8	Data Inputs
Q0-Q8	Data Outputs
VCCO	Vcc to Output

DC ELECTRICAL CHARACTERISTICSV_{EE} = V_{EE} (Min.) to V_{EE} (Max.); V_{CC} = V_{CCO} = GND

Symbol	Parameter	T _A = -40°C			T _A = 0°C			T _A = +25°C			T _A = +85°C			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	
I _{IH}	Input HIGH Current	—	—	200	—	—	200	—	—	200	—	—	200	μA
I _{EE}	Power Supply Current													mA
	10E	—	41	49	—	41	49	—	41	49	—	41	49	
	100E	—	41	49	—	41	49	—	41	49	—	47	57	

AC ELECTRICAL CHARACTERISTICSV_{EE} = V_{EE} (Min.) to V_{EE} (Max.); V_{CC} = V_{CCO} = GND

Symbol	Parameter	T _A = -40°C			T _A = 0°C			T _A = +25°C			T _A = +85°C			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	
t _{PLH} t _{PHL}	Propagation Delay to D to Q	150	350	500	150	350	500	150	350	500	150	350	500	ps
t _{skew}	Within-Device Skew D to Q ⁽¹⁾	—	75	—	—	75	—	—	75	—	—	75	—	ps
t _r t _f	Rise/Fall Time 20% to 80%	300	425	800	300	425	800	300	425	800	300	425	800	ps

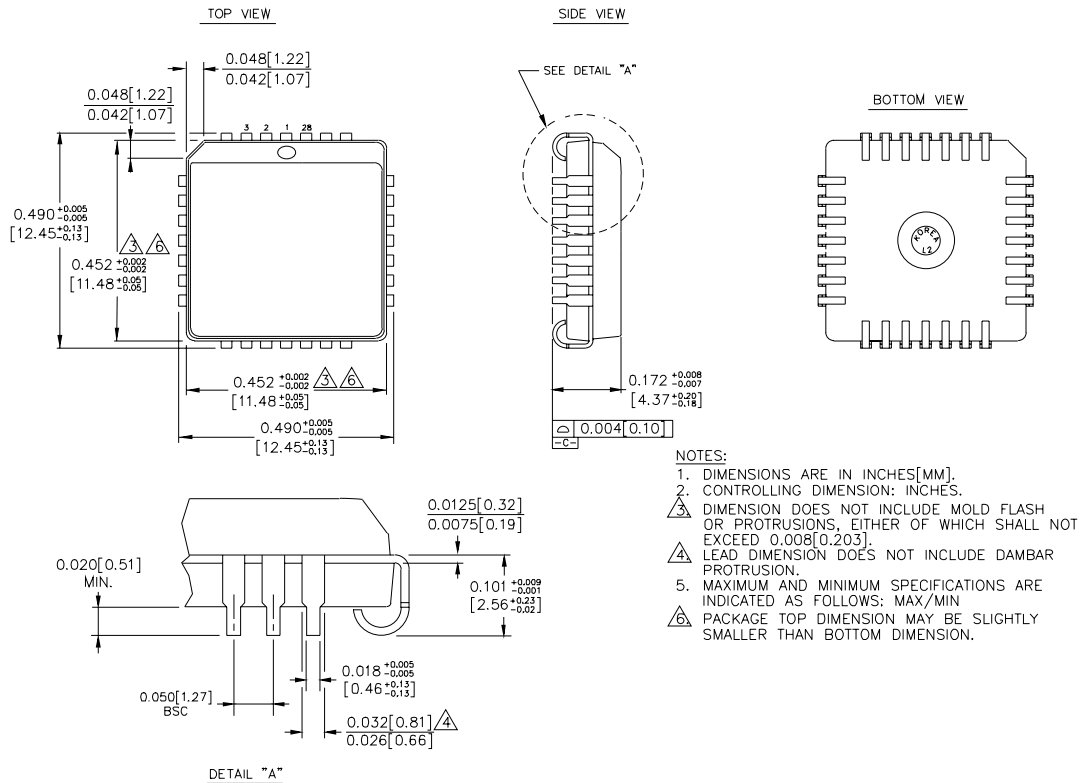
NOTE:

1. Within-device skew is defined as identical transitions on similar paths through a device.

PRODUCT ORDERING CODE

Ordering Code	Package Type	Operating Range	Ordering Code	Package Type	Operating Range
SY10E122JC	J28-1	Commercial	SY10E122JI	J28-1	Industrial
SY10E122JCTR	J28-1	Commercial	SY10E122JITR	J28-1	Industrial
SY100E122JC	J28-1	Commercial	SY100E122JI	J28-1	Industrial
SY100E122JCTR	J28-1	Commercial	SY100E122JITR	J28-1	Industrial

28 LEAD PLCC (J28-1)



Rev. 03

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