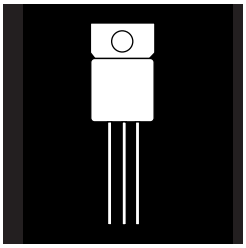
HERMETIC JEDEC TO-257AA HIGH EFFICIENCY, CENTER-TAP RECTIFIER



8 Amp, 150 To 1000V, 25 To 75 nS

FEATURES

- Very Low Forward Voltage
- Very Fast Recovery Time
- Hermetic Metal Package, JEDEC TO-257AA Outline
- Low Thermal Resistance
- Isolated Package
- High Surge

DESCRIPTION

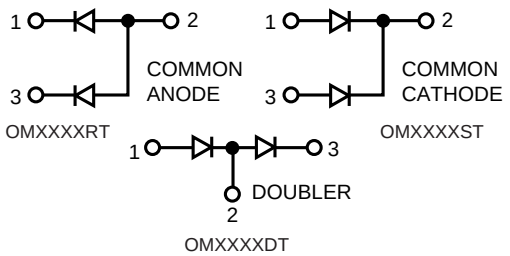
This series of products in a hermetic package is specifically designed for use at power switching frequencies in excess of 100 kHz. The series combines two high efficiency devices into one package, simplifying installation, reducing heat sink hardware, and the need to obtain matched components. These devices are ideally suited for Hi-Rel applications where small size and a hermetically sealed package is required. Common anode configurations are also available. Common cathode is standard.

ABSOLUTE MAXIMUM RATINGS (Per Diode) @ 25°C

Peak Inverse Voltage . . . . . 150, 600 & 1000 V
Maximum Average D.C. Output Current @ T_C = 100° C . . . . . 4 A
Surge Current (Non-Repetitive 8.3 msec) . . . . . 70 A

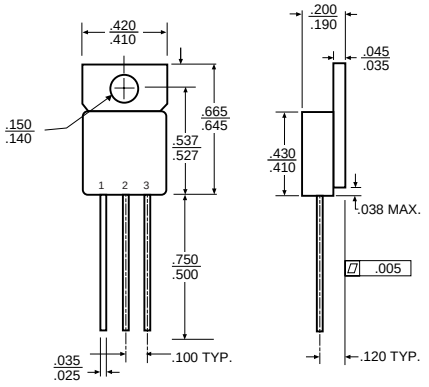
3.2

SCHEMATIC



Common cathode is standard. Contact factory for performance characteristics for common anode and doubler. Z-Tab package also available.

MECHANICAL OUTLINE

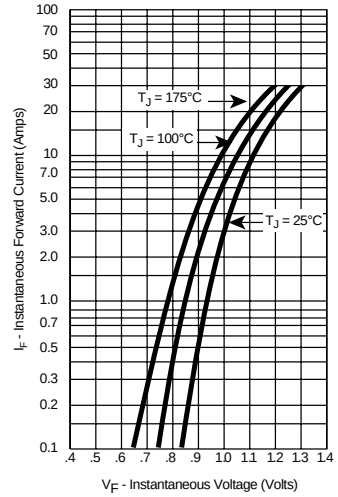


ELECTRICAL CHARACTERISTICS (Per Diode)

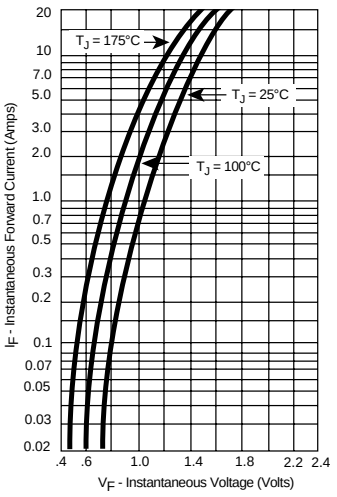
Part Number	PIV	Maximum Forward Voltage (Volts) @ 4A (1)		Maximum Reverse Current @ PIV (2)		Maximum Reverse Recovery Time (2)	Maximum Thermal Resistance
		T_j = 25° C	T_j = 100° C	T_j = 25° C	T_j = 100° C		R_{qJC}
OM5207XX	150	1.10	1.00	5.0	125	25	5.0
OM5208XX	600	1.44	1.34	10.0	225	50	4.0
OM5214XX	1000	2.05	1.95	25.0	800	75	3.0

Notes: (1) Pulse Test: Pulse Width = 300μs, Duty Cycle 2.0%. **(2) Measured in Circuit:** I_F = 0.5 A, I_R = 1.0 A, I_{REC} = 0.25 A

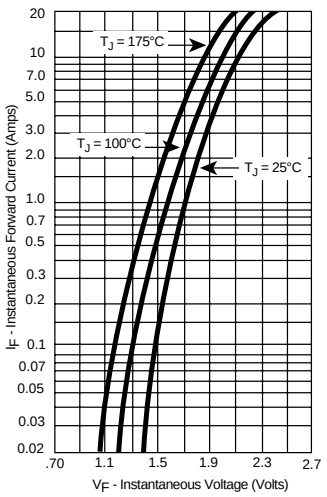
OM5207ST - Typical Forward Voltage



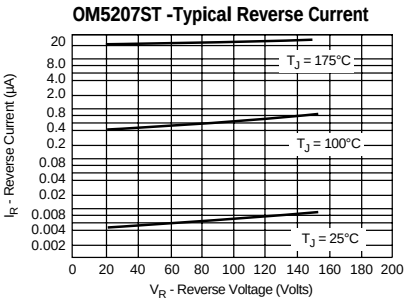
OM5208ST - Typical Forward Voltage



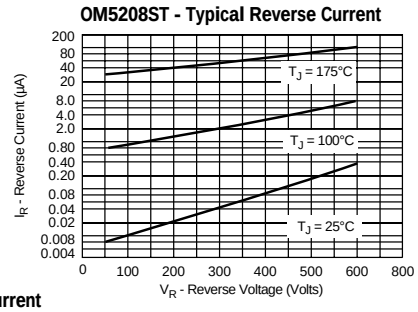
OM5214ST - Typical Forward Voltage



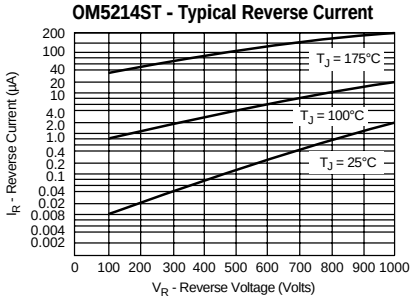
OM5207ST -Typical Reverse Current



OM5208ST - Typical Reverse Current



OM5214ST - Typical Reverse Current



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