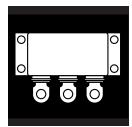
Preliminary Data Sheet

OM60L60SB OM45L120SB OM50F60SB OM35F120SB

IGBTS IN HERMETIC ISOLATED POWER BLOCK PACKAGES



High Current, High Voltage 600V And 1200V, Up To 75 Amp IGBTs With FRED Diodes

FEATURES

- Includes Internal FRED Diode
- · Rugged Package Design
- Solder Terminals
- · Very Low Saturation Voltage
- · Fast Switching, Low Drive Current
- Available Screened To MIL-S-19500, TX, TXV And S Levels
- · Ceramic Feedthroughs

DESCRIPTION

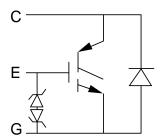
This series of hermetically packaged products feature the latest advanced IGBT technology combined with a package designed specifically for high efficiency, high current applications. They are ideally suited for Hi-Rel requirements where small size, high performance and high reliability are required, and in applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers and high energy pulse circuits.

2.1

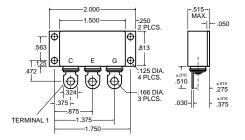
GENERAL CHARACTERISTICS @ 25°C

Part Number	V _{CE} (V)	I _C (A)	V _{CE(sat)}	Туре
OM60L60SB	600	75	1.8 Volts	Lo Sat.
OM45L120SB	1200	70	3 Volts	Lo Sat.
OM50F60SB	600	75	2.7 Volts	Hi Speed
OM35F120SB	1200	70	4 Volts	Hi Speed

SCHEMATIC



MECHANICAL OUTLINE





ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C unless otherwise noted)

Parameters		60L60SB	45L120SB	50F60SB	35F120SB	Units
V _{CES}	Drain Source Voltage	600	1200	600	1200	V
V _{CGR}	Drain Gate Voltage (R _{GS} = 1.0 M)	600	1200	600	1200	V
I _C @ T _C = 25°C	Continuous Drain Current	75	70	75	70	А
I _C @ T _C = 90°C	Continuous Drain Current	60	45	50	35	А
I _C Pulsed	Pulsed Drain Current ¹	200	180	200	140	Α
P _D @ T _C = 25°C	Max. Power Dissipation	250	250	250	250	W
P _D @ T _C = 100°C	Max. Power Dissipation	100	100	100	100	W
Junction-To-Case	Linear Derating Factor	2	2	2	2	W/°C
Junction-To-Ambient	Linear Derating Factor	.033	.033	.033	.033	W/°C
T _j , T _{stg} Operating	And Storage Temperature Range	-55 to +150	-55 to +150	-55 to +150	-55 to +150	°C
Lead Temperature (1/16" from case for 10 sec.)	230	230	230	230	°C
R _{thJC} Therma	al Resistance (Junction-To-Case)	0.5	0.5	0.5	0.5	°C/W
R _{th,JA} Therma	al Resistance (Junction-To-Ambient)	30	30	30	30	°C/W

Note: 1. Pulse Test: Pulse Width 300 µsec, Duty Cycle 2%.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Test Condition		Symbol	Part No.	Min.	Max.	Units
Gate Threshold Voltage	$V_{CE} = V_{GE}, I_{D} = 250 \mu A$		$V_{GE(th)}$	All	2.5	5.0	V
Gate-Emitter Leakage Current	$V_{GE} = \pm 20 V_{DC}$		I _{GES}	All		±100	nA
Off State	$V_{CE} = V_{DSS} \times 0.8$	$T_C = 25^{\circ}C$	I _{CES}	All		200	μA
Collector-Emitter Leakage	$V_{GS} = 0V$	T _C = 125°C	I _{CES}	All		1	mA
	$V_{GE} = 0V$, $I_{C} = 250 \mu A$		Vces	60L60SB	600		V
Collector-Emitter				45L120SB	1200		
Breakdown Voltage				50F60SB	600		
				35F120SB	1200		
	$V_{GE} = 15V, I_{C} = I_{C(100)} \times 0.5$		V _{CE(sat)}	60L60SB		1.8	
Static Collector-Emitter Voltage				45L120SB		3.0	
				50F60SB		2.7	
				35F120SB		4.0	

The above data is preliminary.

Please contact factory for additional data and the dynamic and switching characteristics.