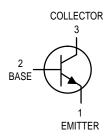
One Watt High Current Transistors NPN Silicon



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage MPSW01 MPSW01A	VCEO	30 40	Vdc
Collector-Base Voltage MPSW01 MPSW01A	VCBO	40 50	Vdc
Emitter-Base Voltage	VEBO	5.0	Vdc
Collector Current — Continuous	IC	1000	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	1.0 8.0	Watts mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	2.5 20	Watts mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{Stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	125	°C/W
Thermal Resistance, Junction to Case	$R_{ heta JC}$	50	°C/W

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage ⁽¹⁾ (I _C = 10 mAdc, I _B = 0)	MPSW01 MPSW01A	V(BR)CEO	30 40	_ _	Vdc
Collector-Base Breakdown Voltage (I _C = 100 μAdc, I _E = 0)	MPSW01 MPSW01A	V(BR)CBO	40 50	_	Vdc
Emitter – Base Breakdown Voltage (I _E = 100 μAdc, I _C = 0)		V(BR)EBO	5.0	_	Vdc
Collector Cutoff Current (V _{CB} = 30 Vdc, I _E = 0) (V _{CB} = 40 Vdc, I _E = 0)	MPSW01 MPSW01A	ICBO	<u> </u>	0.1 0.1	μAdc
Emitter Cutoff Current (V _{EB} = 3.0 Vdc, I _C = 0)		IEBO	_	0.1	μAdc

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

Preferred devices are Motorola recommended choices for future use and best overall value.



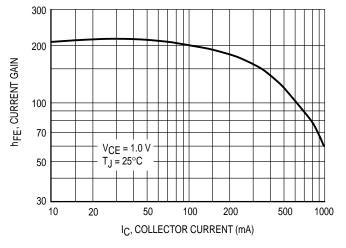
*Motorola Preferred Device



ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit
ON CHARACTERISTICS(1)	•	•		
DC Current Gain (I _C = 10 mAdc, V_{CE} = 1.0 Vdc) (I _C = 100 mAdc, V_{CE} = 1.0 Vdc) (I _C = 1000 mAdc, V_{CE} = 1.0 Vdc)	hFE	55 60 50	_ _ _	_
Collector-Emitter Saturation Voltage (IC = 1000 mAdc, I _B = 100 mAdc)	VCE(sat)	_	0.5	Vdc
Base–Emitter On Voltage (IC = 1000 mAdc, V _{CE} = 1.0 Vdc)	VBE(on)	_	1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Current-Gain — Bandwidth Product (IC = 50 mAdc, VCE = 10 Vdc, f = 20 MHz)	fΤ	50	_	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	_	20	pF

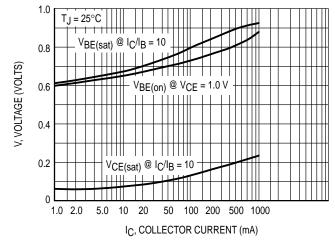
^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.



1.0 V_{CE}, COLLECTOR VOLTAGE (VOLTS) 0.8 0.6 IC = 000 mA 0.4 IC = 500 mA lČ. IC = IC = 250 mA 0.2 10 mA 50 mA 100 mA 0.01 0.02 0.05 0.1 0.2 0.5 5.0 20 1.0 2.0 10 50 100 IB, BASE CURRENT (mA)

Figure 1. DC Current Gain

Figure 2. Collector Saturation Region





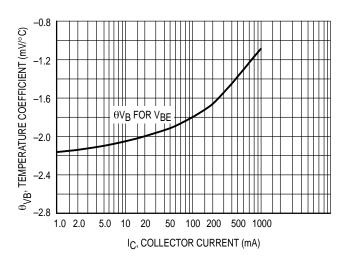
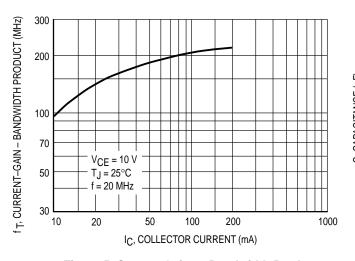


Figure 4. Temperature Coefficient



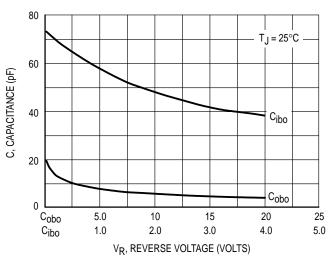


Figure 5. Current Gain — Bandwidth Product

Figure 6. Capacitance

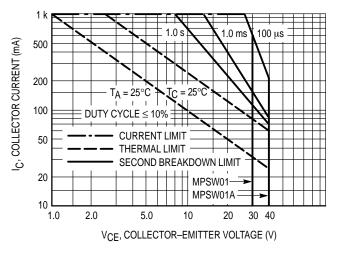
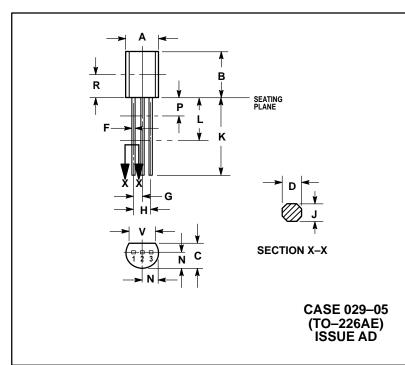


Figure 7. Active Region — Safe Operating Area

PACKAGE DIMENSIONS



- 1. DIMENSIONING AND TOLERANCING PER ANSI
- 714.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- DIMENSION F APPLIES BETWEEN P AND L.
 DIMENSIONS D AND J APPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.44	5.21
В	0.290	0.310	7.37	7.87
С	0.125	0.165	3.18	4.19
D	0.018	0.022	0.46	0.56
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.018	0.024	0.46	0.61
K	0.500		12.70	
L	0.250	_	6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.135		3.43	
V	0.135	_	3.43	

STYLE 1: PIN 1. EMITTER

BASE 3. COLLECTOR

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