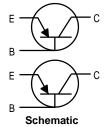
Preliminary Data Sheet

Plastic Power Transistors

SO-8 for Surface Mount Applications

- Collector –Emitter Sustaining Voltage VCEO(sus)
 30 Vdc (Min) @ I_C = 10 mAdc
- High DC Current Gain hFE
 - = 140 (Min) @ I_C = 1.2 Adc
 - = 125 (Min) @ IC = 3.0 Adc
- Low Collector -Emitter Saturation Voltage VCE(sat)
 - $= 0.24 \text{ Vdc (Max)} @ I_C = 1.2 \text{ Adc}$
 - = 0.60 Vdc (Max) @ IC = 5.0 Adc
- Miniature SO-8 Surface Mount Package Saves Board Space



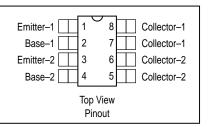
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Motorola Preferred Device

DUAL BIPOLAR
POWER TRANSISTOR
PNP SILICON
30 VOLTS
3 AMPERES



CASE 751-05, Style 16 (SO-8)



MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Collector–Base Voltage	V _{CB}	45	Vdc
Collector–Emitter Voltage	VCEO	30	Vdc
Emitter–Base Voltage	VEB	± 8.0	Vdc
Collector Current — Continuous — Peak	lC	3.0 5.0	Adc
Base Current — Continuous	IВ	1.0	Adc
Operating and Storage Junction Temperature Range	T _J , T _{Stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance – Junction to Ambient ⁽¹⁾	$R_{ heta JC}$	62.5	°C/W
Total Power Dissipation @ T _A = 25°C(1) Derate above 25°C	P _D	2.0 16	Watts mW/°C
Maximum Temperature for Soldering	TL	260	°C

⁽¹⁾ Mounted on 2" sq. FR-4 board (1" sq. 2 oz. Cu 0.06" thick single sided) with one die operating, 10 seconds max. This document contains information on a new product. Specifications and information are subject to change without notice.

Preferred devices are Motorola recommended choices for future use and best overall value.

REV 2

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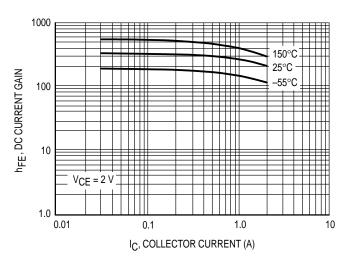
ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Sustaining Voltage (I _C = 10 mAdc, I _B = 0 Adc)	VCEO(sus)	30	_	_	Vdc
Collector Cutoff Current (V _{CE} = 25 Vdc, R _{BE} = 200 Ω)	ICER	_	_	20	μAdc
Emitter Cutoff Current (VBE = 5.0 Vdc)	IEBO	_	_	10	μAdc
ON CHARACTERISTICS(1)					
Collector–Emitter Saturation Voltage (I _C = 0.8 Adc, I _B = 20 mAdc) (I _C = 1.2 Adc, I _B = 20 mAdc) (I _C = 5.0 Adc, I _B = 1.0 Adc)	VCE(sat)	_ _ _	0.14 — —	0.20 0.24 0.60	Vdc
Base–Emitter Saturation Voltage (I _C = 5.0 Adc, I _B = 1.0 Adc)	V _{BE} (sat)	_	_	1.40	Vdc
Base–Emitter On Voltage (I _C = 3.0 Adc, V _{CE} = 4.0 Vdc)	VBE(on)	_	_	1.10	Vdc
DC Current Gain (IC = 1.2 Adc, VCE = 4.0 Vdc) (IC = 3.0 Adc, VCE = 4.0 Vdc)	hFE	140 125	— 180	_ _	_
DYNAMIC CHARACTERISTICS					
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0 Adc, f = 1.0 MHz)	C _{ob}	_	100	_	pF
Input Capacitance (VEB = 8.0 Vdc)	C _{ib}	_	135	_	pF
Current–Gain — Bandwidth Product ⁽²⁾ (I _C = 500 mA, V _{CE} = 10 V, F _{test} = 1.0 MHz)	fT	_	105	_	MHz

⁽¹⁾ Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

⁽²⁾ $f_T = |h_{FE}| \cdot f_{test}$

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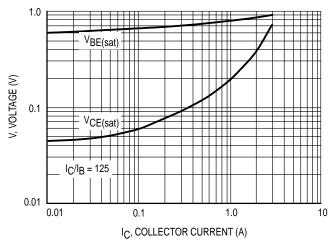
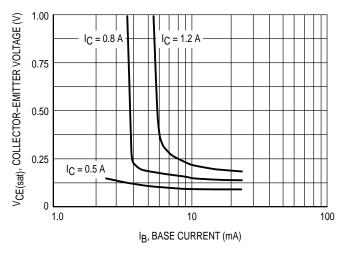


Figure 1. DC Current Gain

Figure 2. "ON" Voltages



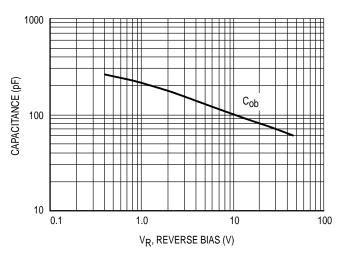


Figure 3. Collector Saturation Region

Figure 4. Capacitance

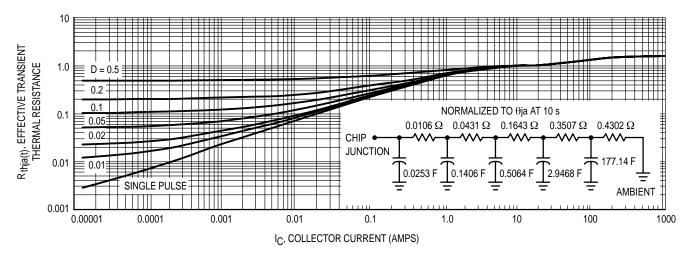
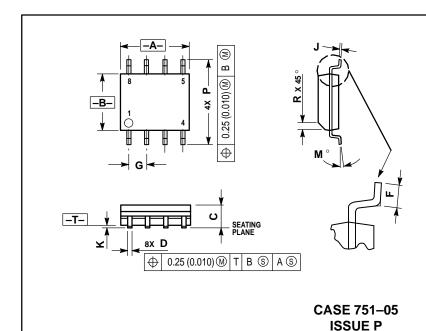


Figure 5. Thermal Response

PACKAGE DIMENSIONS



NOTES:

- DIMENSIONS A AND B ARE DATUMS AND T IS A DATUM SURFACE.
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- DIMENSIONS ARE IN MILLIMETER.
- DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
- MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
 DIMENSION D DOES NOT INCLUDE MOLD
- PROTRUSION. ALLOWABLE DAMBAR
 PROTRUSION SHALL BE 0.127 TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.80	5.00	
В	3.80	4.00	
С	1.35	1.75	
D	0.35	0.49	
F	0.40	1.25	
G	1.27 BSC		
J	0.18	0.25	
K	0.10	0.25	
M	0 °	7 °	
Р	5.80	6.20	
R	0.25	0.50	

STYLE 16:

PIN 1. EMITTER, DIE #1

- BASE DIF #1
- EMITTER, DIE #2
- BASE, DIE #2
- COLLECTOR, DIE #2 COLLECTOR, DIE #2
- 6.
- COLLECTOR, DIE #1
- COLLECTOR, DIE #1

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