# Advance Information

# Surface Mount Schottky Power Rectifier

## **SMB Power Surface Mount Package**

... employing the Schottky Barrier principle in a metal-to-silicon power rectifier. Features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency switching power supplies; free wheeling diodes and polarity protection diodes.

- Compact Package with J-Bend Leads Ideal for Automated Handling
- Highly Stable Oxide Passivated Junction
- Guardring for Over–Voltage Protection
- Low Forward Voltage Drop

### Mechanical Characteristics:

- Case: Molded Epoxy
- Epoxy Meets UL94, VO at 1/8"
- Weight: 95 mg (approximately)
- Maximum Temperature of 260°C / 10 Seconds for Soldering
- Polarity: Notch in Plastic Body Indicates Cathode Lead
- Available in 12 mm Tape, 2500 Units per 13 inch Reel, Add "T3" Suffix to Part Number
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Marking: BKJL

#### MAXIMUM RATINGS

Symbol	Value	Unit
V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	40	V
IO	2.0	A
IFRM	4.0	A
IFSM	70	A
T <sub>stg</sub> , T <sub>C</sub>	-55 to 150	°C
TJ	-55 to 125	
dv/dt	10,000	V/μs
• •		-
	VRRM VRWM VR IO IFRM IFSM Tstg, TC TJ	VRRM V <sub>RWM</sub> VR 40   IO 2.0   IFRM 4.0   IFRM 70   Tstg, TC -55 to 150   TJ -55 to 125

Thermal Resistance – Junction–to–Ambient (3) Rtja 78	Thermal Resistance – Junction–to–Lead (2) Thermal Resistance – Junction–to–Ambient (3)	R <sub>tjl</sub> R <sub>tja</sub>	/8	°C/W
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#### ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (1), See Figure 2	VF	Tj = 25°C	TJ = 125°C	V
(I <sub>F</sub> = 2 A) (I <sub>F</sub> = 4 A)		0.43 0.50	0.34 0.45	
Maximum Instantaneous Reverse Current, See Figure 4	IR	Tj = 25°C	Tj = 100°C	mA
(V <sub>R</sub> = 40 V) (V <sub>R</sub> = 20 V)		0.80 0.10	20 6.0	

(1) Pulse Test: Pulse Width  $\leq$  250 µs, Duty Cycle  $\leq$  2.0%.

(2) Minimum pad size (0.108 X 0.085 inch) for each lead on FR4 board.

(3) 1 inch square pad size (1 X 0.5 inch for each lead) on FR4 board.

This document contains information on a new product. Specifications and information herein are subject to change without notice.



SCHOTTKY BARRIER RECTIFIER 2.0 AMPERES 40 VOLTS





### MBRS2040LT3





Figure 3. Typical Reverse Current

Figure 4. Maximum Reverse Current



Figure 5. Current Derating

**Figure 6. Forward Power Dissipation** 

#### MBRS2040LT3



Figure 7. Capacitance

#### Figure 8. Typical Operating Temperature Derating\*

\* Reverse power dissipation and the possibility of thermal runaway must be considered when operating this device under any reverse voltage conditions. Calculations of T<sub>J</sub> therefore must include forward and reverse power effects. The allowable operating T<sub>J</sub> may be calculated from the equation:  $T_J = T_{Jmax} - r(t)(Pf + Pr)$  where

r(t) = thermal impedance under given conditions,

Pf = forward power dissipation, and

Pr = reverse power dissipation

This graph displays the derated allowable T<sub>J</sub> due to reverse bias under DC conditions only and is calculated as  $T_J = T_{Jmax} - r(t)Pr$ , where r(t) = Rthja. For other power applications further calculations must be performed.











#### PACKAGE DIMENSIONS



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