

FEATURES

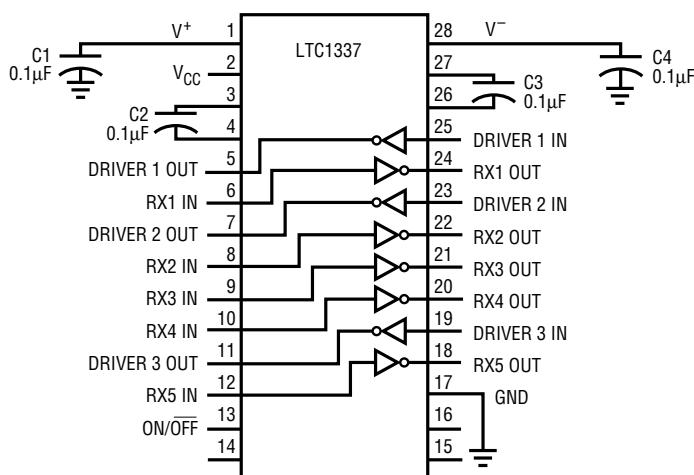
- **Low Supply Current: 300 μ A**
- 1 μ A Supply Current in SHUTDOWN
- **ESD Protection: Over ± 10 kV**
- Operates from a Single 5V Supply
- Uses Small Capacitors: 0.1 μ F
- Operates to 120k Baud
- Three-State Outputs Are High Impedance When Off
- Output Overvoltage Does Not Force Current Back into Supplies
- RS232 I/O Lines Can Be Forced to ± 25 V without Damage
- Pin Compatible with LT1137A and LT1237
- Flowthrough Architecture

APPLICATIONS

- Notebook Computers
- Palmtop Computers

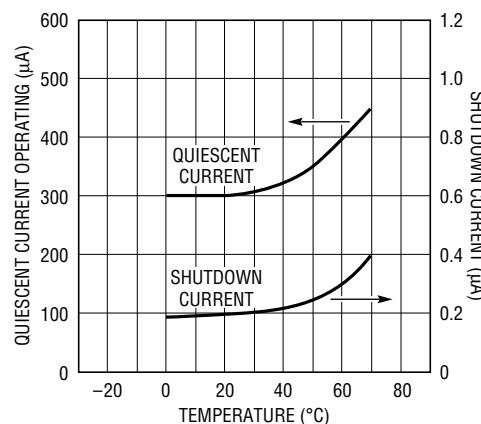
TYPICAL APPLICATION

3-Drivers/5-Receiver with SHUTDOWN



1337 TA01

Supply Current



TEST CONDITION:
 $V_{CC} = 5$ V, ALL DRIVER INPUTS TIED TO V_{CC}

1337 TA02

ABSOLUTE MAXIMUM RATINGS

(Note 1)

Supply Voltage (V_{CC}) 6V

Input Voltage

Driver -0.3V to V_{CC} + 0.3V

Receiver -25V to 25V

On/Off Pin -0.3V to V_{CC} + 0.3V

Output Voltage

Driver -25V to 25V

Receiver -0.3V to V_{CC} + 0.3V

Short Circuit Duration

V^+ 30 sec

V^- 30 sec

Driver Output Indefinite

Receiver Output Indefinite

Operating Temperature Range

Commercial (LTC1337C) 0°C to 70°C

Storage Temperature Range -65°C to 150°C

Lead Temperature (Soldering, 10 sec) 300°C

PACKAGE/ORDER INFORMATION

| TOP VIEW | ORDER PART NUMBER |
|----------|-------------------|
| | LTC1337CG |
| | LTC1337CN |
| | LTC1337CS |

G PACKAGE N PACKAGE
28-LEAD SSOP 28-LEAD PLASTIC DIP
S PACKAGE
28-LEAD PLASTIC SOL

$T_{JMAX} = 125^\circ\text{C}, \theta_{JA} = 96^\circ\text{C/W (G)}$
 $T_{JMAX} = 125^\circ\text{C}, \theta_{JA} = 56^\circ\text{C/W (N)}$
 $T_{JMAX} = 125^\circ\text{C}, \theta_{JA} = 85^\circ\text{C/W (S)}$

Consult factory for Industrial and Military grade parts.

DC ELECTRICAL CHARACTERISTICS

$V_{CC} = 5\text{V}$, $C1 = C2 = C3 = C4 = 0.1\mu\text{F}$, unless otherwise noted.

| PARAMETER | CONDITIONS | | MIN | TYP | MAX | UNITS |
|------------------------------|--|----------------------|--------|-------------|-------------|------------------|
| Any Driver | | | | | | |
| Output Voltage Swing | $R_L = 3\text{k}$ to GND $R_L = 3\text{k}$ to GND | Positive Negative | ● ● | 5.0 -5.0 | 7.0 -6.5 | V |
| Logic Input Voltage Level | Input Low Level ($V_{OUT} = \text{High}$) Input High Level ($V_{OUT} = \text{Low}$) | | ● ● | 2.0 | 1.4 1.4 | V |
| Logic Input Current | $V_{IN} = 5\text{V}$ $V_{IN} = 0$ | | ● ● | | 5 -5 | μA |
| Output Short-Circuit Current | $V_{OUT} = 0\text{V}$ | | | | ± 10 | mA |
| Output Leakage Current | SHUTDOWN, $V_{OUT} = \pm 20\text{V}$ (Note 3) | ● | | 10 | 200 | μA |
| Any Receiver | | | | | | |
| Input Voltage Thresholds | Input Low Threshold Input High Threshold | | ● ● | 0.8 1.7 | 1.3 2.4 | V |
| Hysteresis | | | ● | 0.1 | 0.4 | 1 |
| Input Resistance | | | | 3 | 5 | $\text{k}\Omega$ |
| Output Voltage | Output Low, $I_{OUT} = -1.6\text{mA}$ ($V_{CC} = 5\text{V}$) Output High, $I_{OUT} = 160\mu\text{A}$ ($V_{CC} = 5\text{V}$) | | ● ● | 0.2 3.5 | 0.4 4.8 | V |
| Output Short-Circuit Current | Sourcing Current, $V_{OUT} = 0$ Sinking Current, $V_{OUT} = V_{CC}$ | | | 15 -15 | 20 -40 | mA |
| Output Leakage Current | SHUTDOWN, $0 \leq V_{OUT} \leq V_{CC}$ (Note 3) | ● | | 1 | 10 | μA |

DC ELECTRICAL CHARACTERISTICS $V_{CC} = 5V$, $C1 = C2 = C3 = C4 = 0.1\mu F$, unless otherwise noted.

| PARAMETER | CONDITIONS | | MIN | TYP | MAX | UNITS |
|-------------------------------------|-------------------------------------|---|------|------|-----|---------|
| Power Supply Generator | | | | | | |
| V^+ Output Voltage | $I_{OUT} = 0mA$ $I_{OUT} = 12mA$ | | 8.0 | 7.5 | | V |
| V^- Output Voltage | $I_{OUT} = 0mA$ $I_{OUT} = 12mA$ | | -8.0 | -6.5 | | V |
| Supply Rise Time | SHUTDOWN to Turn-On | | 0.2 | | | ms |
| Power Supply | | | | | | |
| V_{CC} Supply Current | No Load (Note 2) | ● | 0.3 | 0.5 | | mA |
| Supply Leakage Current (V_{CC}) | SHUTDOWN (Note 3) | ● | 1 | 10 | | μA |
| On/Off Threshold Low | | ● | 1.4 | 0.8 | | V |
| On/Off Threshold High | | ● | 2.0 | 1.4 | | V |

AC CHARACTERISTICS

| PARAMETER | CONDITIONS | | MIN | TYP | MAX | UNITS |
|--|--|---|-----|-----|-----|-----------|
| Slew Rate | $R_L = 3k$, $C_L = 51pF$ $R_L = 3k$, $C_L = 2500pF$ | | 8 | 30 | | $V/\mu s$ |
| | | | 2 | 4 | | $V/\mu s$ |
| Driver Propagation Delay (TTL to RS232) | t_{HLD} (Figure 1) | ● | 2 | 3 | | μs |
| Receiver Propagation Delay (RS232 to TTL) | t_{LHD} (Figure 1) | ● | 2 | 3 | | μs |
| Driver Propagation Delay (RS232 to TTL) | t_{HLR} (Figure 2) | ● | 0.3 | 0.6 | | μs |
| Receiver Propagation Delay (TTL to RS232) | t_{LHR} (Figure 2) | ● | 0.2 | 0.6 | | μs |

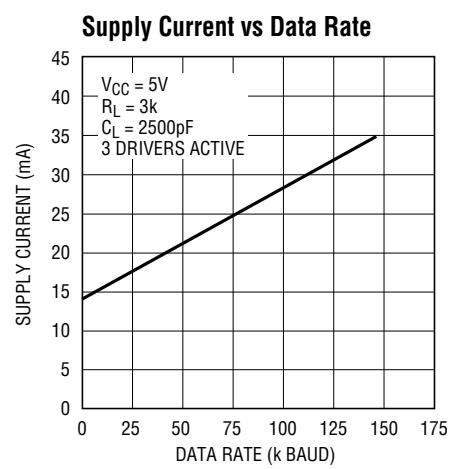
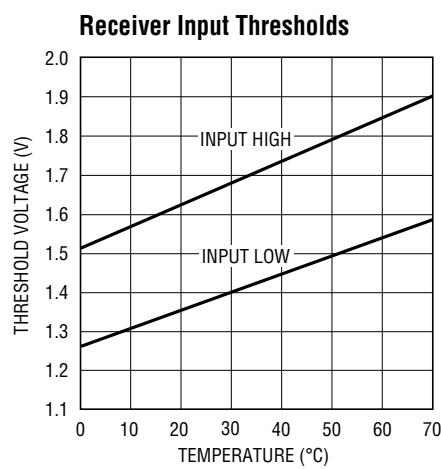
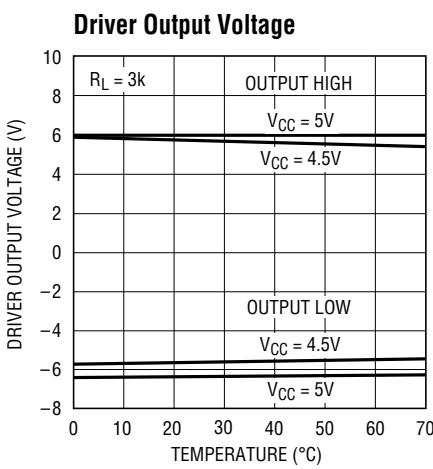
The ● denotes specifications which apply over the operating temperature range ($0^\circ C \leq T_A \leq 70^\circ C$).

Note 1: Absolute maximum ratings are those values beyond which the life of the device may be impaired.

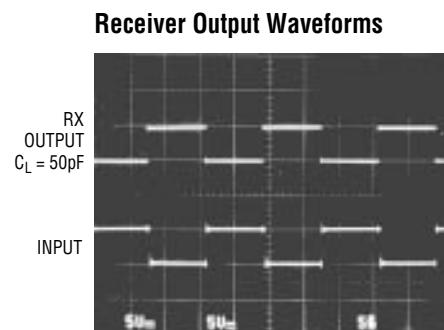
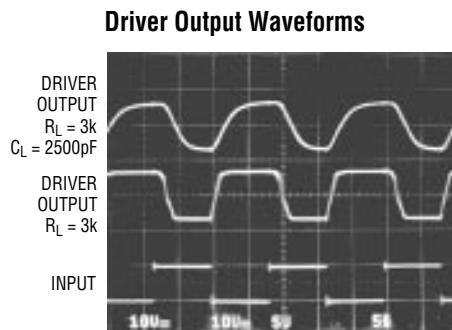
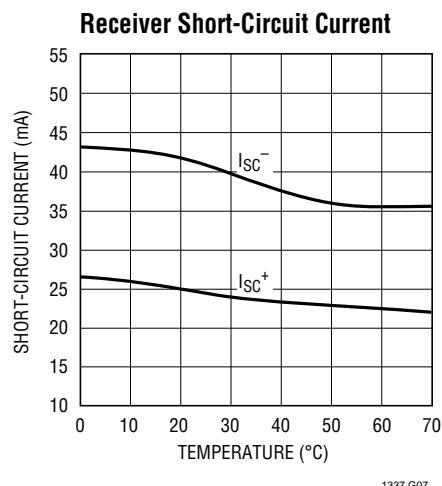
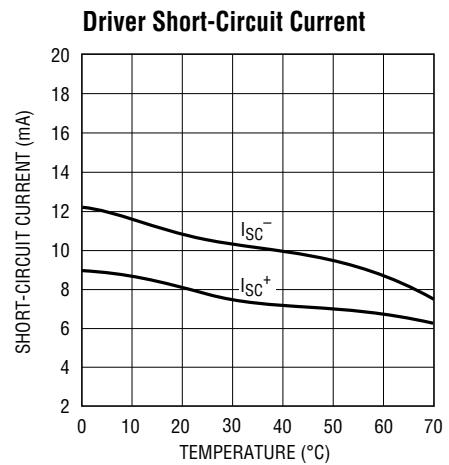
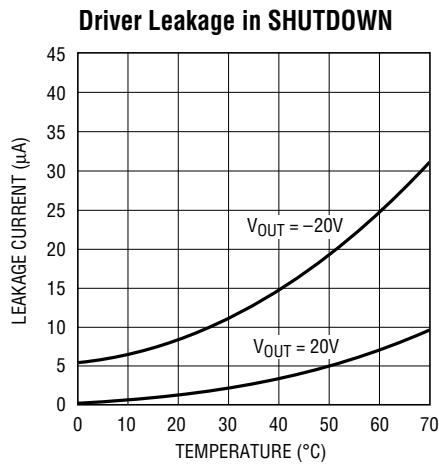
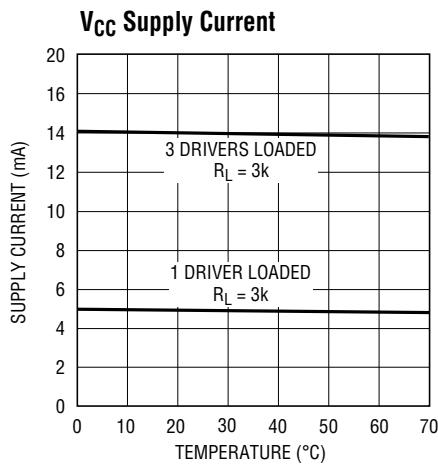
Note 2: Supply current is measured with driver and receiver outputs unloaded and driver inputs tied high.

Note 3: Supply current and leakage measurements in SHUTDOWN are performed with $V_{ON} = 0V$.

TYPICAL PERFORMANCE CHARACTERISTICS



TYPICAL PERFORMANCE CHARACTERISTICS



PIN FUNCTIONS

V_{CC}: 5V Input Supply Pin. Supply current less than 1µA in the SHUTDOWN mode. This pin should be decoupled with a 0.1µF ceramic capacitor.

GND: Ground Pin.

ON/OFF: TTL/CMOS Compatible Shutdown Pin. A logic low puts the device in the SHUTDOWN mode which reduces input supply current to less than 1µA and places all drivers and receivers in high impedance state.

V⁺: Positive Supply Output (RS232 Drivers). V⁺ \geq 2V_{CC} – 1V. This pin requires an external capacitor C = 0.1µF for charge storage. The capacitor may be tied to ground or 5V. With multiple devices, the V⁺ and V⁻ pins may be par-

alleled into common capacitors. For large numbers of devices, increasing the size of the shared common storage capacitors is recommended to reduce ripple.

V⁻: Negative Supply Output (RS232 Drivers). V⁻ \leq (2V_{CC} – 1.5V). This pin requires an external capacitor C = 0.1µF for charge storage.

C₁⁺, C₁⁻, C₂⁺, C₂⁻: Commutating Capacitor Inputs. These pins require two external capacitors C = 0.1µF. One from C₁⁺ to C₁⁻, and another from C₂⁺ to C₂⁻. To maintain charge pump efficiency, the capacitor's effective series resistance should be less than 50Ω.

PIN FUNCTIONS

DRIVER IN: RS232 Driver Input Pins. Inputs are TTL/CMOS compatible. Inputs should not be allowed to float. Tie unused inputs to V_{CC} .

DRIVER OUT: Driver Outputs at RS232 Voltage Levels. Outputs are in a high impedance state when in SHUTDOWN mode or $V_{CC} = 0V$. The driver outputs are protected against ESD to $\pm 10kV$ for human body model discharges.

RX IN: Receiver Inputs. These pins can be forced to $\pm 25V$ without damage. The receiver inputs are protected against ESD to $\pm 10kV$ for human body model discharges. Each receiver provides 0.4V of hysteresis for noise immunity.

RX OUT: Receiver Outputs with TTL/CMOS Voltage Levels. Outputs are in a high impedance state when in SHUTDOWN mode to allow data line sharing.

SWITCHING TIME WAVEFORMS

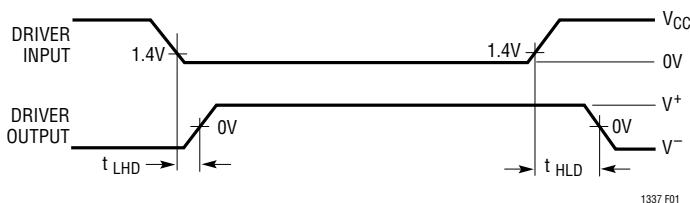


Figure 1. Driver Propagation Delay Timing

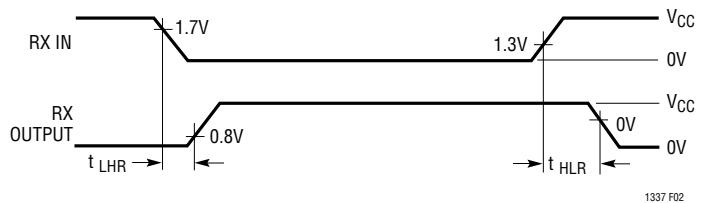


Figure 2. Receiver Propagation Delay Timing

TEST CIRCUITS

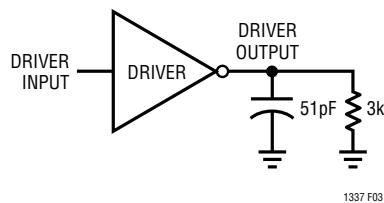


Figure 3. Driver Timing Test Load

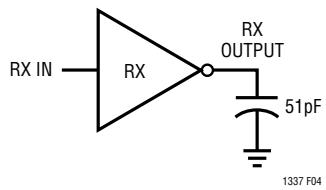
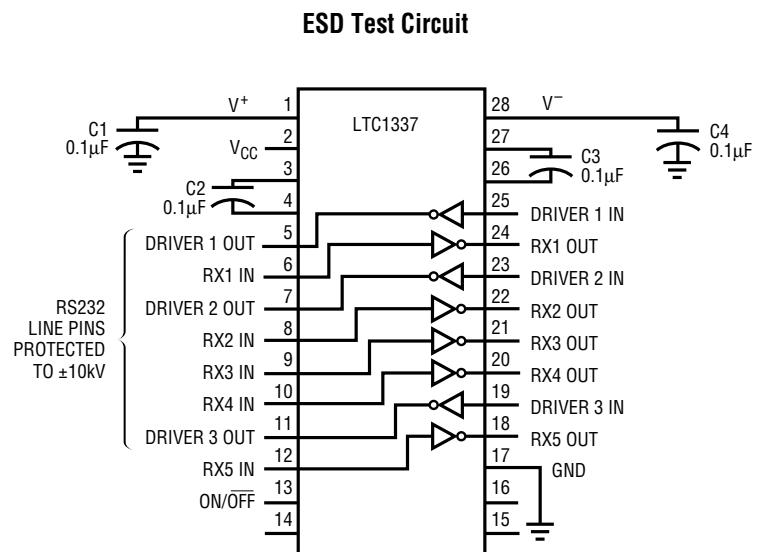
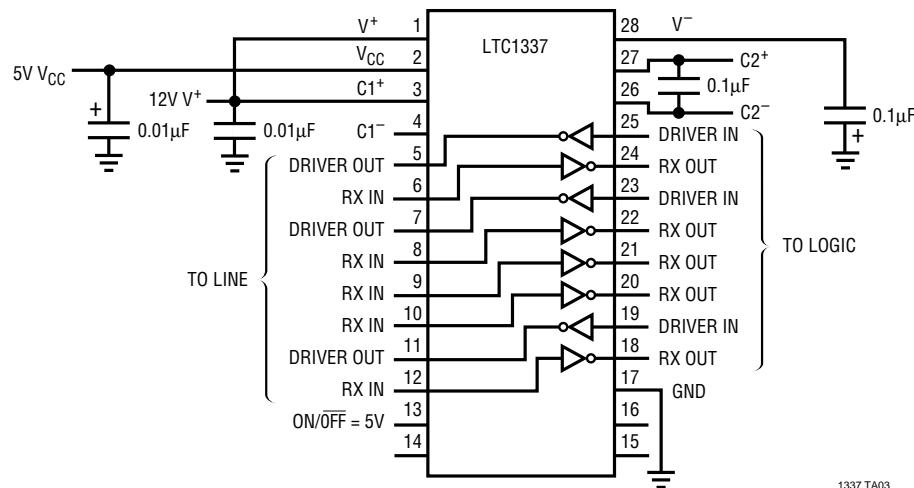


Figure 4. Receiver Timing Test Load



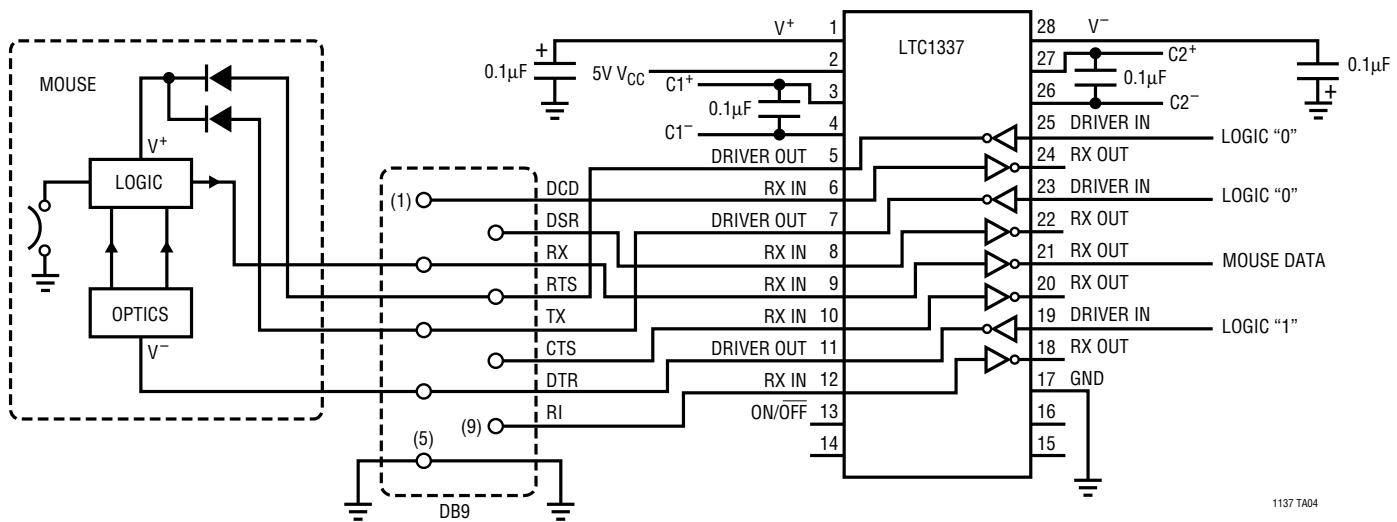
TYPICAL APPLICATIONS

Operation Using 5V and 12V Power Supplies



1337 TA03

Typical Mouse Driving Application

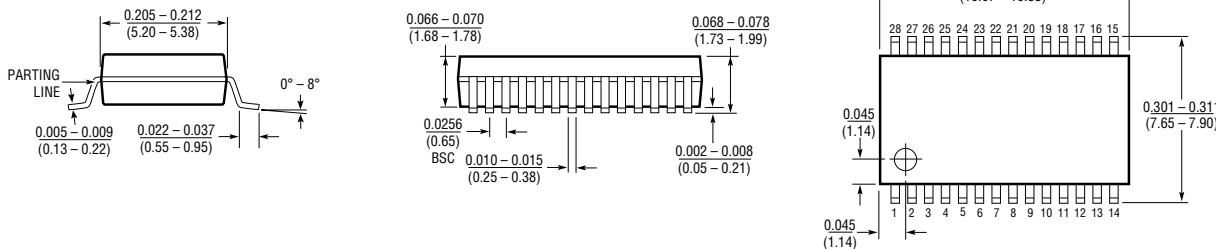


1137 TA04

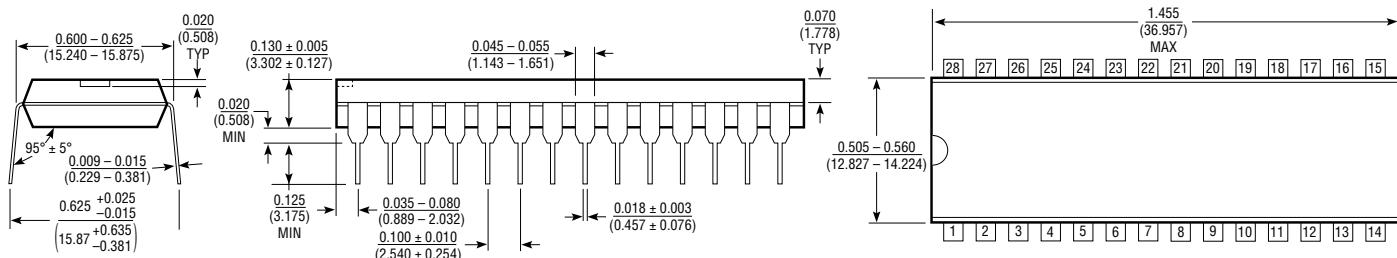
PACKAGE DESCRIPTION

Dimensions in inches (millimeters) unless otherwise noted.

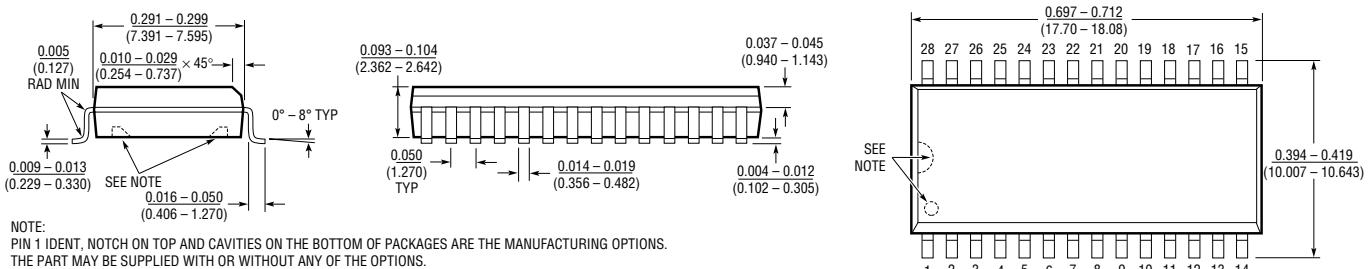
G Package
28-Lead SSOP



N Package
28-Lead Plastic DIP



S Package
28-Lead SO_L



NOTE:
PIN 1 IDENT, NOTCH ON TOP AND CAVITIES ON THE BOTTOM OF PACKAGES ARE THE MANUFACTURING OPTIONS.
THE PART MAY BE SUPPLIED WITH OR WITHOUT ANY OF THE OPTIONS.

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