

THYRISTOR/ DIODE and THYRISTOR/ THYRISTOR

ADD-A-pak™ GEN V Power Modules

Features

- High Voltage
- Industrial Standard Package
- Thick Al metal die and double stick bonding
- Thick copper baseplate
- UL E78996 approved
- 3500V_{RMS} isolating voltage

Benefits

- Up to 1600V
- Full compatible TO-240AA
- High Surge capability
- Easy Mounting on heatsink
- Al₂O₃ DBC insulator
- Heatsink grounded

105 A

Mechanical Description

The Generation V of Add-A-pak module combine the excellent thermal performance obtained by the usage of Direct Bonded Copper substrate with superior mechanical ruggedness, thanks to the insertion of a solid Copper baseplate at the bottom side of the device. The Cu baseplate allow an easier mounting on the majority of heatsink with increased tolerance of surface roughness and improve thermal spread.

The Generation V of AAP module is manufactured without hard mold, eliminating in this way any possible direct stress on the leads.

The electrical terminals are secured against axial pull-out: they are fixed to the module housing via a click-stop feature already tested and proved as reliable on other IR modules.

Electrical Description

These modules are intended for general purpose high voltage applications such as high voltage regulated power supplies, lighting circuits, temperature and motor speed control circuits, UPS and battery charger.

Major Ratings and Characteristics

Parameters	IRK.105	Units
$I_{T(AV)}$ or $I_{F(AV)}$ @ 85°C	105	A
$I_{O(RMS)}$ (*)	235	A
I_{TSM} @ 50Hz	1785	A
I_{FSM} @ 60Hz	1870	A
I^2t @ 50Hz	15.91	KA ² s
@ 60Hz	14.52	KA ² s
$I^2\sqrt{t}$	159.1	KA ² √s
V_{RRM} range	400 to 1600	V
T_{STG}	- 40 to 150	°C
T_J	- 40 to 130	°C

(*) As AC switch.



IRK.105 Series

Bulletin I27133 rev. G 02/02

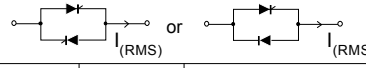
International
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ELECTRICAL SPECIFICATIONS

Voltage Ratings

Type number	Voltage Code	V_{RRM} , maximum repetitive peak reverse voltage V	V_{RSM} , maximum non-repetitive peak reverse voltage V	V_{DRM} , max. repetitive peak off-state voltage, gate open circuit V	I_{RRM} I_{DRM} 130°C mA
IRK.105	04	400	500	400	20
	06	600	700	600	
	08	800	900	800	
	10	1000	1100	1000	
	12	1200	1300	1200	
	14	1400	1500	1400	
	16	1600	1700	1600	

On-state Conduction

Parameters	IRK.105	Units	Conditions	
$I_{T(AV)}$ Max. average on-state current (Thyristors)	105	A	180° conduction, half sine wave, $T_C = 85^{\circ}\text{C}$	
$I_{F(AV)}$ Max. average forward current (Diodes)				
$I_{O(RMS)}$ Max. continuous RMS on-state current. As AC switch				
I_{TSM} Max. peak, one cycle or non-repetitive on-state I_{FSM} or forward current	1785		$t=10\text{ms}$ No voltage	Sinusoidal half wave, Initial $T_J = T_J \text{ max.}$
	1870		$t=8.3\text{ms}$ reapplied	
	1500		$t=10\text{ms}$ 100% V_{RRM}	
	1570		$t=8.3\text{ms}$ reapplied	
	2000		$t=10\text{ms}$ $T_J = 25^{\circ}\text{C}$,	
	2100		$t=8.3\text{ms}$ no voltage reapplied	
I^2t Max. I^2t for fusing	15.91		KA^2s	$t=10\text{ms}$ No voltage
	14.52	$t=8.3\text{ms}$ reapplied		
	11.25	$t=10\text{ms}$ 100% V_{RRM}		
	10.27	$t=8.3\text{ms}$ reapplied		
	20.00	$t=10\text{ms}$ $T_J = 25^{\circ}\text{C}$,		
	18.30	$t=8.3\text{ms}$ no voltage reapplied		
	$I^2\sqrt{t}$ Max. $I^2\sqrt{t}$ for fusing (1)	159.1		$\text{KA}^2\sqrt{\text{s}}$
$V_{T(TO)}$ Max. value of threshold voltage (2)	0.80	V	Low level (3)	$T_J = T_J \text{ max}$
	0.85		High level (4)	
r_t Max. value of on-state slope resistance (2)	2.37	$\text{m}\Omega$	Low level (3)	$T_J = T_J \text{ max}$
	2.25		High level (4)	
V_{TM} Max. peak on-state or	1.64	V	$I_{TM} = p \times I_{T(AV)}$	$T_J = 25^{\circ}\text{C}$
V_{FM} forward voltage			$I_{FM} = p \times I_{F(AV)}$	
di/dt Max. non-repetitive rate of rise of turned on current	150	$\text{A}/\mu\text{s}$	$T_J = 25^{\circ}\text{C}$, from $0.67 V_{DRM}$, $I_{TM} = p \times I_{T(AV)}$, $I_g = 500\text{mA}$, $t_r < 0.5 \mu\text{s}$, $t_p > 6 \mu\text{s}$	
I_H Max. holding current	200	mA	$T_J = 25^{\circ}\text{C}$, anode supply = 6V , resistive load, gate open circuit	
I_L Max. latching current	400		$T_J = 25^{\circ}\text{C}$, anode supply = 6V , resistive load	

(1) I^2t for time $t_x = I^2\sqrt{t} \times \sqrt{t_x}$

(2) Average power = $V_{T(TO)} \times I_{T(AV)} + r_t \times (I_{T(RMS)})^2$

(3) $16.7\% \times p \times I_{AV} < I < p \times I_{AV}$

(4) $I > p \times I_{AV}$

Triggering

Parameters	IRK.105	Units	Conditions
P_{GM} Max. peak gate power	12	W	
$P_{G(AV)}$ Max. average gate power	3		
I_{GM} Max. peak gate current	3	A	
$-V_{GM}$ Max. peak negative gate voltage	10	V	Anode supply = 6V resistive load
V_{GT} Max. gate voltage required to trigger	4.0		
	2.5		
	1.7		
I_{GT} Max. gate current required to trigger	270	mA	Anode supply = 6V resistive load
	150		
	80		
V_{GD} Max. gate voltage that will not trigger	0.25	V	$T_J = 125^\circ\text{C}$, rated V_{DRM} applied
I_{GD} Max. gate current that will not trigger	6	mA	$T_J = 125^\circ\text{C}$, rated V_{DRM} applied

Blocking

Parameters	IRK.105	Units	Conditions
I_{RRM} Max. peak reverse and off-state leakage current at V_{RRM} , V_{DRM}	20	mA	$T_J = 130^\circ\text{C}$, gate open circuit
V_{INS} RMS isolation voltage	2500 (1 min) 3500 (1 sec)	V	50 Hz, circuit to base, all terminals shorted
dv/dt Max. critical rate of rise of off-state voltage (5)	500	V/ μs	$T_J = 130^\circ\text{C}$, linear to $0.67 V_{DRM}$, gate open circuit

(5) Available with $dv/dt = 1000\text{V/ms}$, to complete code add S90 i.e. IRKT105/16AS90.

Thermal and Mechanical Specifications

Parameters	IRK.105	Units	Conditions
T_J Junction operating temperature range	- 40 to 130	$^\circ\text{C}$	
T_{stg} Storage temp. range	-40 to 150		
R_{thJC} Max. internal thermal resistance, junction to case	0.135	K/W	Per module, DC operation
R_{thCS} Typical thermal resistance case to heatsink	0.1		Mounting surface flat, smooth and greased
T Mounting torque $\pm 10\%$ to heatsink busbar	5	Nm	A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow for the spread of the compound
	3		
wt Approximate weight	110 (4)	gr (oz)	
Case style	TO-240AA		JEDEC

ΔR Conduction (per Junction)

(The following table shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC)

Devices	Sine half wave conduction					Rect. wave conduction					Units
	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	
IRK.105	0.04	0.05	0.06	0.08	0.12	0.03	0.05	0.06	0.08	0.12	$^\circ\text{C}/\text{W}$

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Device Code

IRK	T	105	/	16	A	S90
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1	Module type
2	Circuit configuration (See Circuit Configuration table below)
3	Current code **
4	Voltage code (See Voltage Ratings table)
5	A : Gen V
6	dv/dt code: S90 = dv/dt 1000 V/μs No letter = dv/dt 500 V/μs

IRK.106 types
With no auxiliary cathode

** Available with no auxiliary cathode.

To specify change: 105 to 106

e.g. : IRKT106/16A etc.

Technical drawing of the IRK series terminal block, showing side and front views with dimensions in millimeters and inches.

Side View Dimensions:

- Overall width: 80 ± 0.3 [3.15" ± 0.0118 "]
- Overall height: 35 ± 0.5 [1.4" ± 0.0197 "]
- Mounting hole diameter: $\varnothing 5.6$ [0.22" ± 0.0118 "]
- Mounting hole spacing: 18 [0.7" REF.]
- Terminal spacing: 15.6 ± 0.5 [0.6" ± 0.0197 "]
- Terminal width: 6.3 ± 0.3 [0.25" ± 0.0098 "]
- Terminal pitch: 2.4 ± 0.5 [0.1" ± 0.0394 "]
- Terminal length: 30 ± 1 [1.2" ± 0.0394 "]
- FASTEN TAB 2.8×0.8 (0.110 x 0.03)

Front View Dimensions:

- Overall width: 92 ± 0.75 [3.6" ± 0.0295 "]
- Overall height: 21 ± 0.75 [0.8" ± 0.0295 "]
- Terminal pitch: 6.3 ± 0.2 [0.25" ± 0.0079 "]
- Terminal width: 15 ± 0.5 [0.59" ± 0.0197 "]
- Terminal spacing: 20 ± 0.5 [0.79" ± 0.0197 "]

Dimensions are in millimeters and [inches]

NOTE: To order the Optional Hardware see Bulletin I27900

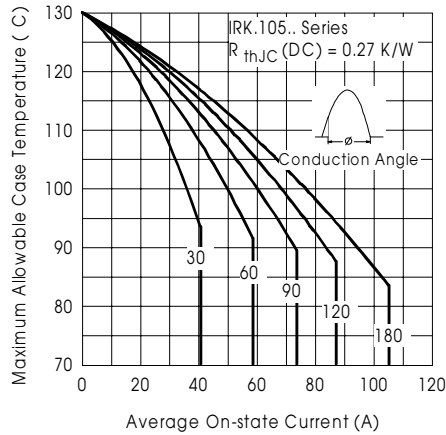


Fig. 1 - Current Ratings Characteristics

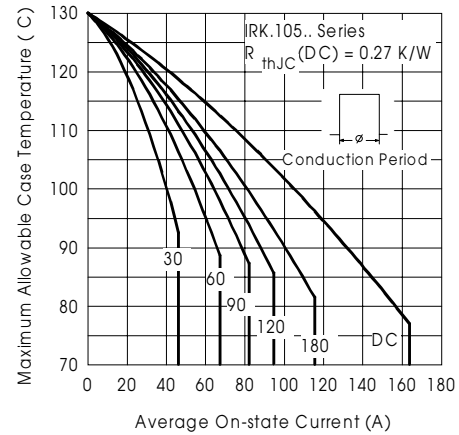


Fig. 2 - Current Ratings Characteristics

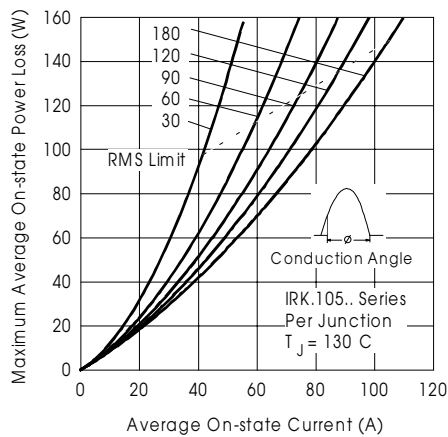


Fig. 3 - On-state Power Loss Characteristics

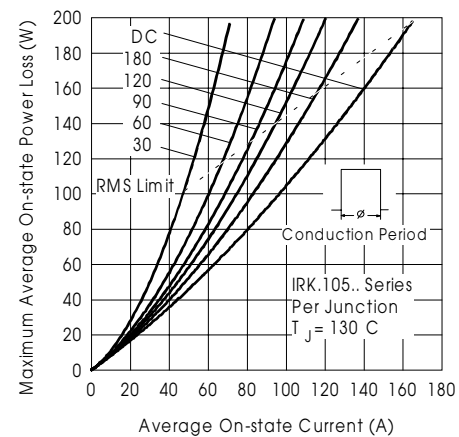


Fig. 4 - On-state Power Loss Characteristics

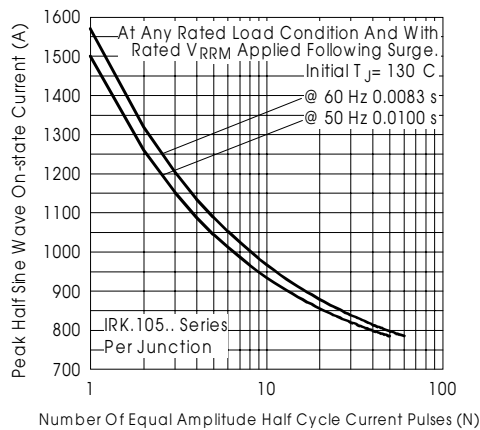


Fig. 5 - Maximum Non-Repetitive Surge Current

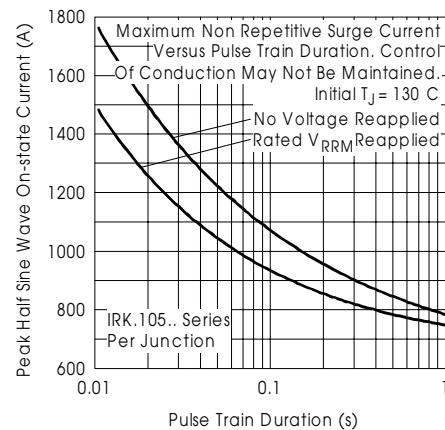


Fig. 6 - Maximum Non-Repetitive Surge Current

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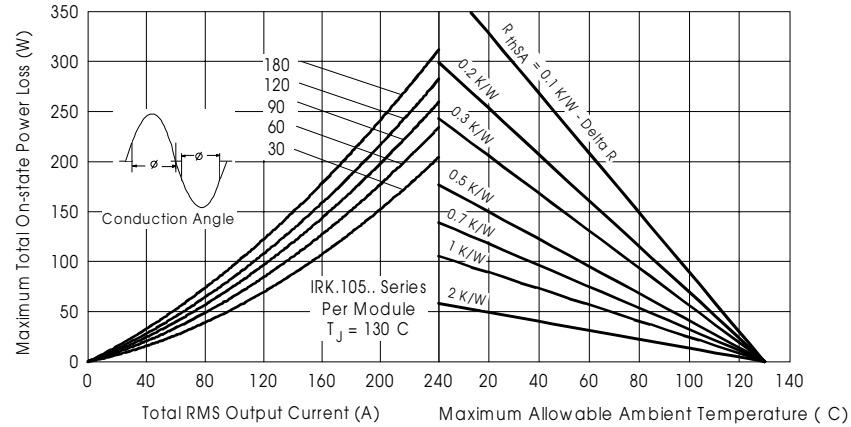


Fig. 7 - On-state Power Loss Characteristics

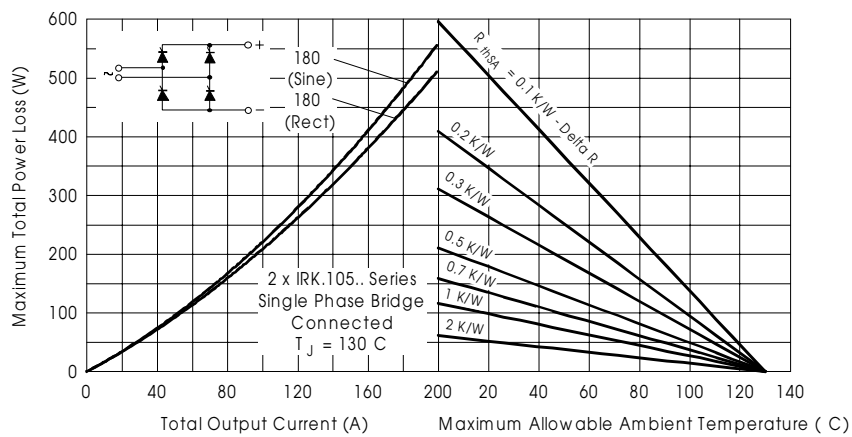


Fig. 8 - On-state Power Loss Characteristics

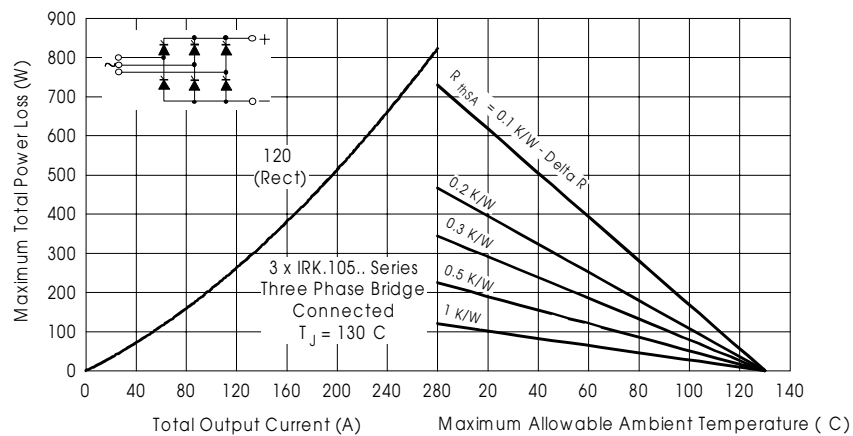


Fig. 9 - On-state Power Loss Characteristics

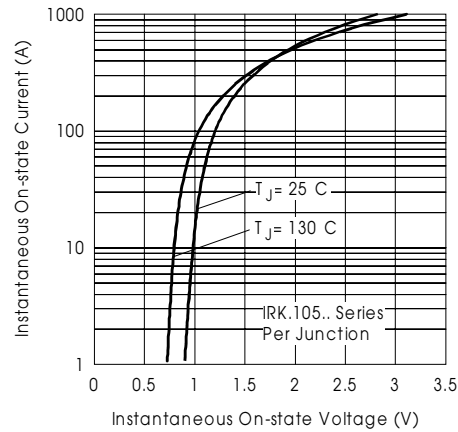


Fig. 10 - On-state Voltage Drop Characteristics

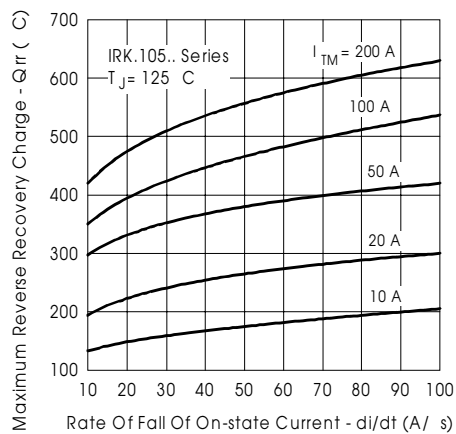


Fig. 11 - Recovery Charge Characteristics

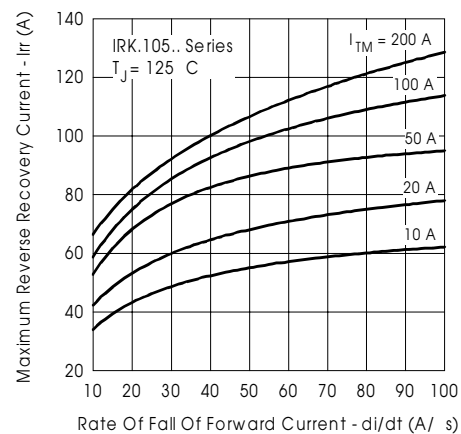


Fig. 12 - Recovery Current Characteristics

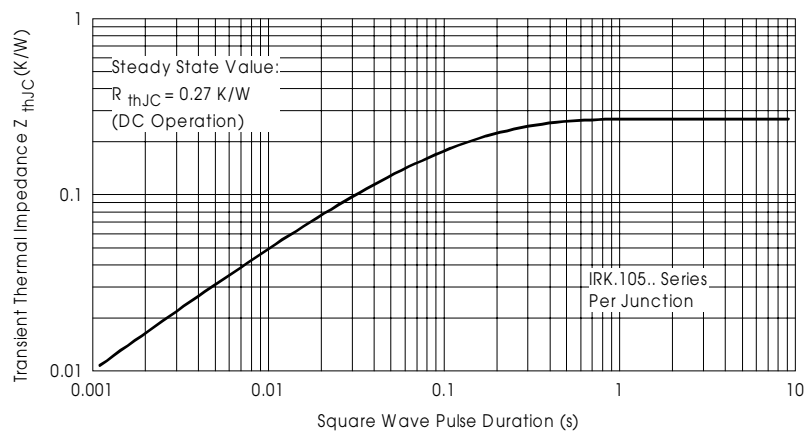


Fig. 13 - Thermal Impedance Z_{thJC} Characteristics

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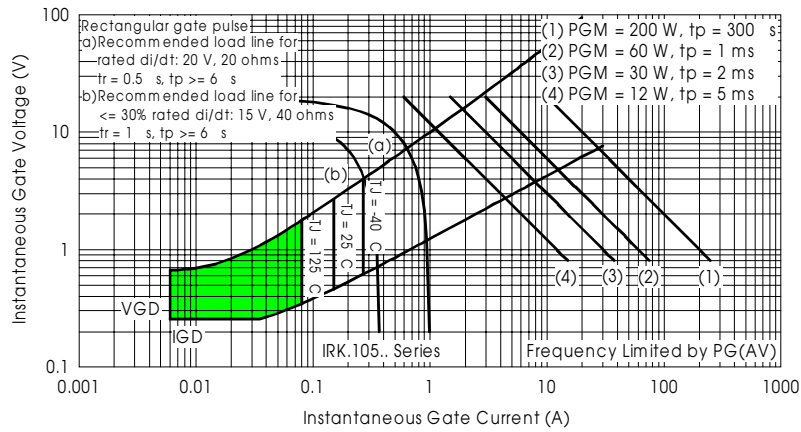


Fig. 14- Gate Characteristics

Data and specifications subject to change without notice.
This product has been designed and qualified for Industrial Level.
Qualification Standards can be found on IR's Web site.

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