

**RADIATION HARDENED  
POWER MOSFET  
THRU-HOLE (TO-254AA)**

**IRHM9160  
JANSR2N7425  
100V, P-CHANNEL  
REF: MIL-PRF-19500/660  
RAD-Hard™ HEXFET® TECHNOLOGY**

**Product Summary**

Part Number	Radiation Level	R <sub>Ds(on)</sub>	I <sub>D</sub>	QPL Part Number
IRHM9160	100K Rads (Si)	0.073Ω	-35A*	JANSR2N7425
IRHM93160	300K Rads (Si)	0.073Ω	-35A*	JANSF2N7425

International Rectifier's RAD-Hard HEXFET™ technology provides high performance power MOSFETs for space applications. This technology has over a decade of proven performance and reliability in satellite applications. These devices have been characterized for both Total Dose and Single Event Effects (SEE). The combination of low Rdson and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well established advantages of MOSFETs such as voltage control, fast switching, ease of paralleling and temperature stability of electrical parameters.



TO-254AA

**Features:**

- Single Event Effect (SEE) Hardened
- Low R<sub>Ds(on)</sub>
- Low Total Gate Charge
- Proton Tolerant
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Ceramic Package
- Light Weight

**Absolute Maximum Ratings**

**Pre-Irradiation**

	Parameter		Units
I <sub>D</sub> @ V <sub>GS</sub> = -12V, T <sub>C</sub> = 25°C	Continuous Drain Current	-35*	A
I <sub>D</sub> @ V <sub>GS</sub> = -12V, T <sub>C</sub> = 100°C	Continuous Drain Current	-24	
I <sub>DM</sub>	Pulsed Drain Current ①	-152	
P <sub>D</sub> @ T <sub>C</sub> = 25°C	Max. Power Dissipation	250	W
	Linear Derating Factor	2.0	W/°C
V <sub>GS</sub>	Gate-to-Source Voltage	±20	V
EAS	Single Pulse Avalanche Energy ②	500	mJ
I <sub>AR</sub>	Avalanche Current ①	-35	A
E <sub>AR</sub>	Repetitive Avalanche Energy ①	25	mJ
dV/dt	Peak Diode Recovery dV/dt ③	-16	V/ns
T <sub>J</sub>	Operating Junction	-55 to 150	°C
T <sub>STG</sub>	Storage Temperature Range		
	Lead Temperature	300 ( 0.063 in. (1.6mm) from case for 10s)	
	Weight	9.3 (typical)	g

For footnotes refer to the last page

\*Current is limited by internal wire diameter

**Electrical Characteristics @  $T_j = 25^\circ\text{C}$  (Unless Otherwise Specified)**

	Parameter	Min	Typ	Max	Units	Test Conditions
$\text{BV}_{\text{DSS}}$	Drain-to-Source Breakdown Voltage	-100	—	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{ID} = -1.0\text{mA}$
$\Delta \text{BV}_{\text{DSS}}/\Delta T_j$	Temperature Coefficient of Breakdown Voltage	—	-0.11	—	$\text{V}/^\circ\text{C}$	Reference to $25^\circ\text{C}$ , $\text{ID} = -1.0\text{mA}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source On-State Resistance	—	—	0.073	$\Omega$	$\text{V}_{\text{GS}} = -12\text{V}, \text{ID} = -22\text{A}$ ④
		—	—	0.075		$\text{V}_{\text{GS}} = -12\text{V}, \text{ID} = -35\text{A}$ ④
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	-2.0	—	-4.0	V	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{ID} = -1.0\text{mA}$
$g_{\text{fs}}$	Forward Transconductance	15	—	—	S (S)	$\text{V}_{\text{DS}} > -15\text{V}, \text{IDS} = -22\text{A}$ ④
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	—	—	-25	$\mu\text{A}$	$\text{V}_{\text{DS}} = -80\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
		—	—	-250		$\text{V}_{\text{DS}} = -80\text{V}, \text{V}_{\text{GS}} = 0\text{V}, T_j = 125^\circ\text{C}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Forward	—	—	-100	$\text{nA}$	$\text{V}_{\text{GS}} = -20\text{V}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Reverse	—	—	100		$\text{V}_{\text{GS}} = 20\text{V}$
$Q_g$	Total Gate Charge	—	—	290	$\text{nC}$	$\text{V}_{\text{GS}} = -12\text{V}, \text{ID} = -35\text{A}$
$Q_{gs}$	Gate-to-Source Charge	—	—	72		$\text{V}_{\text{DS}} = -50\text{V}$
$Q_{gd}$	Gate-to-Drain ('Miller') Charge	—	—	77		
$t_{\text{d(on)}}$	Turn-On Delay Time	—	—	35	$\text{ns}$	$\text{V}_{\text{DD}} = -50\text{V}, \text{ID} = -35\text{A}, \text{V}_{\text{GS}} = -12\text{V}, \text{RG} = 2.35\Omega$
$t_r$	Rise Time	—	—	170		
$t_{\text{d(off)}}$	Turn-Off Delay Time	—	—	190		
$t_f$	Fall Time	—	—	190		
$L_S + L_D$	Total Inductance	—	6.8	—	nH	Measured from drain lead (6mm/0.25in. from package) to source lead (6mm/0.25in. from package)
$C_{\text{iss}}$	Input Capacitance	—	6000	—	$\text{pF}$	$\text{V}_{\text{GS}} = 0\text{V}, \text{V}_{\text{DS}} = -25\text{V}$ $f = 1.0\text{MHz}$
$C_{\text{oss}}$	Output Capacitance	—	1400	—		
$C_{\text{rss}}$	Reverse Transfer Capacitance	—	400	—		

**Source-Drain Diode Ratings and Characteristics**

	Parameter	Min	Typ	Max	Units	Test Conditions
$I_S$	Continuous Source Current (Body Diode)	—	—	-35*	A	
$I_{\text{SM}}$	Pulse Source Current (Body Diode) ①	—	—	-140		
$V_{\text{SD}}$	Diode Forward Voltage	—	—	-3.3	V	$T_j = 25^\circ\text{C}, I_S = -35\text{A}, \text{V}_{\text{GS}} = 0\text{V}$ ④
$t_{\text{rr}}$	Reverse Recovery Time	—	—	300	nS	$T_j = 25^\circ\text{C}, I_F = -35\text{A}, dI/dt \leq -100\text{A}/\mu\text{s}$
$Q_{\text{RR}}$	Reverse Recovery Charge	—	—	2.1	$\mu\text{C}$	$\text{V}_{\text{DD}} \leq -50\text{V}$ ④
$t_{\text{on}}$	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $L_S + L_D$ .				

\*Current is limited by internal wire diameter

**Thermal Resistance**

	Parameter	Min	Typ	Max	Units	Test Conditions
$R_{\text{thJC}}$	Junction-to-Case	—	—	0.50	$^\circ\text{C/W}$	Typical socket mount
$R_{\text{thCS}}$	Case-to-Sink	—	0.21	—		
$R_{\text{thJA}}$	Junction-to-Ambient	—	—	48		

**Note: Corresponding Spice and Saber models are available on the G&S Website.**

For footnotes refer to the last page

## Radiation Characteristics

**IRHM9160**

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

**Table 1. Electrical Characteristics @  $T_j = 25^\circ\text{C}$ , Post Total Dose Irradiation<sup>⑤⑥</sup>**

	Parameter	100K Rads(Si) <sup>1</sup>		300K Rads (Si) <sup>2</sup>		Units	Test Conditions
		Min	Max	Min	Max		
$\text{BV}_{\text{DSS}}$	Drain-to-Source Breakdown Voltage	-100	—	-100	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = -1.0\text{mA}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	-2.0	-4.0	-2.0	-5.0		$\text{V}_{\text{GS}} = \text{V}_{\text{DS}}, \text{I}_D = -1.0\text{mA}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Forward	—	-100	—	-100	nA	$\text{V}_{\text{GS}} = -20\text{V}$
$\text{I}_{\text{GSS}}$	Gate-to-Source Leakage Reverse	—	100	—	100		$\text{V}_{\text{GS}} = 20\text{ V}$
$\text{I}_{\text{DSS}}$	Zero Gate Voltage Drain Current	—	-25	—	-25	$\mu\text{A}$	$\text{V}_{\text{DS}} = -80\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source <sup>④</sup> On-State Resistance (TO-3)	—	0.073	—	0.073	$\Omega$	$\text{V}_{\text{GS}} = -12\text{V}, \text{I}_D = -22\text{A}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source <sup>④</sup> On-State Resistance (TO-254AA)	—	0.073	—	0.073	$\Omega$	$\text{V}_{\text{GS}} = -12\text{V}, \text{I}_D = -22\text{A}$
$\text{V}_{\text{SD}}$	Diode Forward Voltage <sup>④</sup>	—	-3.3	—	-3.3	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_S = -35\text{A}$

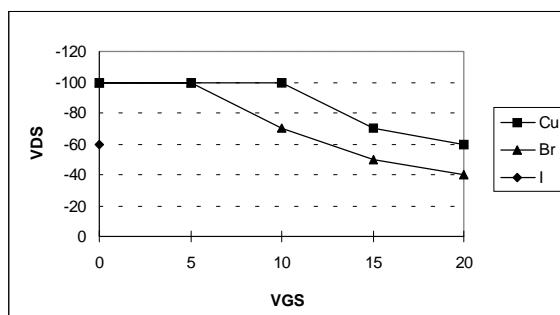
1. Part number IRHM9160 (JANSR2N7425)

2. Part number IRHM93160 (JANSF2N7425)

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

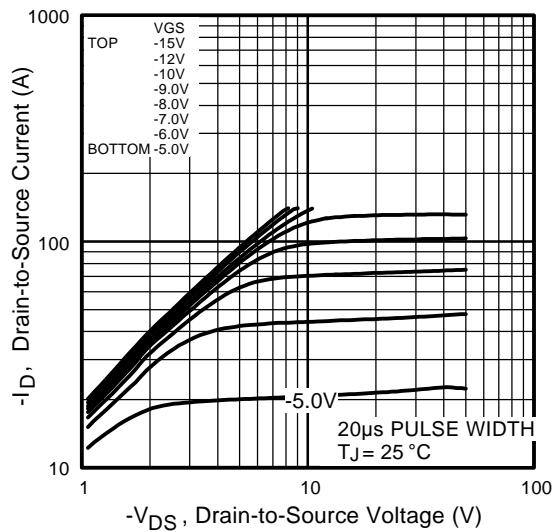
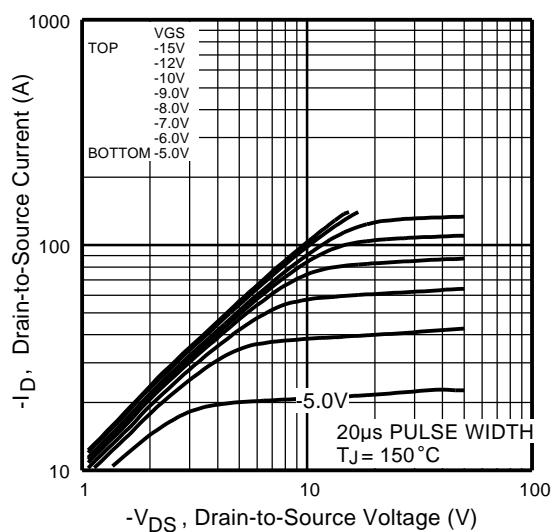
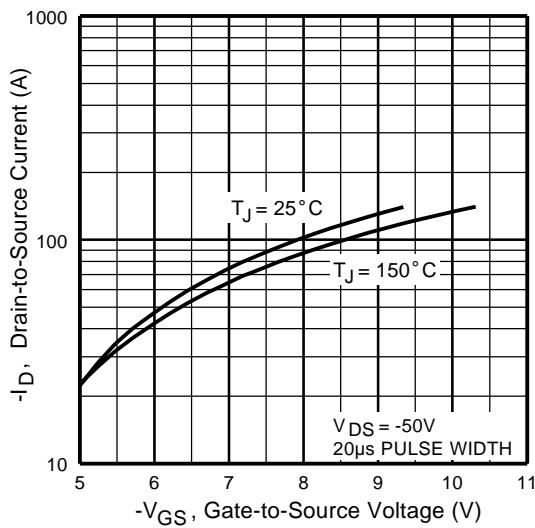
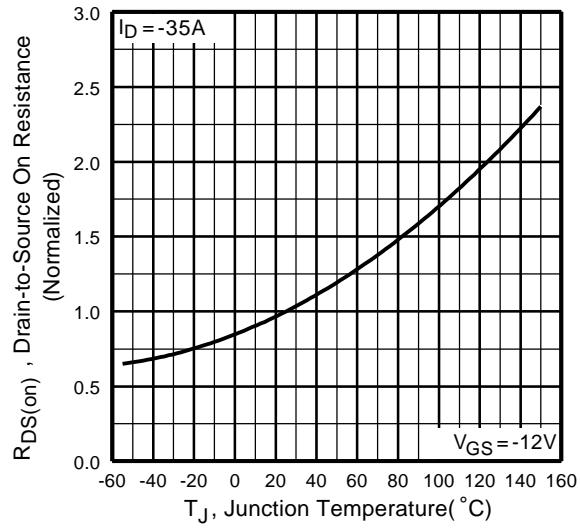
**Table 2. Single Event Effect Safe Operating Area**

Ion	LET MeV/(mg/cm <sup>2</sup> )	Energy (MeV)	Range ( $\mu\text{m}$ )	VDS(V)				
				@ $\text{VGS}=0\text{V}$	@ $\text{VGS}=5\text{V}$	@ $\text{VGS}=10\text{V}$	@ $\text{VGS}=15\text{V}$	@ $\text{VGS}=20\text{V}$
Cu	28	285	43	-100	-100	-100	-70	-60
Br	36.8	305	39	-100	-100	-70	-50	-40
I	59.9	345	32.8	-60	—	—	—	—



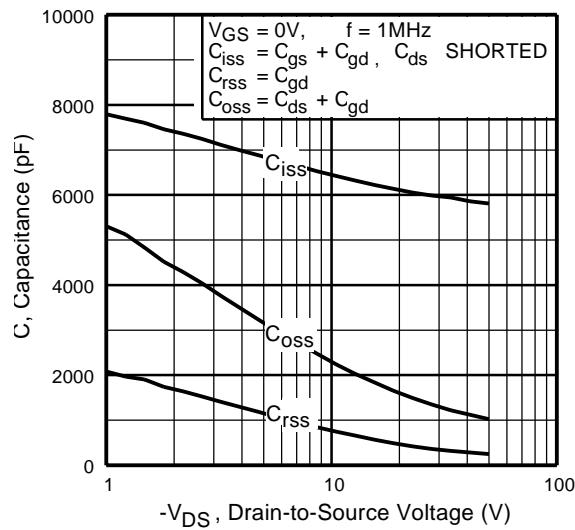
**Fig a.** Single Event Effect, Safe Operating Area

For footnotes refer to the last page

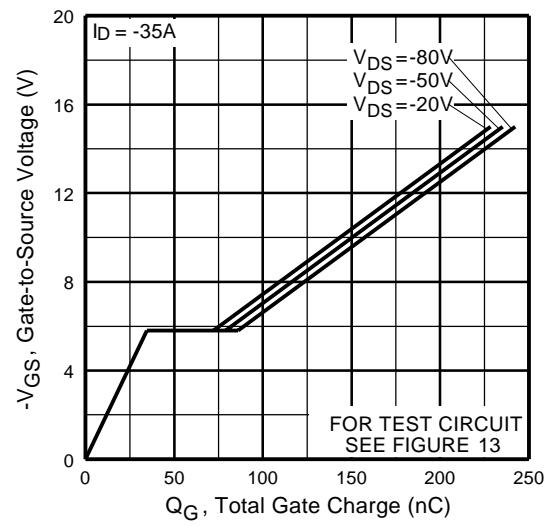
**IRHM9160****Pre-Irradiation****Fig1.** Typical Output Characteristics**Fig2.** Typical Output Characteristics**Fig3.** Typical Transfer Characteristics**Fig4.** Normalized On-Resistance Vs. Temperature

## Pre-Irradiation

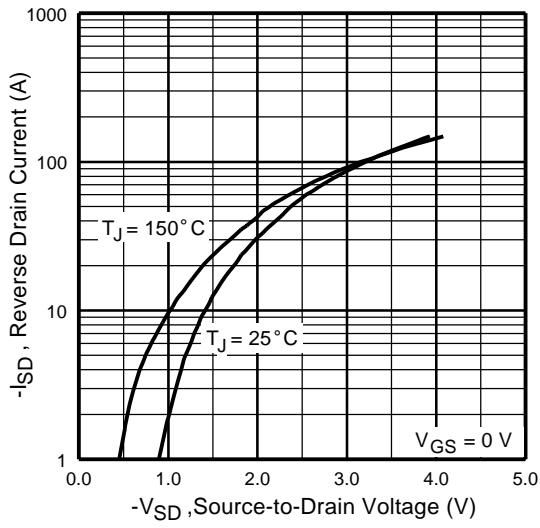
**IRHM9160**



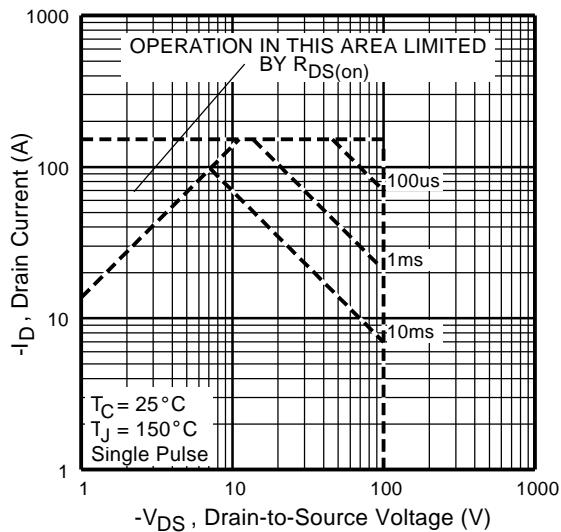
**Fig5.** Typical Capacitance Vs.  
Drain-to-Source Voltage



**Fig6.** Typical Gate Charge Vs.  
Gate-to-Source Voltage



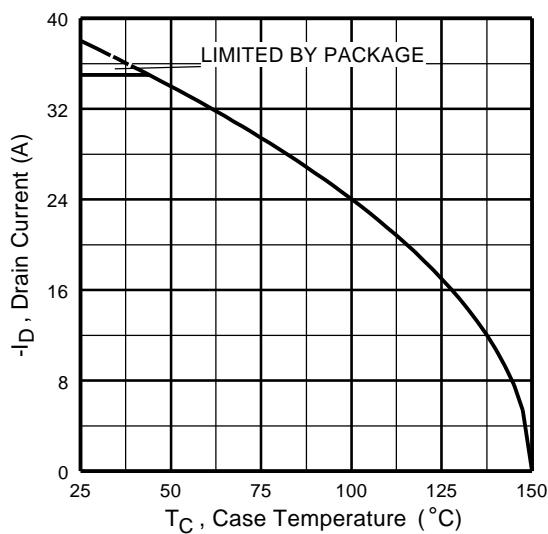
**Fig7.** Typical Source-Drain Diode  
Forward Voltage



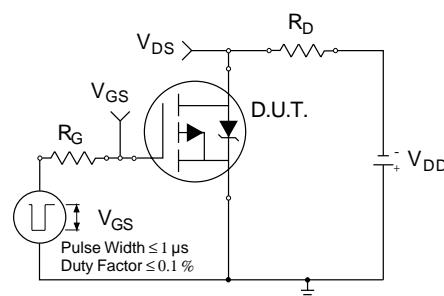
**Fig8.** Maximum Safe Operating Area

## IRHM9160

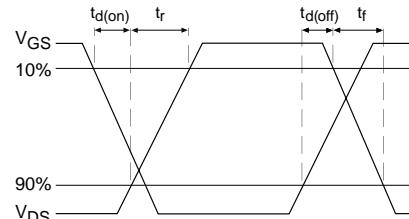
## Pre-Irradiation



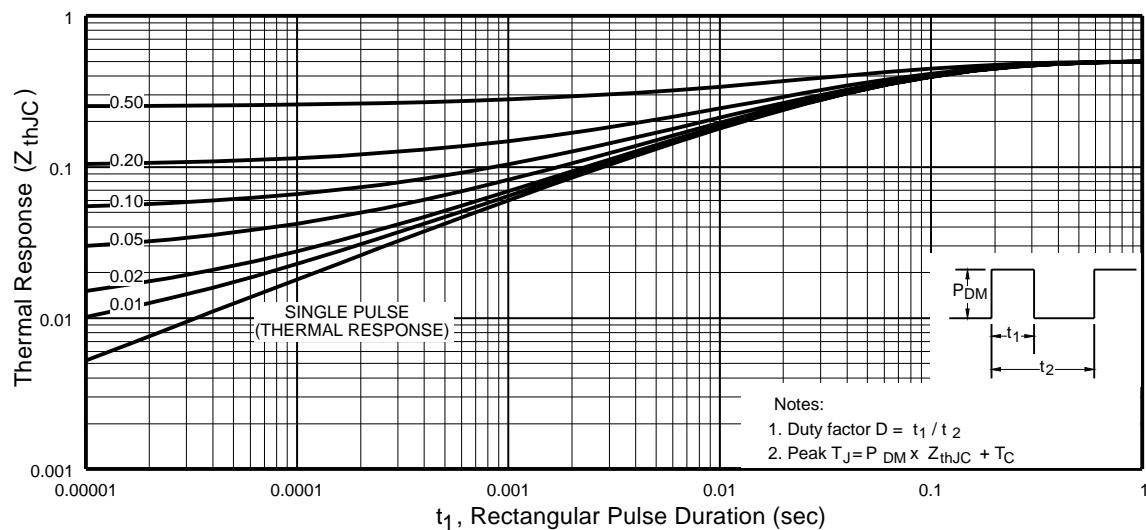
**Fig 9.** Maximum Drain Current Vs.  
Case Temperature



**Fig 10a.** Switching Time Test Circuit



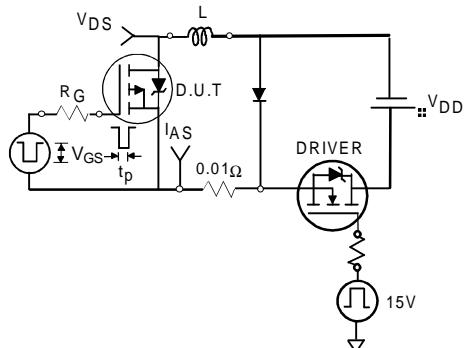
**Fig 10b.** Switching Time Waveforms



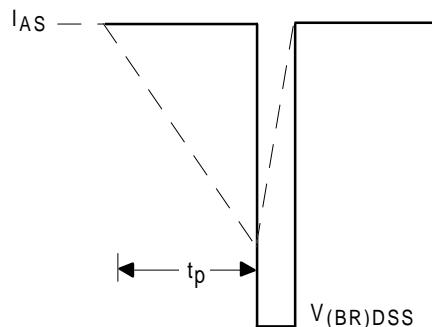
**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case

## Pre-Irradiation

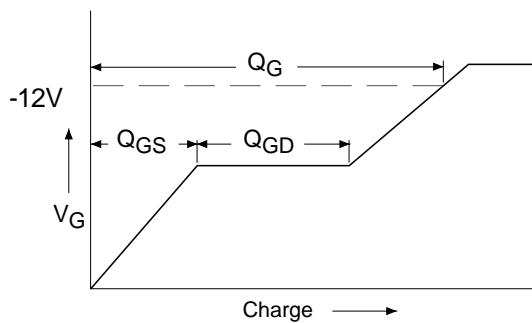
**IRHM9160**



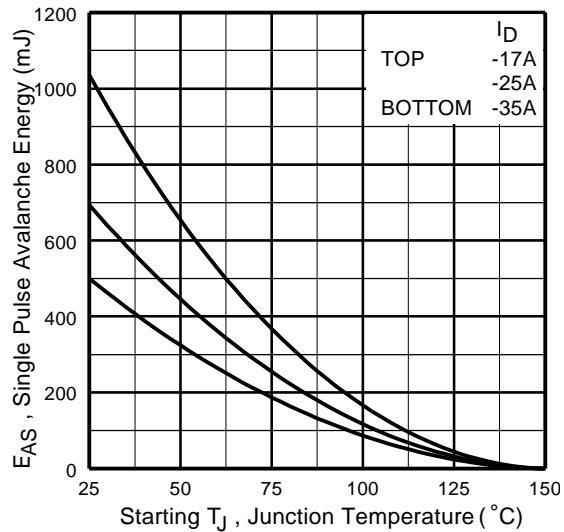
**Fig 12a.** Unclamped Inductive Test Circuit



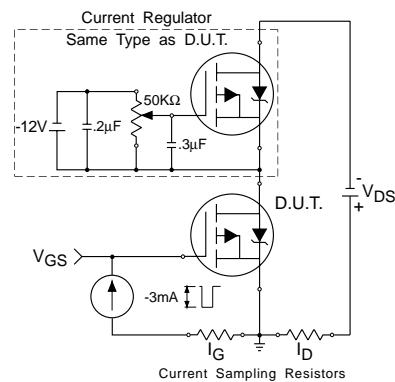
**Fig 12b.** Unclamped Inductive Waveforms



**Fig 13a.** Basic Gate Charge Waveform



**Fig 12c.** Maximum Avalanche Energy Vs. Drain Current



**Fig 13b.** Gate Charge Test Circuit

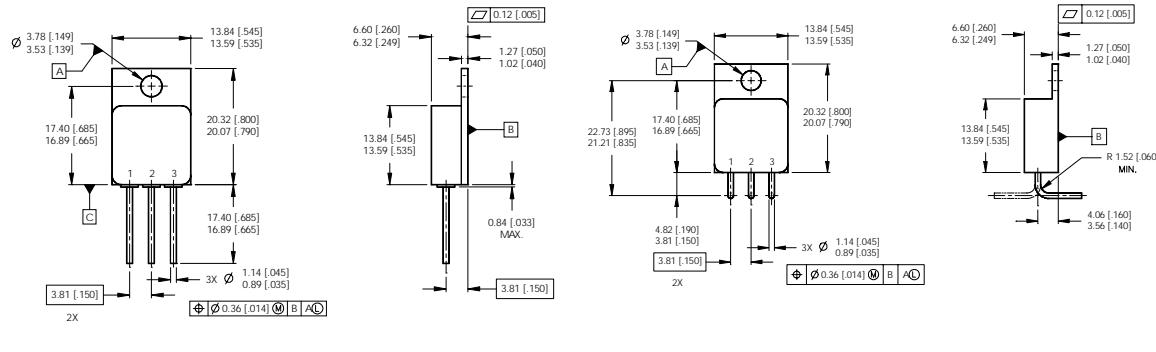
## IRHM9160

## Pre-Irradiation

### Foot Notes:

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ② V<sub>DD</sub> = -25V, starting T<sub>J</sub> = 25°C, L=0.82mH  
Peak I<sub>L</sub> = -35A, V<sub>GS</sub> = -12V
- ③ I<sub>SD</sub> ≤ -35A, di/dt ≤ -480A/μs,  
V<sub>DD</sub> ≤ -100V, T<sub>J</sub> ≤ 150°C
- ④ Pulse width ≤ 300 μs; Duty Cycle ≤ 2%
- ⑤ **Total Dose Irradiation with V<sub>GS</sub> Bias.**  
12 volt V<sub>GS</sub> applied and V<sub>DS</sub> = 0 during irradiation per MIL-STD-750, method 1019, condition A.
- ⑥ **Total Dose Irradiation with V<sub>DS</sub> Bias.**  
-80 volt V<sub>DS</sub> applied and V<sub>GS</sub> = 0 during irradiation per MIL-STD-750, method 1019, condition A.

### Case Outline and Dimensions — TO-254AA



NOTES:  
1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.  
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].  
3. CONTROLLING DIMENSION: INCH.  
4. CONFORMS TO JEDEC OUTLINE TO-254AA

PIN ASSIGNMENTS  
1 = DRAIN  
2 = SOURCE  
3 = GATE

### CAUTION BERYLLOID WARNING PER MIL-PRF-19500

Packages containing beryllia shall not be ground, sandblasted, machined or have other operations performed on them which will produce beryllia or beryllium dust. Furthermore, beryllium oxide packages shall not be placed in acids that will produce fumes containing beryllium.

International  
**IR** Rectifier

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*Data and specifications subject to change without notice. 12/01*