

International IR Rectifier

RADIATION HARDENED POWER MOSFET THRU-HOLE (MO-036)

Product Summary

Part Number	Radiation Level	R _{Ds(on)}	I _D
IRHG57110	100K Rads (Si)	0.29Ω	1.6A
IRHG53110	300K Rads (Si)	0.29Ω	1.6A
IRHG54110	600K Rads (Si)	0.29Ω	1.6A
IRHG58110	1000K Rads (Si)	0.31Ω	1.6A

International Rectifier's RAD-Hard™ HEXFET® MOSFET Technology provides high performance power MOSFETs for space applications. This technology has over a decade of proven performance and reliability in satellite applications. These devices have been characterized for both Total Dose and Single Event Effects (SEE). The combination of low R_{Ds(on)} and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well established advantages of MOSFETs such as voltage control, fast switching, ease of paralleling and temperature stability of electrical parameters.

Absolute Maximum Ratings (Per Die)

	Parameter	Units	Pre-Irradiation
I _D @ V _{GS} = 12V, T _C = 25°C	Continuous Drain Current	1.6	
I _D @ V _{GS} = 12V, T _C = 100°C	Continuous Drain Current	1.0	
I _{DM}	Pulsed Drain Current ①	6.4	
P _D @ T _C = 25°C	Max. Power Dissipation	1.4	W
	Linear Derating Factor	0.011	W/°C
V _{GS}	Gate-to-Source Voltage	±20	V
EAS	Single Pulse Avalanche Energy ②	130	mJ
I _{AR}	Avalanche Current ①	1.6	A
E _{AR}	Repetitive Avalanche Energy ①	0.14	mJ
dV/dt	Peak Diode Recovery dV/dt ③	6.5	V/ns
T _J	Operating Junction	-55 to 150	
T _{STG}	Storage Temperature Range		°C
	Lead Temperature	300 (0.63 in./1.6 mm from case for 10s)	
	Weight	1.3 (Typical)	g

For footnotes refer to the last page

IRHG57110

100V, Quad N-CHANNEL

RAD-Hard™ HEXFET®
RS TECHNOLOGY



Features:

- Single Event Effect (SEE) Hardened
- Low R_{Ds(on)}
- Low Total Gate Charge
- Proton Tolerant
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Ceramic Package
- Light Weight

Electrical Characteristics For Each N-Channel Device @ $T_J = 25^\circ\text{C}$ (Unless Otherwise Specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	100	—	—	V	$\text{V}_{\text{GS}} = 0\text{V}$, $\text{I}_D = 1.0\text{mA}$
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	0.14	—	$\text{V}/^\circ\text{C}$	Reference to 25°C , $\text{I}_D = 1.0\text{mA}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source On-State Resistance	—	—	0.29	Ω	$\text{V}_{\text{GS}} = 12\text{V}$, $\text{I}_D = 1.0\text{A}$ ④
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	2.0	—	4.0	V	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}$, $\text{I}_D = 1.0\text{mA}$
g_{fs}	Forward Transconductance	1.0	—	—	$\text{S} (\text{m}\text{A}/\text{V})$	$\text{V}_{\text{DS}} > 15\text{V}$, $\text{I}_{\text{DS}} = 1.0\text{A}$ ④
I_{DSS}	Zero Gate Voltage Drain Current	—	—	10	μA	$\text{V}_{\text{DS}} = 80\text{V}$, $\text{V}_{\text{GS}} = 0\text{V}$
		—	—	25		$\text{V}_{\text{DS}} = 80\text{V}$, $\text{V}_{\text{GS}} = 0\text{V}$, $T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Leakage Forward	—	—	100	nA	$\text{V}_{\text{GS}} = 20\text{V}$
I_{GSS}	Gate-to-Source Leakage Reverse	—	—	-100		$\text{V}_{\text{GS}} = -20\text{V}$
Q_{g}	Total Gate Charge	—	—	17	nC	$\text{V}_{\text{GS}} = 12\text{V}$, $\text{I}_D = 1.6\text{A}$,
Q_{gs}	Gate-to-Source Charge	—	—	4.4		$\text{V}_{\text{DS}} = 50\text{V}$
Q_{gd}	Gate-to-Drain ('Miller') Charge	—	—	3.9	ns	$\text{V}_{\text{DD}} = 50\text{V}$, $\text{I}_D = 1.6\text{A}$, $\text{V}_{\text{GS}} = 12\text{V}$, $\text{R}_G = 7.5\Omega$
$t_{\text{d(on)}}$	Turn-On Delay Time	—	—	21		
t_{r}	Rise Time	—	—	16		
$t_{\text{d(off)}}$	Turn-Off Delay Time	—	—	30		
t_{f}	Fall Time	—	—	15	nH	Measured from Drain lead (6mm /0.25in. from package) to Source lead (6mm /0.25in. from package) with Source wires internally bonded from Source Pin to Drain Pad
$\text{L}_{\text{S}} + \text{L}_{\text{D}}$	Total Inductance	—	10	—		
C_{iss}	Input Capacitance	—	370	—	pF	$\text{V}_{\text{GS}} = 0\text{V}$, $\text{V}_{\text{DS}} = 25\text{V}$ $f = 1.0\text{MHz}$
C_{oss}	Output Capacitance	—	110	—		
Crss	Reverse Transfer Capacitance	—	3.4	—		

Source-Drain Diode Ratings and Characteristics (Per Die)

	Parameter	Min	Typ	Max	Units	Test Conditions
I_{S}	Continuous Source Current (Body Diode)	—	—	1.6	A	$T_J = 25^\circ\text{C}$, $\text{I}_{\text{S}} = 1.6\text{A}$, $\text{V}_{\text{GS}} = 0\text{V}$ ④
I_{SM}	Pulse Source Current (Body Diode) ①	—	—	6.4		
V_{SD}	Diode Forward Voltage	—	—	1.2	V	$T_J = 25^\circ\text{C}$, $\text{I}_{\text{F}} = 1.6\text{A}$, $d\text{i}/dt \leq 100\text{A}/\mu\text{s}$
t_{rr}	Reverse Recovery Time	—	—	110	nS	$\text{V}_{\text{DD}} \leq 25\text{V}$ ④
Q_{RR}	Reverse Recovery Charge	—	—	380	nC	
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $\text{L}_{\text{S}} + \text{L}_{\text{D}}$.				

Thermal Resistance (Per Die)

	Parameter	Min	Typ	Max	Units	Test Conditions
R_{thJA}	Junction-to-Ambient	—	—	90	$^\circ\text{C}/\text{W}$	Typical socket mount

Note: Corresponding Spice and Saber models are available on the G&S Website.

For footnotes refer to the last page

Pre-Irradiation

IRHG57110

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

Table 1. Electrical Characteristics @ $T_j = 25^\circ\text{C}$, Post Total Dose Irradiation^{⑤⑥} (Per Die)

	Parameter	100K Rads(Si) ¹		300K to 1000K Rads(Si) ²		Units	Test Conditions
		Min	Max	Min	Max		
BV_{DSS}	Drain-to-Source Breakdown Voltage	100	—	100	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 1.0\text{mA}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	2.0	4.0	2.0	4.5	V	$\text{V}_{\text{GS}} = \text{V}_{\text{DS}}, \text{I}_D = 1.0\text{mA}$
I_{GSS}	Gate-to-Source Leakage Forward	—	100	—	100	nA	$\text{V}_{\text{GS}} = 20\text{V}$
I_{GSS}	Gate-to-Source Leakage Reverse	—	-100	—	-100	nA	$\text{V}_{\text{GS}} = -20\text{V}$
I_{DSS}	Zero Gate Voltage Drain Current	—	10	—	10	μA	$\text{V}_{\text{DS}} = 80\text{V}, \text{V}_{\text{GS}} = 0\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source ^④ On-State Resistance (TO-39)	—	0.226	—	0.246	Ω	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 1.0\text{A}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source ^④ On-State Resistance (MO-036AB)	—	0.29	—	0.31	Ω	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 1.0\text{A}$
V_{SD}	Diode Forward Voltage ^④	—	1.2	—	1.2	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_S = 1.6\text{A}$

1. Part number IRHG57110, IRHG53110, IRHG54110

2. Part number IRHG58110

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

Table 2. Single Event Effect Safe Operating Area (Per Die)

Ion	LET MeV/(mg/cm ²)	Energy (MeV)	Range (μm)	V_{DS} (V)					
				@ $\text{V}_{\text{GS}} = 0\text{V}$	@ $\text{V}_{\text{GS}} = -5\text{V}$	@ $\text{V}_{\text{GS}} = -10\text{V}$	@ $\text{V}_{\text{GS}} = -12.5\text{V}$	@ $\text{V}_{\text{GS}} = -15\text{V}$	@ $\text{V}_{\text{GS}} = -20\text{V}$
Br	36.7	309	39.5	100	100	100	100	100	80
I	59.8	341	32.5	100	100	100	90	25	20

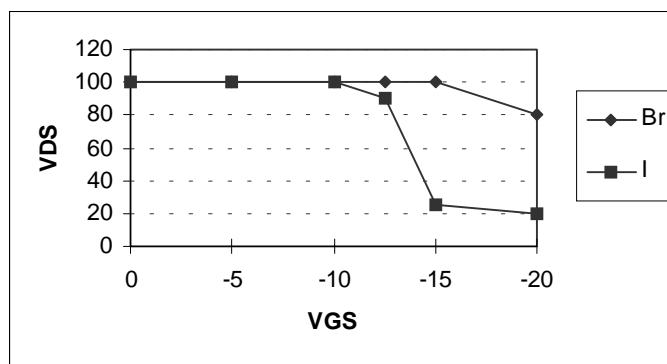


Fig a. Single Event Effect, Safe Operating Area

For footnotes refer to the last page

IRHG57110

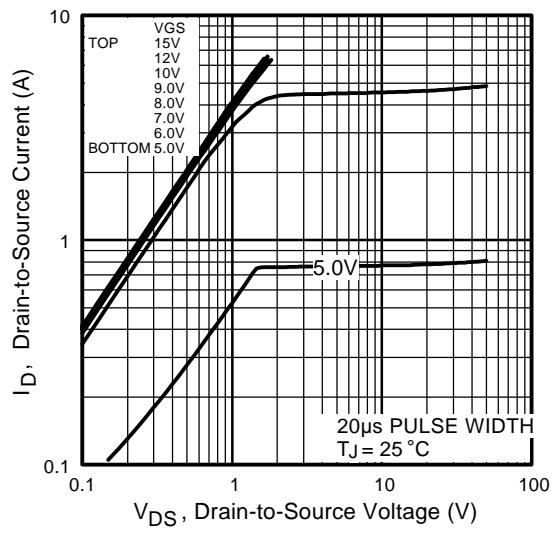


Fig 1. Typical Output Characteristics

Pre-Irradiation

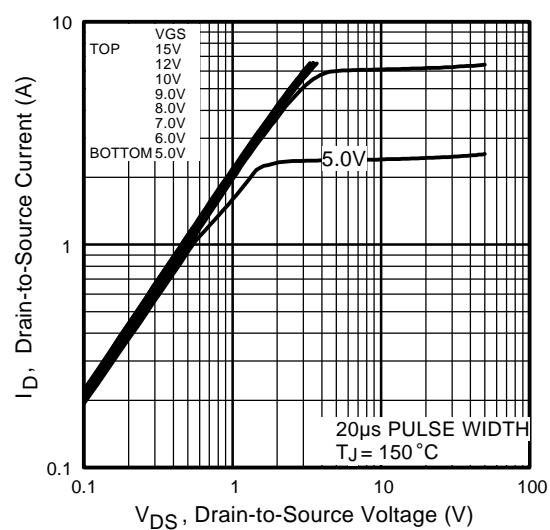


Fig 2. Typical Output Characteristics

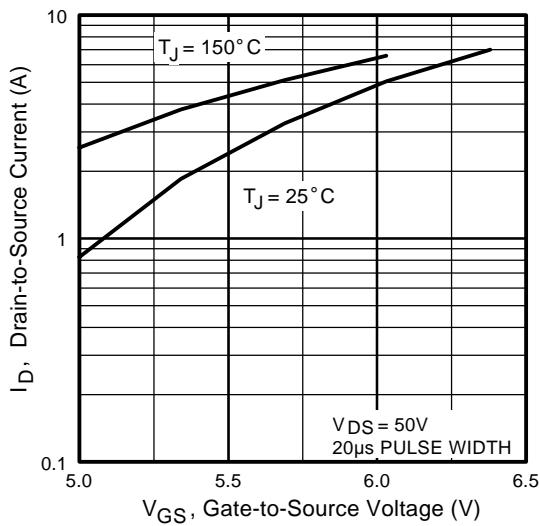


Fig 3. Typical Transfer Characteristics

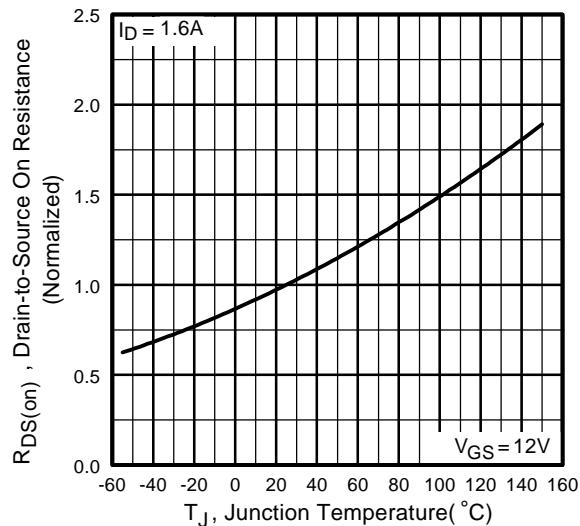


Fig 4. Normalized On-Resistance Vs. Temperature

Pre-Irradiation

IRHG57110

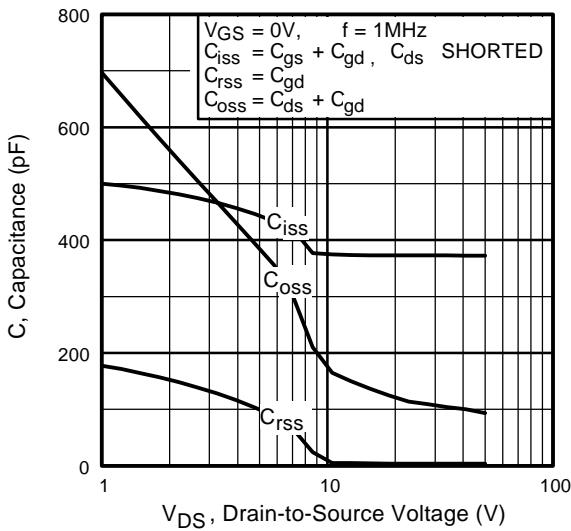


Fig 5. Typical Capacitance Vs.
Drain-to-Source Voltage

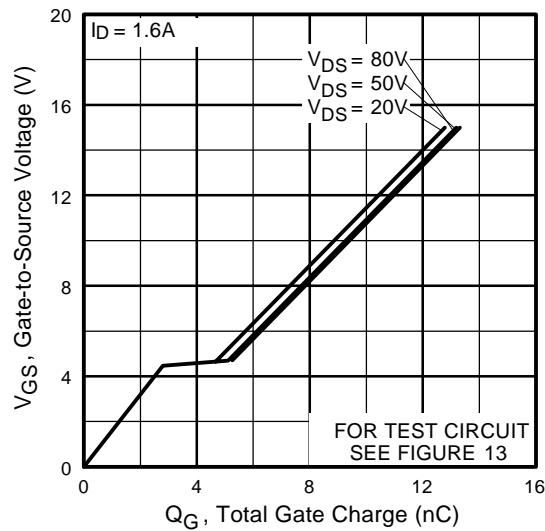


Fig 6. Typical Gate Charge Vs.
Gate-to-Source Voltage

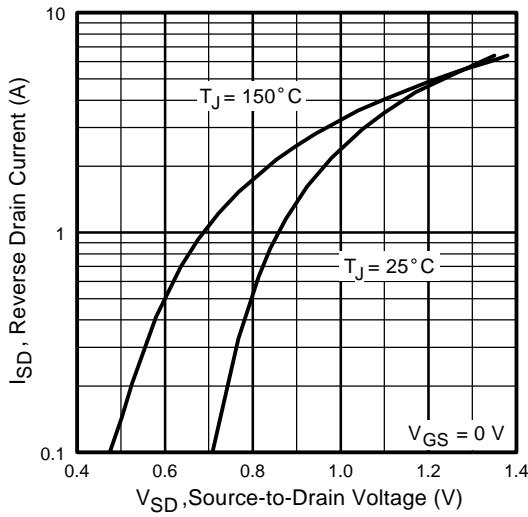


Fig 7. Typical Source-Drain Diode
Forward Voltage

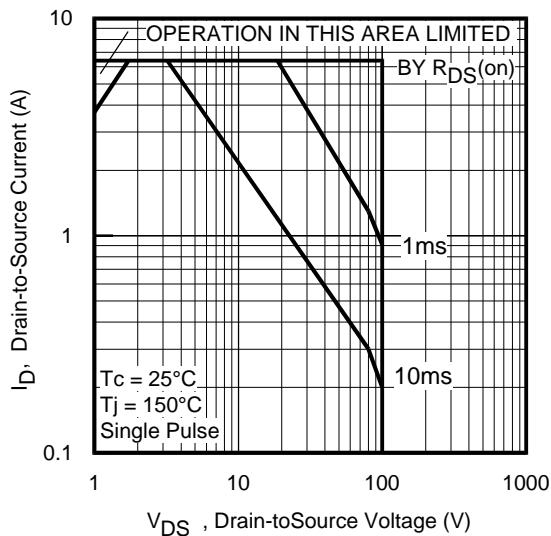


Fig 8. Maximum Safe Operating Area

IRHG57110

Pre-Irradiation

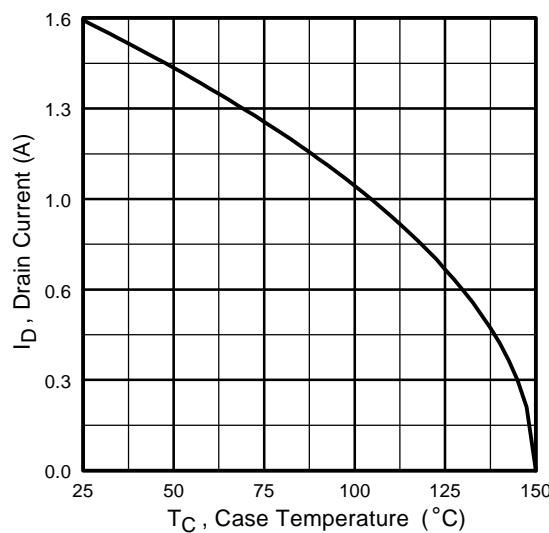


Fig 9. Maximum Drain Current Vs.
Case Temperature

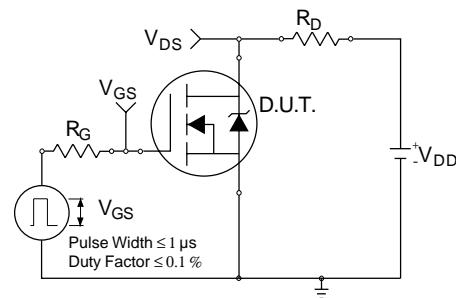


Fig 10a. Switching Time Test Circuit

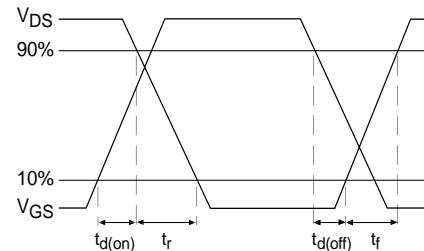


Fig 10b. Switching Time Waveforms

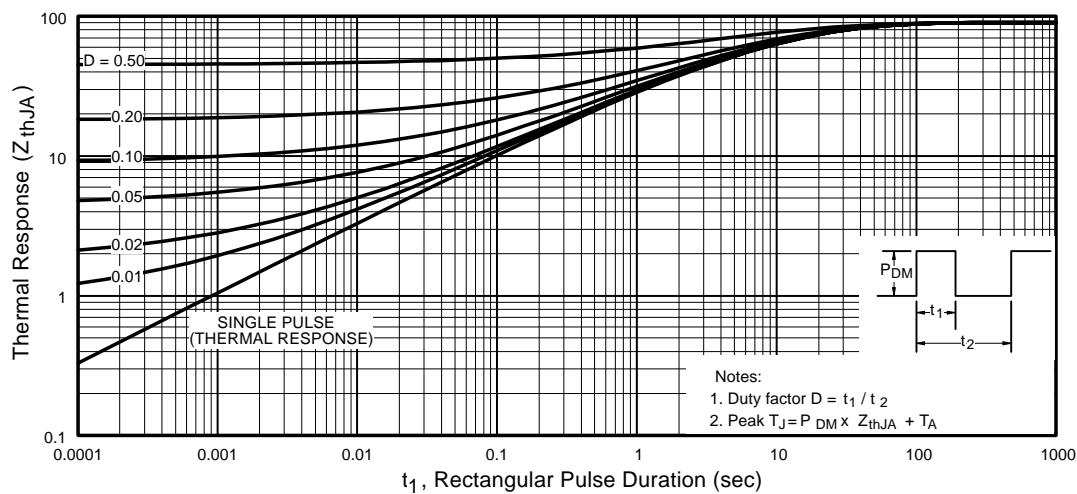


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

Pre-Irradiation

IRHG57110

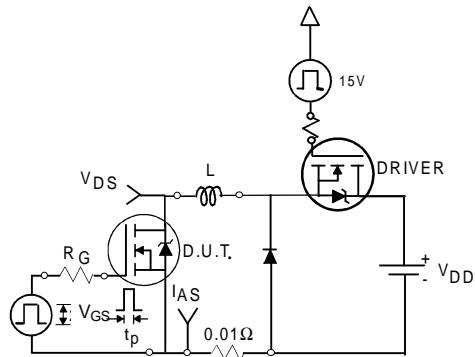


Fig 12a. Unclamped Inductive Test Circuit

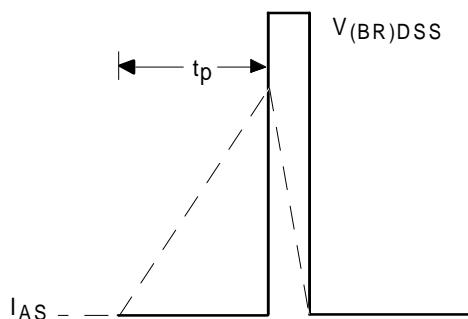


Fig 12b. Unclamped Inductive Waveforms

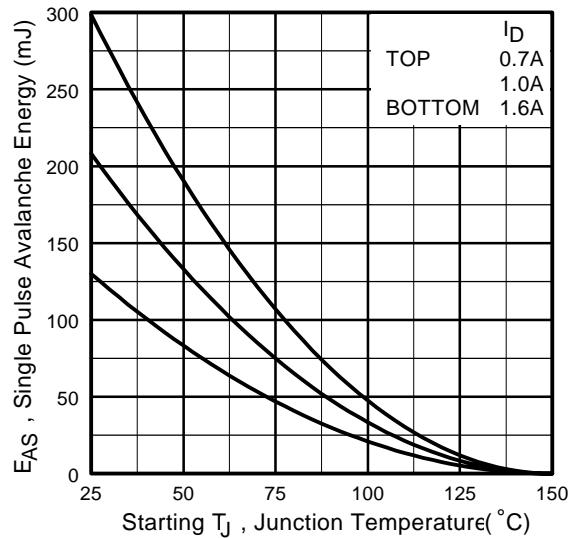


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

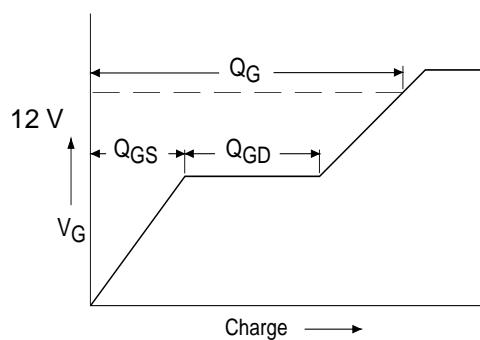


Fig 13a. Basic Gate Charge Waveform

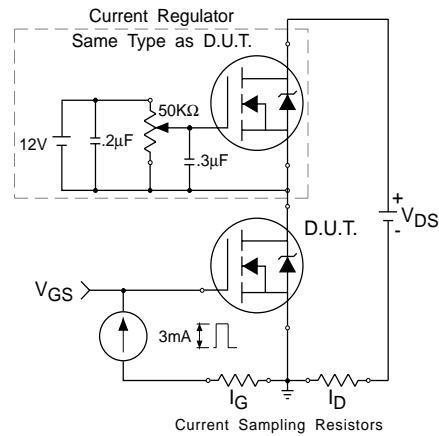
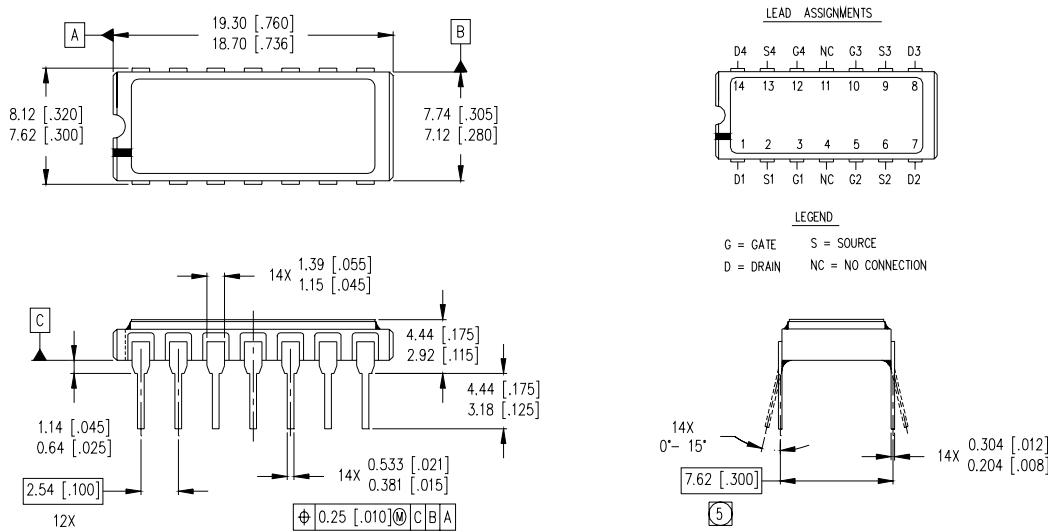


Fig 13b. Gate Charge Test Circuit

Footnotes:

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ② V_{DD} = 25V, starting T_J = 25°C, L = 100mH, Peak I_L = 1.6A, V_{GS} = 12V
- ③ I_{SD} ≤ 1.6A, di/dt ≤ 340A/μs, V_{DD} ≤ 100V, T_J ≤ 150°C
- ④ Pulse width ≤ 300 μs; Duty Cycle ≤ 2%
- ④ Pulse width ≤ 300 μs; Duty Cycle ≤ 2%
- ⑤ **Total Dose Irradiation with V_{GS} Bias.**
12 volt V_{GS} applied and V_{DS} = 0 during irradiation per MIL-STD-750, method 1019, condition A
- ⑥ **Total Dose Irradiation with V_{DS} Bias.**
80 volt V_{DS} applied and V_{GS} = 0 during irradiation per MIL-STD-750, method 1019, condition A

Case Outline and Dimensions — MO-036AB**NOTES:**

1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
4. OUTLINE CONFORMS TO JEDEC OUTLINE MO-036AB.
5. MEASURED WITH THE LEADS CONSTRAINED TO BE PERPENDICULAR TO DATUM PLANE C.

International
IR Rectifier

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105
TAC Fax: (310) 252-7903

Visit us at www.irf.com for sales contact information.
Data and specifications subject to change without notice. 04/02