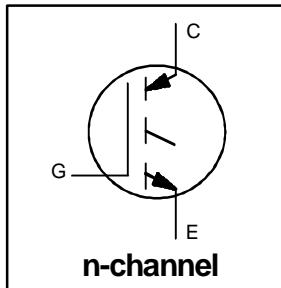


INSULATED GATE BIPOLEAR TRANSISTOR

**Short Circuit Rated
Fast IGBT**

Features

- Short circuit rated - 10 μ s @ 125°C, $V_{GE} = 15V$
- Switching-loss rating includes all "tail" losses
- Optimized for medium operating frequency (1 to 10kHz) See Fig. 1 for Current vs. Frequency curve

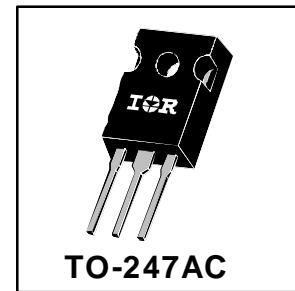


$V_{CES} = 1200V$
 $V_{CE(sat)} \leq 2.9V$
 @ $V_{GE} = 15V, I_C = 23A$

Description

Insulated Gate Bipolar Transistors (IGBTs) from International Rectifier have higher usable current densities than comparable bipolar transistors, while at the same time having simpler gate-drive requirements of the familiar power MOSFET. They provide substantial benefits to a host of high-voltage, high-current applications.

These new short circuit rated devices are especially suited for motor control and other applications requiring short circuit withstand capability.



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Voltage	1200	V
$I_C @ T_C = 25^\circ C$	Continuous Collector Current	42	A
$I_C @ T_C = 100^\circ C$	Continuous Collector Current	23	
I_{CM}	Pulsed Collector Current ①	84	
I_{LM}	Clamped Inductive Load Current ②	84	
t_{sc}	Short Circuit Withstand Time	10	μs
V_{GE}	Gate-to-Emitter Voltage	± 20	V
E_{ARV}	Reverse Voltage Avalanche Energy ③	20	mJ
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	200	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	78	
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to +150	$^\circ C$
	Soldering Temperature, for 10 sec.	300 (0.063 in. (1.6mm) from case)	
	Mounting torque, 6-32 or M3 screw.	10 lbf•in (1.1N•m)	

Thermal Resistance

	Parameter	Min.	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	—	—	0.64	$^\circ C/W$
$R_{\theta CS}$	Case-to-Sink, flat, greased surface	—	0.24	—	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount	—	—	40	
Wt	Weight	—	6 (0.21)	—	
					g (oz)

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{CES}}$	Collector-to-Emitter Breakdown Voltage	1200	—	—	V	$V_{GE} = 0V, I_C = 250\mu\text{A}$
$V_{(\text{BR})\text{ECS}}$	Emitter-to-Collector Breakdown Voltage ④	20	—	—	V	$V_{GE} = 0V, I_C = 1.0\text{A}$
$\Delta V_{(\text{BR})\text{CES}/\Delta T_J}$	Temp. Coeff. of Breakdown Voltage	—	1.1	—	V/ $^\circ\text{C}$	$V_{GE} = 0V, I_C = 1.0\text{mA}$
$V_{CE(\text{on})}$	Collector-to-Emitter Saturation Voltage	—	2.3	2.9	V	$I_C = 23\text{A}, V_{GE} = 15\text{V}$
		—	3.0	—		$I_C = 42\text{A}$
		—	2.8	—		$I_C = 23\text{A}, T_J = 150^\circ\text{C}$
		3.0	—	5.5		$V_{CE} = V_{GE}, I_C = 250\mu\text{A}$
$\Delta V_{GE(\text{th})/\Delta T_J}$	Temp. Coeff. of Threshold Voltage	—	-13	—	mV/ $^\circ\text{C}$	$V_{CE} = V_{GE}, I_C = 250\mu\text{A}$
g_{fe}	Forward Transconductance ⑤	11	15	—	S	$V_{CE} = 100\text{V}, I_C = 23\text{A}$
I_{CES}	Zero Gate Voltage Collector Current	—	—	250	μA	$V_{GE} = 0V, V_{CE} = 1200\text{V}$
		—	—	2000		$V_{GE} = 0V, V_{CE} = 1200\text{V}, T_J = 150^\circ\text{C}$
I_{GES}	Gate-to-Emitter Leakage Current	—	—	± 100	nA	$V_{GE} = \pm 20\text{V}$

Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge (turn-on)	—	89	130	nC	$I_C = 23\text{A}$
Q_{ge}	Gate - Emitter Charge (turn-on)	—	22	33		$V_{CC} = 400\text{V}$
Q_{gc}	Gate - Collector Charge (turn-on)	—	26	39		$V_{GE} = 15\text{V}$
$t_{d(on)}$	Turn-On Delay Time	—	42	—	ns	$T_J = 25^\circ\text{C}$
t_r	Rise Time	—	32	—		$I_C = 23\text{A}, V_{CC} = 960\text{V}$
$t_{d(off)}$	Turn-Off Delay Time	—	280	820		$V_{GE} = 15\text{V}, R_G = 5.0\Omega$
t_f	Fall Time	—	190	410		Energy losses include "tail"
E_{on}	Turn-On Switching Loss	—	1.6	—	mJ	See Fig. 9, 10, 11, 14
E_{off}	Turn-Off Switching Loss	—	3.3	—		
E_{ts}	Total Switching Loss	—	4.9	11		
t_{sc}	Short Circuit Withstand Time	10	—	—	μs	$V_{CC} = 720\text{V}, T_J = 125^\circ\text{C}$ $V_{GE} = 15\text{V}, R_G = 5.0\Omega, V_{CPK} < 1000\text{V}$
$t_{d(on)}$	Turn-On Delay Time	—	32	—	ns	$T_J = 150^\circ\text{C},$ $I_C = 23\text{A}, V_{CC} = 960\text{V}$
t_r	Rise Time	—	21	—		$V_{GE} = 15\text{V}, R_G = 5.0\Omega$
$t_{d(off)}$	Turn-Off Delay Time	—	490	—		Energy losses include "tail"
t_f	Fall Time	—	440	—		See Fig. 10, 14
E_{ts}	Total Switching Loss	—	10	—	mJ	Measured 5mm from package
L_E	Internal Emitter Inductance	—	13	—	nH	
C_{ies}	Input Capacitance	—	1900	—	pF	$V_{GE} = 0\text{V}$
C_{oes}	Output Capacitance	—	140	—		$V_{CC} = 30\text{V}$
C_{res}	Reverse Transfer Capacitance	—	24	—		$f = 1.0\text{MHz}$

Notes:

① Repetitive rating; $V_{GE}=20\text{V}$, pulse width limited by max. junction temperature.
(See fig. 13b)

② $V_{CC}=80\%(V_{CES})$, $V_{GE}=20\text{V}$, $L=10\mu\text{H}$, $R_G=5.0\Omega$, (See fig. 13a)

③ Repetitive rating; pulse width limited by maximum junction temperature.

④ Pulse width $\leq 80\mu\text{s}$; duty factor $\leq 0.1\%$.

⑤ Pulse width $5.0\mu\text{s}$, single shot.

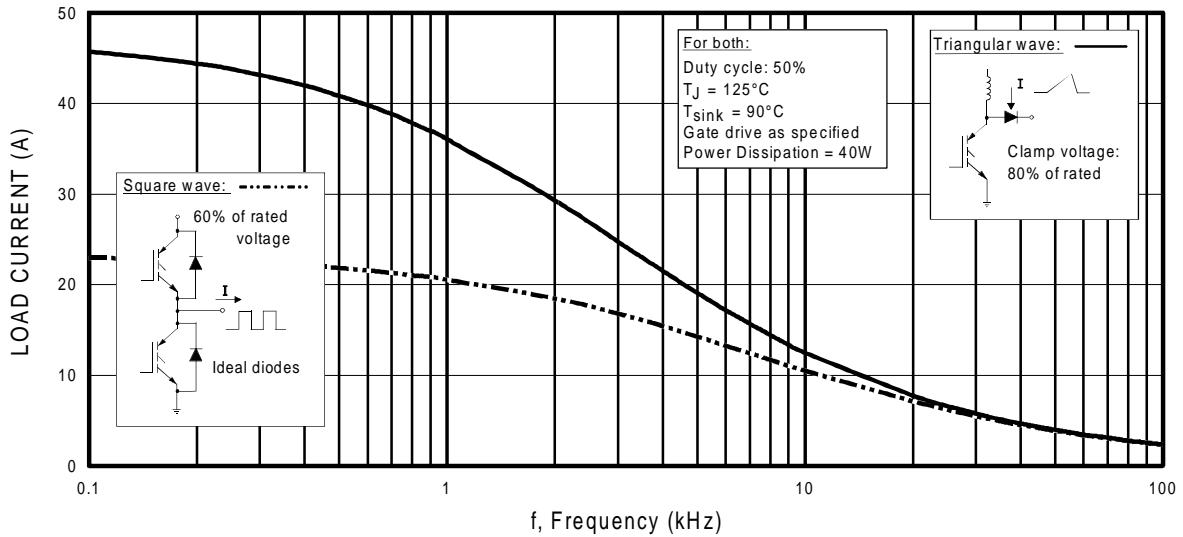


Fig. 1 - Typical Load Current vs. Frequency
(For square wave, $I=I_{RMS}$ of fundamental; for triangular wave, $I=I_{PK}$)

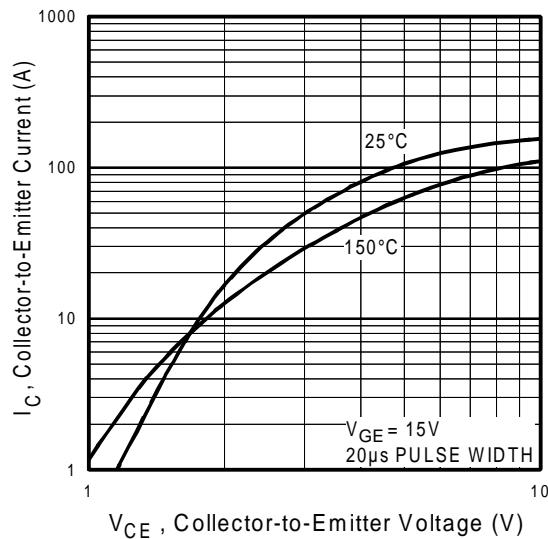


Fig. 2 - Typical Output Characteristics

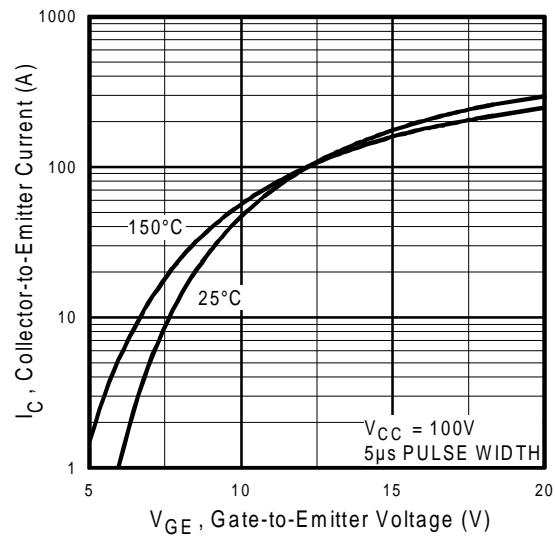


Fig. 3 - Typical Transfer Characteristics

IRGPH50M

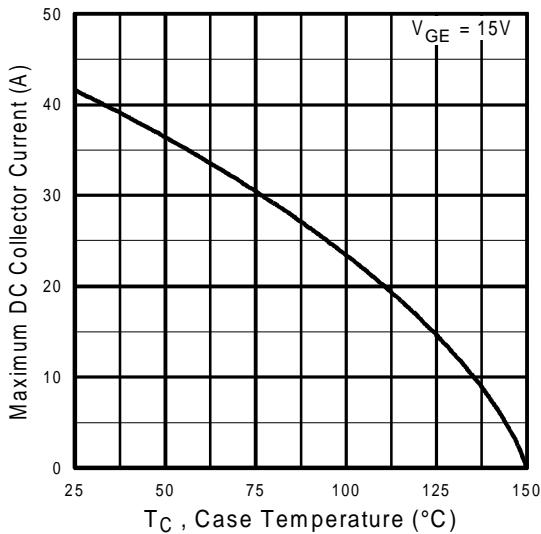


Fig. 4 - Maximum Collector Current vs. Case Temperature

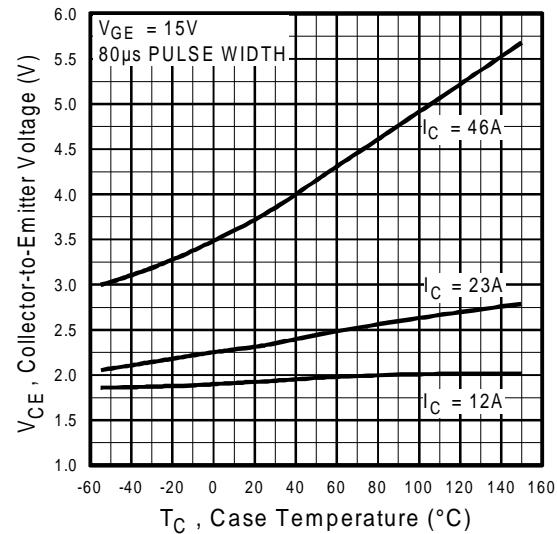


Fig. 5 - Collector-to-Emitter Voltage vs. Case Temperature

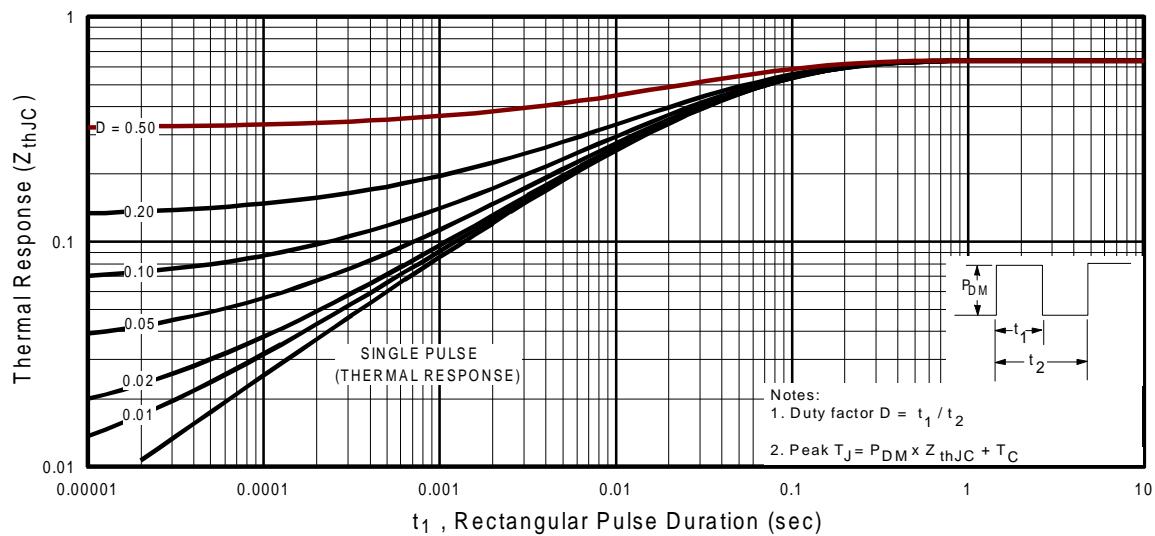


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

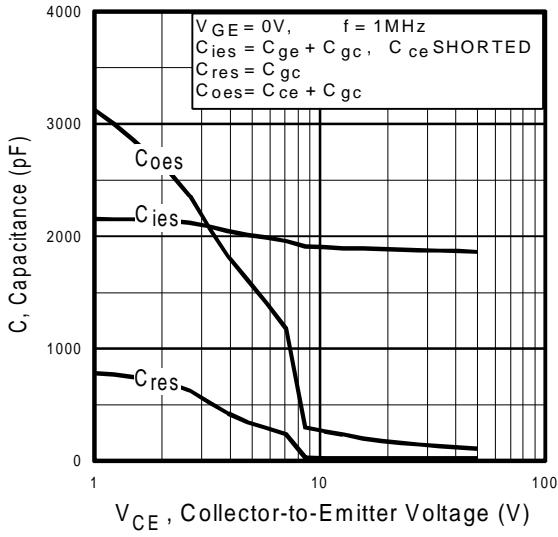


Fig. 7 - Typical Capacitance vs. Collector-to-Emitter Voltage

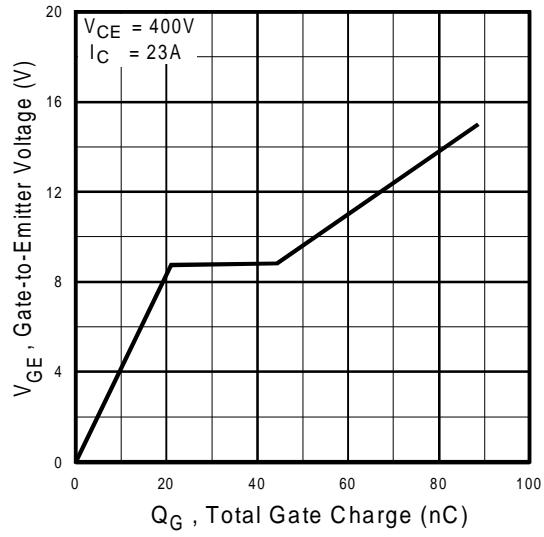


Fig. 8 - Typical Gate Charge vs. Gate-to-Emitter Voltage

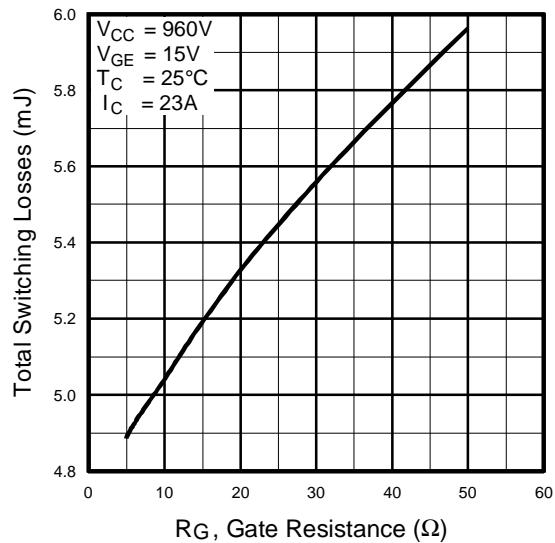


Fig. 9 - Typical Switching Losses vs. Gate Resistance

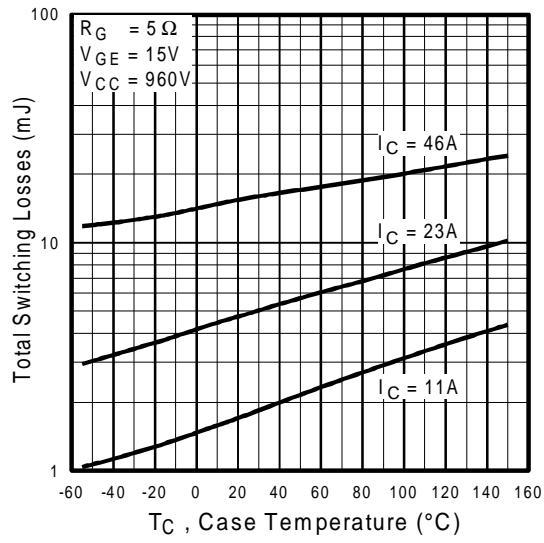


Fig. 10 - Typical Switching Losses vs. Case Temperature

IRGPH50M

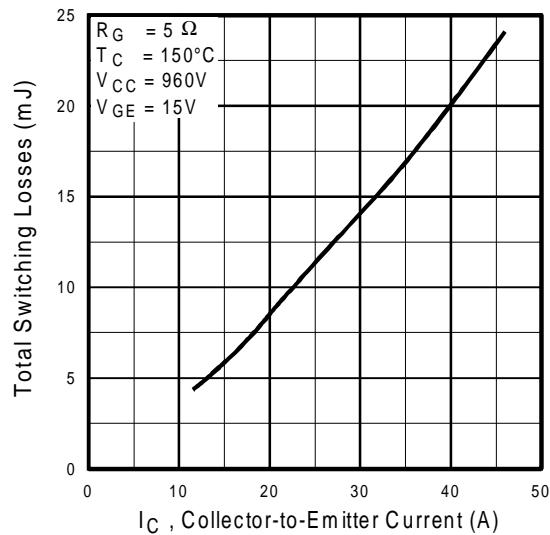


Fig. 11 - Typical Switching Losses vs.
Collector-to-Emitter Current

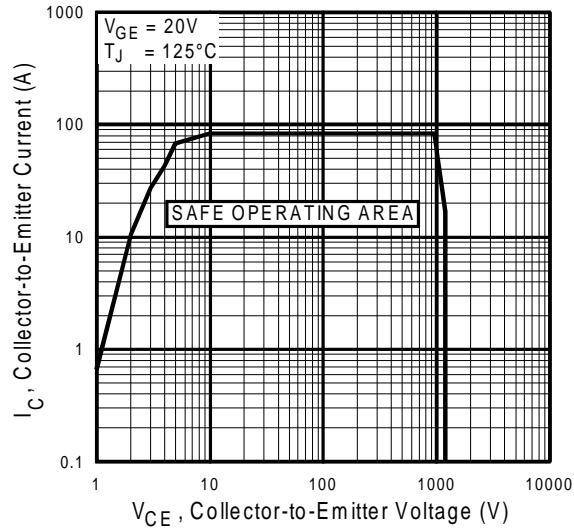


Fig. 12 - Turn-Off SOA

Refer to Section D for the following:

Appendix G: Section D - page D-9

- Fig. 13a - Clamped Inductive Load Test Circuit
- Fig. 13b - Pulsed Collector Current Test Circuit
- Fig. 14a - Switching Loss Test Circuit
- Fig. 14b - Switching Loss Waveform

Package Outline 3 - JEDEC Outline TO-247AC

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