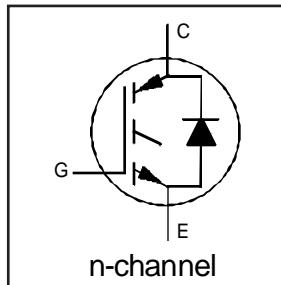


**INSULATED GATE BIPOLAR TRANSISTOR  
WITH ULTRAFAST SOFT RECOVERY DIODE**

**Short Circuit Rated  
UltraFast CoPack IGBT**

**Features**

- Short circuit rated  $-10\mu\text{s}$  @ $125^\circ\text{C}$ ,  $V_{GE} = 15\text{V}$
- Switching-loss rating includes all "tail" losses
- HEXFRED™ soft ultrafast diodes
- Optimized for high operating frequency (over 5kHz)  
See Fig. 1 for Current vs. Frequency curve

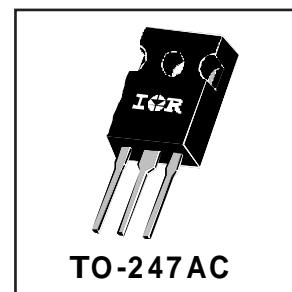


$V_{CES} = 600\text{V}$
$V_{CE(\text{sat})} \leq 3.5\text{V}$
@ $V_{GE} = 15\text{V}$ , $I_C = 6.0\text{A}$

**Description**

Co-packaged IGBTs are a natural extension of International Rectifier's well known IGBT line. They provide the convenience of an IGBT and an ultrafast recovery diode in one package, resulting in substantial benefits to a host of high-voltage, high-current, applications.

These new short circuit rated devices are especially suited for motor control and other applications requiring short circuit withstand capability.



**Absolute Maximum Ratings**

	Parameter	Max.	Units
$V_{CES}$	Collector-to-Emitter Voltage	600	V
$I_C @ T_C = 25^\circ\text{C}$	Continuous Collector Current	10	A
$I_C @ T_C = 100^\circ\text{C}$	Continuous Collector Current	6.0	
$I_{CM}$	Pulsed Collector Current ①	20	
$I_{LM}$	Clamped Inductive Load Current ②	20	
$I_F @ T_C = 100^\circ\text{C}$	Diode Continuous Foward Current	7.0	
$I_{FM}$	Diode Maximum Forward Current	20	
$t_{sc}$	Short Circuit Withstand Time	10	$\mu\text{s}$
$V_{GE}$	Gate-to-Emitter Voltage	$\pm 20$	V
$P_D @ T_C = 25^\circ\text{C}$	Maximum Power Dissipation	60	W
$P_D @ T_C = 100^\circ\text{C}$	Maximum Power Dissipation	24	
$T_J$ $T_{STG}$	Operating Junction and Storage Temperature Range	-55 to +150	$^\circ\text{C}$
	Soldering Temperature, for 10 sec.	300 (0.063 in. (1.6mm) from case)	
	Mounting Torque, 6-32 or M3 Screw.	10 lbf·in (1.1 Nm)	

**Thermal Resistance**

	Parameter	Min.	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case - IGBT	-----	-----	2.1	$^\circ\text{C/W}$
$R_{\theta JC}$	Junction-to-Case - Diode	-----	-----	3.5	
$R_{\theta CS}$	Case-to-Sink, flat, greased surface	-----	0.24	-----	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount	-----	-----	40	
Wt	Weight	-----	6 (0.21)	-----	g (oz)

# IRGPC20KD2



## Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{CES}}$	Collector-to-Emitter Breakdown Voltage	600	----	----	V	$V_{\text{GE}} = 0\text{V}$ , $I_C = 250\mu\text{A}$
$\Delta V_{(\text{BR})\text{CES}/\Delta T_J}$	Temperature Coeff. of Breakdown Voltage	0.37	----	----	$\text{V}^\circ\text{C}$	$V_{\text{GE}} = 0\text{V}$ , $I_C = 1.0\text{mA}$
$V_{\text{CE}(\text{on})}$	Collector-to-Emitter Saturation Voltage	2.4	3.5	----	V	$I_C = 6.0\text{A}$ $V_{\text{GE}} = 15\text{V}$
		3.6	----	----		$I_C = 10\text{A}$ See Fig. 2, 5
		2.9	----	----		$I_C = 6.0\text{A}$ , $T_J = 150^\circ\text{C}$
$V_{\text{GE}(\text{th})}$	Gate Threshold Voltage	3.0	----	5.5	----	$V_{\text{CE}} = V_{\text{GE}}$ , $I_C = 250\mu\text{A}$
$\Delta V_{\text{GE}(\text{th})/\Delta T_J}$	Temperature Coeff. of Threshold Voltage	----	-11	----	$\text{mV}^\circ\text{C}$	$V_{\text{CE}} = V_{\text{GE}}$ , $I_C = 250\mu\text{A}$
$g_{\text{fe}}$	Forward Transconductance ④	1.9	3.3	----	S	$V_{\text{CE}} = 100\text{V}$ , $I_C = 6.0\text{A}$
$I_{\text{CES}}$	Zero Gate Voltage Collector Current	----	----	250	$\mu\text{A}$	$V_{\text{GE}} = 0\text{V}$ , $V_{\text{CE}} = 600\text{V}$
		----	----	1700	V	$V_{\text{GE}} = 0\text{V}$ , $V_{\text{CE}} = 600\text{V}$ , $T_J = 150^\circ\text{C}$
$V_{\text{FM}}$	Diode Forward Voltage Drop	----	1.4	1.7		$I_C = 8.0\text{A}$ See Fig. 13
		----	1.3	1.6		$I_C = 8.0\text{A}$ , $T_J = 150^\circ\text{C}$
$I_{\text{GES}}$	Gate-to-Emitter Leakage Current	----	----	$\pm 100$	nA	$V_{\text{GE}} = \pm 20\text{V}$

## Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$Q_g$	Total Gate Charge (turn-on)	----	17	26	nC	$I_C = 6.0\text{A}$
$Q_{ge}$	Gate - Emitter Charge (turn-on)	----	4.3	6.8		$V_{\text{CC}} = 400\text{V}$
$Q_{gc}$	Gate - Collector Charge (turn-on)	----	6.4	11		See Fig. 8
$t_{d(\text{on})}$	Turn-On Delay Time	----	59	----	ns	$T_J = 25^\circ\text{C}$
$t_r$	Rise Time	----	38	----		$I_C = 6.0\text{A}$ , $V_{\text{CC}} = 480\text{V}$
$t_{d(\text{off})}$	Turn-Off Delay Time	----	110	210		$V_{\text{GE}} = 15\text{V}$ , $R_G = 50\Omega$
$t_f$	Fall Time	----	80	120	mJ	Energy losses include "tail" and diode reverse recovery. See Fig. 9, 10, 11, 18
$E_{\text{on}}$	Turn-On Switching Loss	----	0.28	----		
$E_{\text{off}}$	Turn-Off Switching Loss	----	0.15	----		
$E_{ts}$	Total Switching Loss	----	0.43	0.90	μs	$V_{\text{CC}} = 360\text{V}$ , $T_J = 125^\circ\text{C}$ $V_{\text{GE}} = 15\text{V}$ , $R_G = 50\Omega$ , $V_{\text{CPK}} < 500\text{V}$
$t_{sc}$	Short Circuit Withstand Time	10	----	----	ns	$T_J = 150^\circ\text{C}$ , See Fig. 9, 10, 11, 18
$t_{d(\text{on})}$	Turn-On Delay Time	----	52	----		$I_C = 6.0\text{A}$ , $V_{\text{CC}} = 480\text{V}$
$t_r$	Rise Time	----	35	----		$V_{\text{GE}} = 15\text{V}$ , $R_G = 50\Omega$
$t_{d(\text{off})}$	Turn-Off Delay Time	----	170	----	mJ	Energy losses include "tail" and diode reverse recovery. See Fig. 9, 10, 11, 18
$t_f$	Fall Time	----	170	----		
$E_{ts}$	Total Switching Loss	----	0.65	----		
$L_E$	Internal Emitter Inductance	----	13	----	nH	Measured 5mm from package
$C_{\text{ies}}$	Input Capacitance	----	350	----	pF	$V_{\text{GE}} = 0\text{V}$
$C_{\text{oes}}$	Output Capacitance	----	45	----		$V_{\text{CC}} = 30\text{V}$ See Fig. 7
$C_{\text{res}}$	Reverse Transfer Capacitance	----	4.7	----		$f = 1.0\text{MHz}$
$t_{rr}$	Diode Reverse Recovery Time	----	37	55	ns	$T_J = 25^\circ\text{C}$ See Fig.
		----	55	90		$T_J = 125^\circ\text{C}$ 14
$I_{rr}$	Diode Peak Reverse Recovery Current	----	3.5	5.0	A	$T_J = 25^\circ\text{C}$ See Fig.
		----	4.5	8.0		$T_J = 125^\circ\text{C}$ 15
$Q_{rr}$	Diode Reverse Recovery Charge	----	65	138	nC	$T_J = 25^\circ\text{C}$ See Fig.
		----	124	360		$T_J = 125^\circ\text{C}$ 16
$t_s$	$d_{(\text{rec})M}/dt$ Diode Peak Rate of Fall of Recovery	----	----	----	During $t_b$	di/dt = 200A/240
		A/μs	$T_J = 25^\circ\text{C}$ See Fig.	----		210

### Notes:

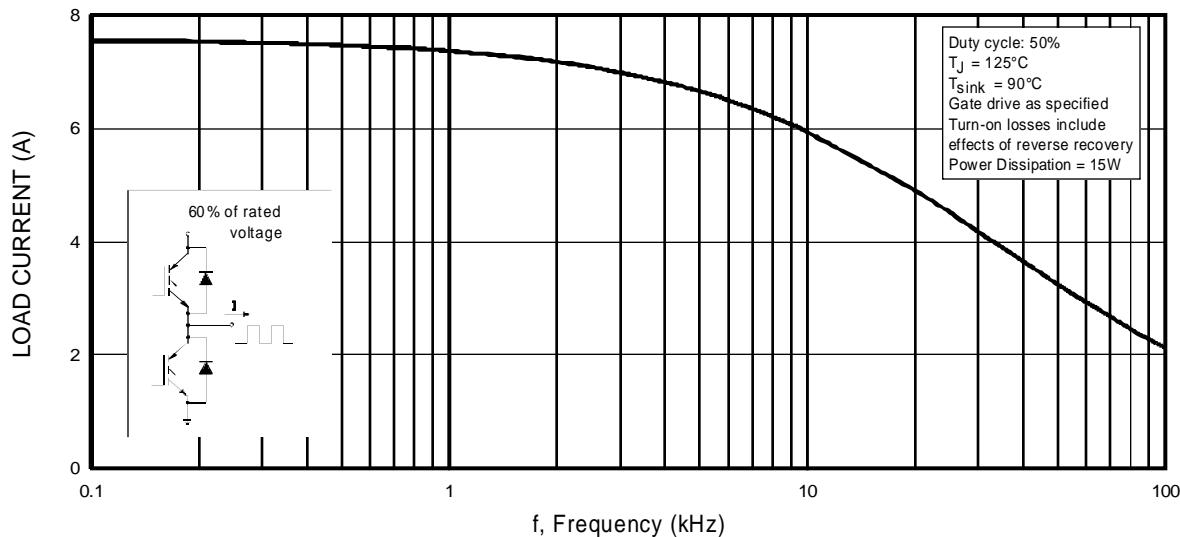
$T_J = 125^\circ\text{C}$ ,  $V_{\text{CC}} = 80\%$ ( $V_{\text{CES}}$ ),  $V_{\text{GE}} = 20\text{V}$ ,  $L = 10\mu\text{H}$ , ④ Pulse width 5.0μs,

① Repetitive rating;  $V_{\text{GE}} = 20\text{V}$ , pulse width limited by max. junction temperature. ( See fig. 20 )

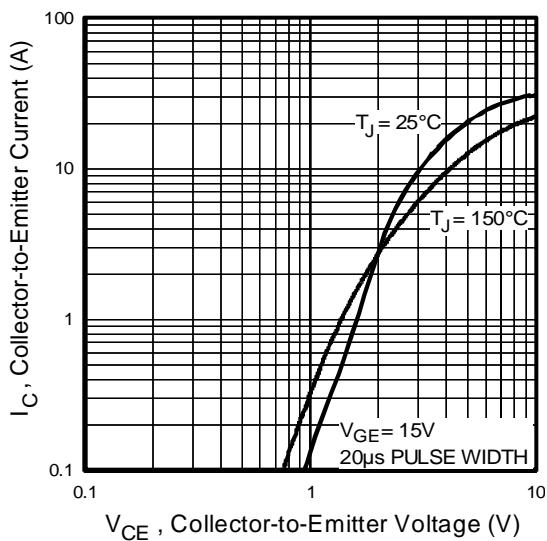
$R_G = 50\Omega$ , ( See fig. 19 )

single shot.

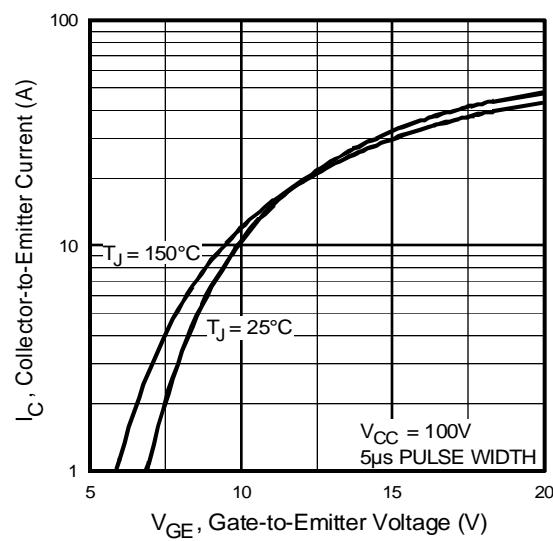
③ Pulse width ≤ 80μs; duty factor ≤ 0.1%.



**Fig. 1 - Typical Load Current vs. Frequency**  
(Load Current =  $I_{RMS}$  of fundamental)

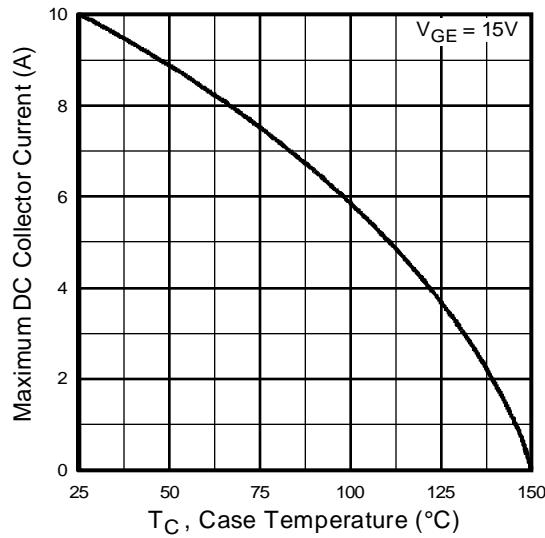


**Fig. 2 - Typical Output Characteristics**

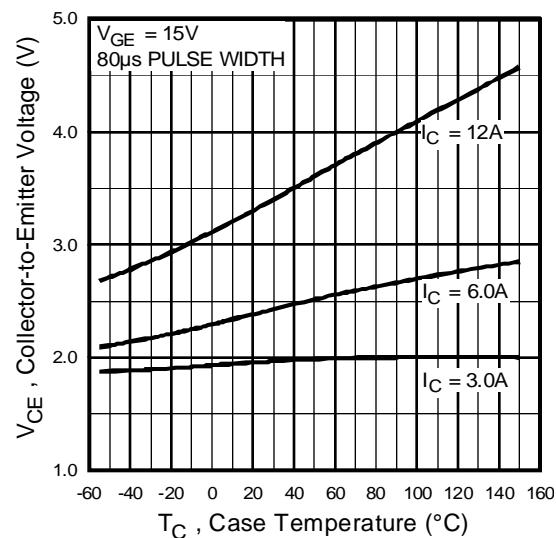


**Fig. 3 - Typical Transfer Characteristics**

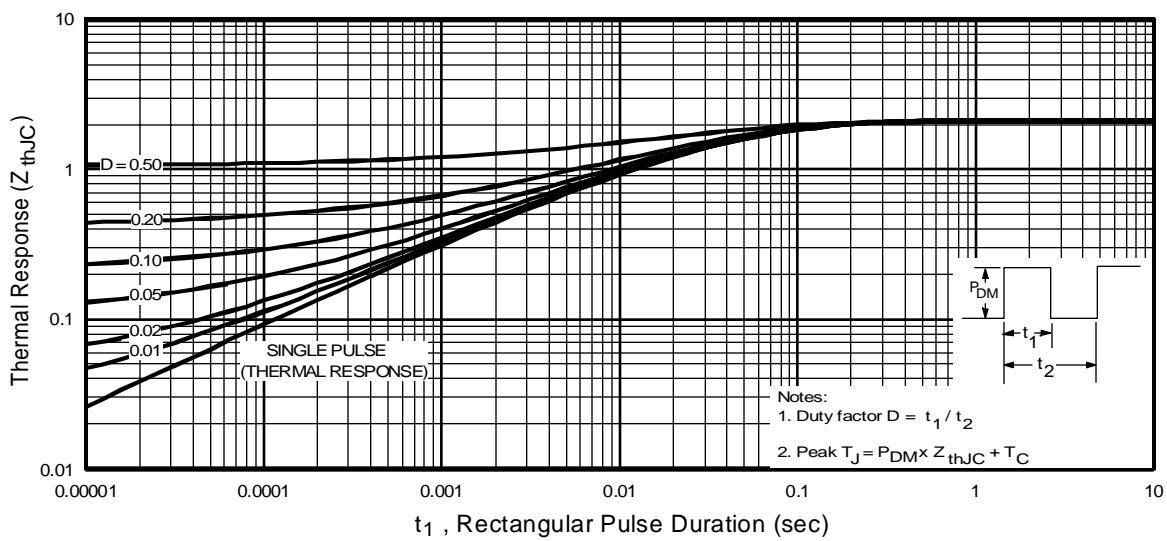
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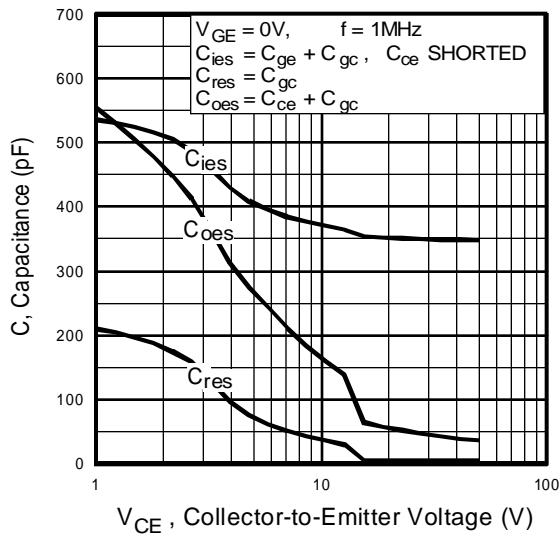
**Fig. 4 - Maximum Collector Current vs. Case Temperature**



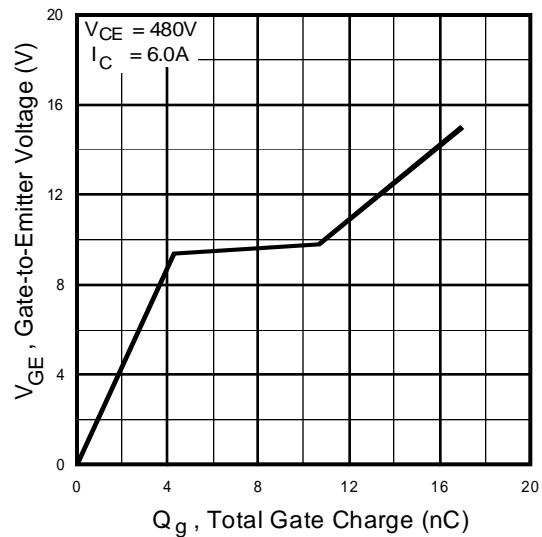
**Fig. 5 - Collector-to-Emitter Voltage vs. Case Temperature**



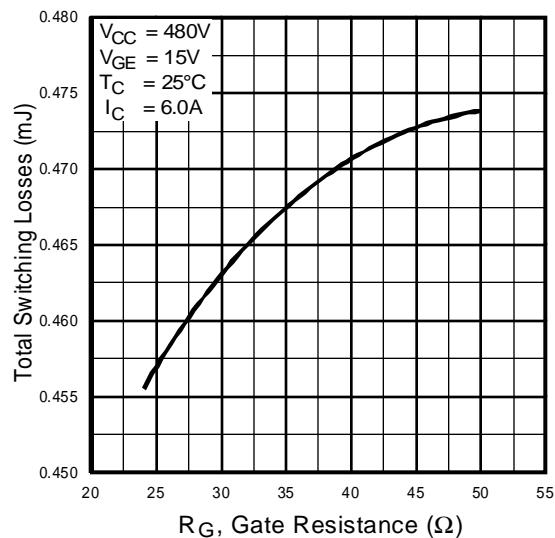
**Fig. 6 - Maximum IGBT Effective Transient Thermal Impedance, Junction-to-Case**



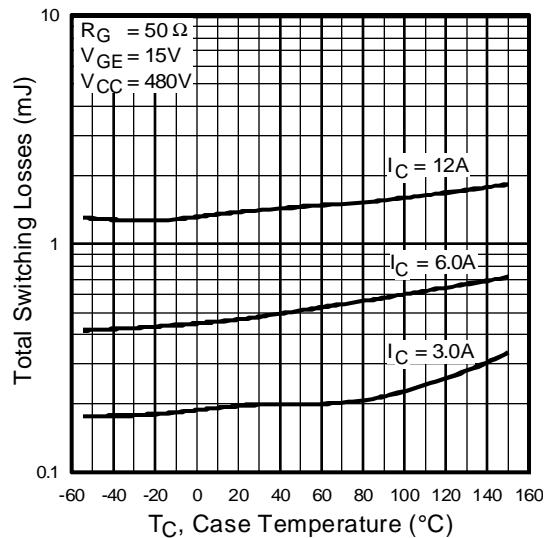
**Fig. 7 - Typical Capacitance vs.  
Collector-to-Emitter Voltage**



**Fig. 8 - Typical Gate Charge vs.  
Gate-to-Emitter Voltage**

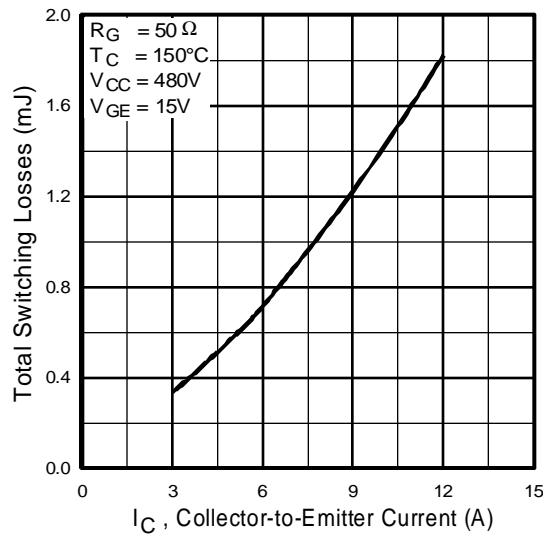


**Fig. 9 - Typical Switching Losses vs. Gate  
Resistance**

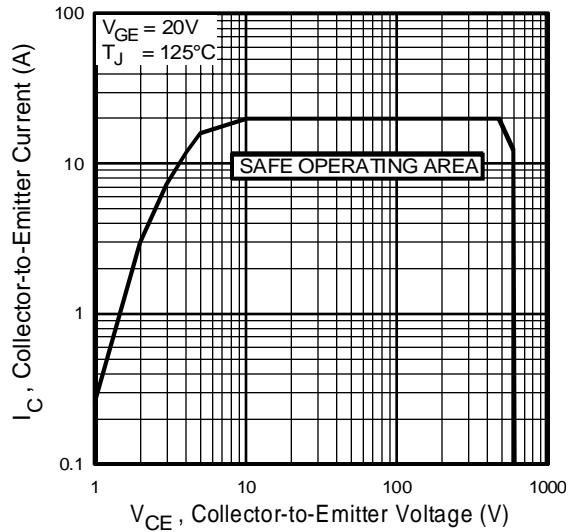


**Fig. 10 - Typical Switching Losses vs.  
Case Temperature**

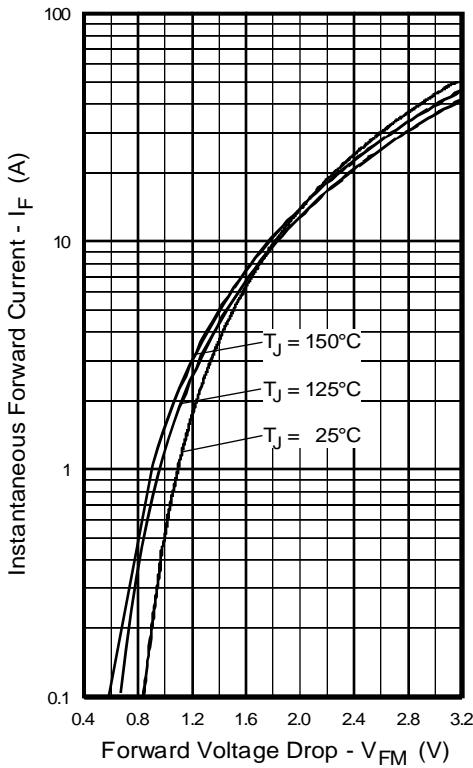
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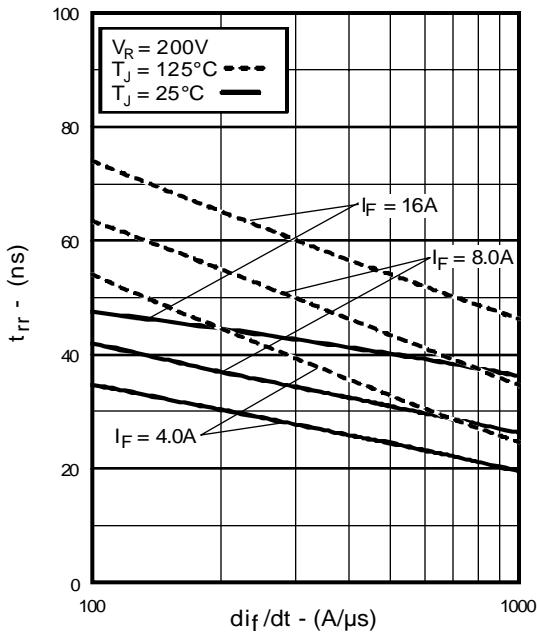
**Fig. 11** - Typical Switching Losses vs.  
Collector-to-Emitter Current



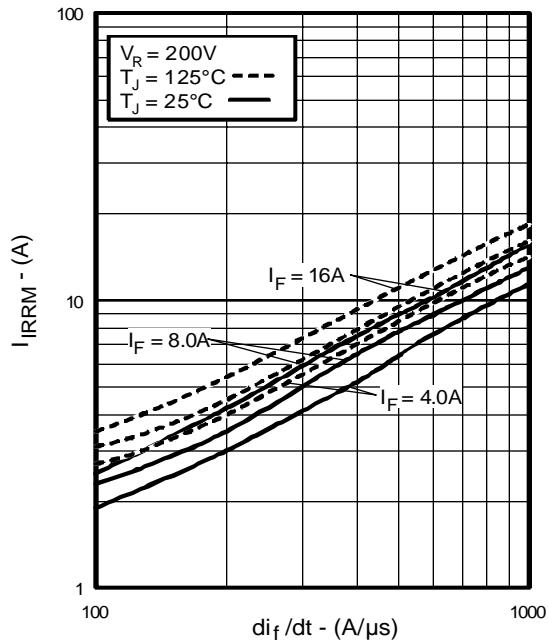
**Fig. 12** - Turn-Off SOA



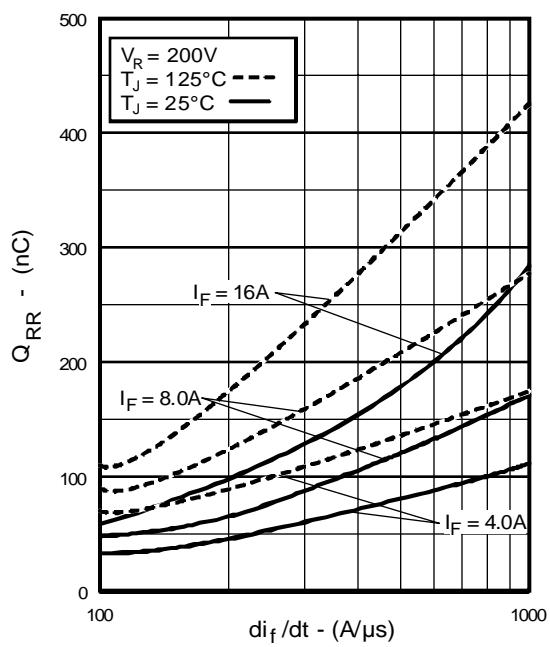
**Fig. 13** - Maximum Forward Voltage Drop vs. Instantaneous Forward Current



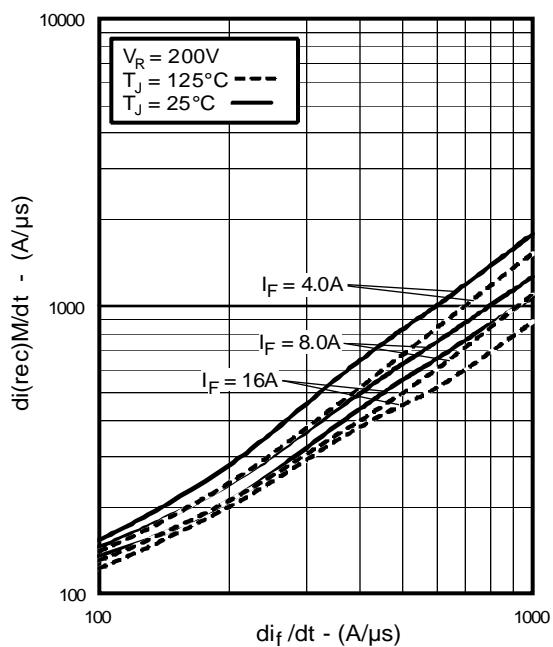
**Fig. 14** - Typical Reverse Recovery vs.  $di_f/dt$



**Fig. 15** - Typical Recovery Current vs.  $di_f/dt$

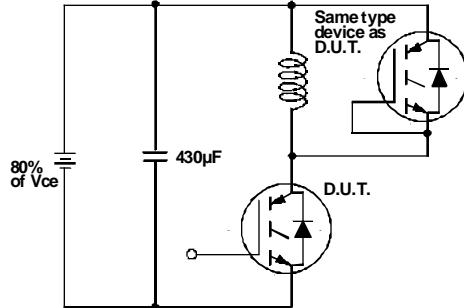


**Fig. 16** - Typical Stored Charge vs.  $di_f/dt$

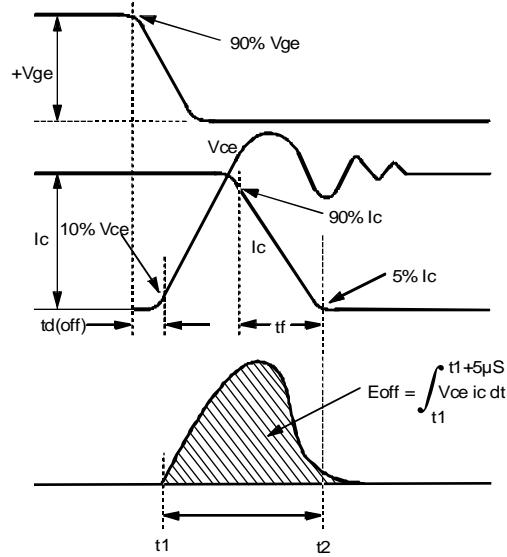


**Fig. 17** - Typical  $d(di_{rc})/dt$  vs.  $di_f/dt$

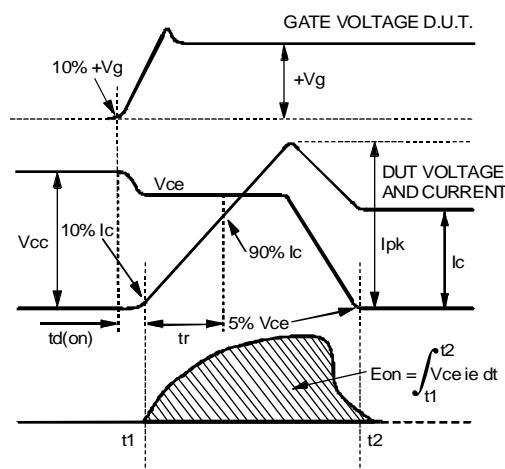
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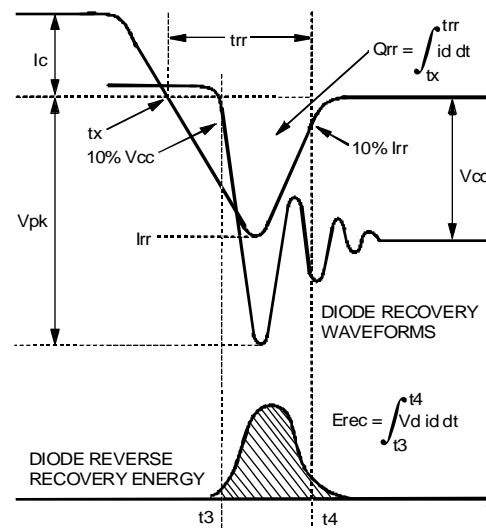
**Fig. 18a** - Test Circuit for Measurement of  
 $I_{LM}$ ,  $E_{on}$ ,  $E_{off(diode)}$ ,  $t_{rr}$ ,  $Q_{rr}$ ,  $I_{rr}$ ,  $t_d(on)$ ,  $t_r$ ,  $t_d(off)$ ,  $t_f$



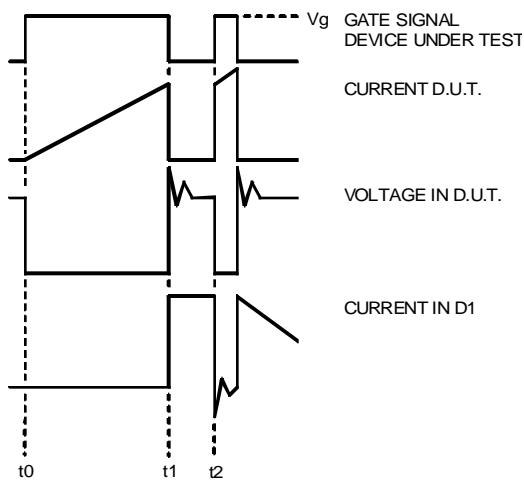
**Fig. 18b** - Test Waveforms for Circuit of Fig. 18a, Defining  
 $E_{off}$ ,  $t_d(off)$ ,  $t_f$



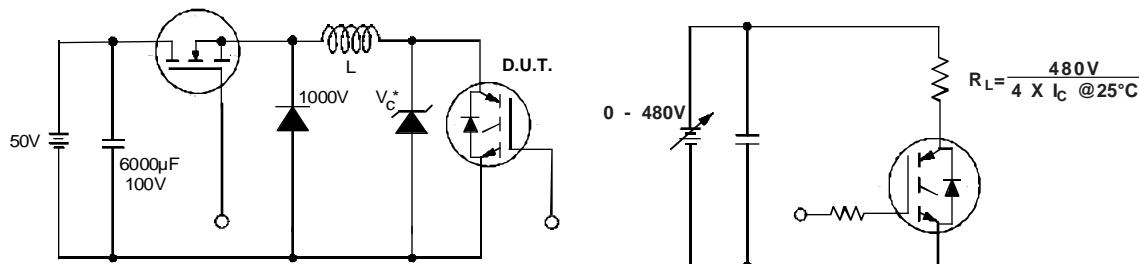
**Fig. 18c** - Test Waveforms for Circuit of Fig. 18a,  
Defining  $E_{on}$ ,  $t_{d(on)}$ ,  $t_r$



**Fig. 18d** - Test Waveforms for Circuit of Fig. 18a,  
Defining  $E_{rec}$ ,  $t_{rr}$ ,  $Q_{rr}$ ,  $I_{rr}$

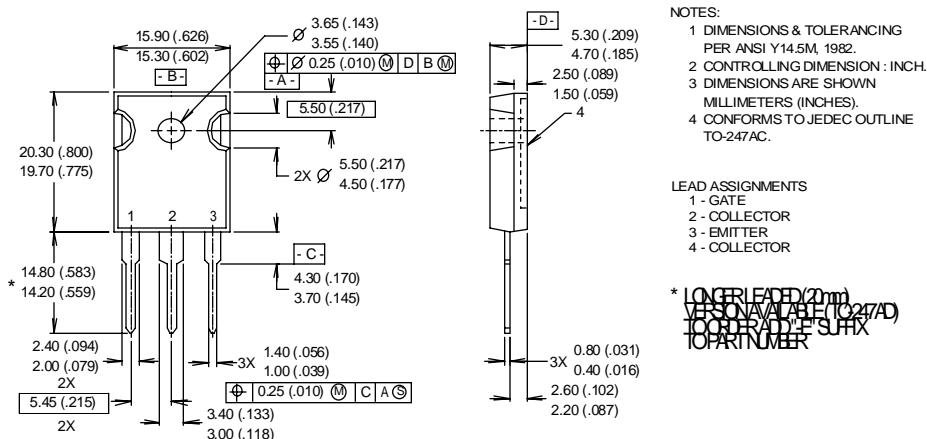


**Fig. 18e - Macro Waveforms for Test Circuit of Fig. 18a**



**Fig. 19 - Clamped Inductive Load Test Circuit**

**Fig. 20 - Pulsed Collector Current Test Circuit**



CONFORMS TO JEDEC OUTLINE TO-247AC (TO-3P)  
Dimensions in Millimeters and (Inches)