



PROVISIONAL

IRFK6H/J054

Isolated Base Power HEX-pakTM

Assembly - Parallel Chip

- High Current Capability.
- UL recognized E78996.
- Electrically Isolated Base Plate.
- Easy Assembly into Equipment.

 $V_{DSS} = 60V$ $R_{DS(on)} = 3.3m\Omega$ $I_D = 350A$

Description

The HEX-pakTM utilizes the well-proven HEXFETTM transistor die, combining low on-state resistance with high transconductance. These superior technology die are assembled by state of the art techniques into the TO-240 package, featuring 2.5kV rms isolation and solid M5 screw connections. The small footprint means the package is highly suited to power applications where space is a premium. Available in two versions, IRFK.H... for fast switching and IRFK.J... for oscillation sensitive applications.

HEX-pak

Absolute Maximum Ratings

	Parameter	Max.	Units
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V \oplus$	350	
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V \oplus$	220	A
I_{DM}	Pulsed Drain Current \ominus	1400	
	Linear Derating Factor	5.0	W/C
$P_D @ T_C = 25^\circ C$	Power Dissipation	625	W
V_{GS}	Gate-to-Source Voltage	± 20	V
V_{IN}	R.M.S. Isolation Voltage, Circuit to Base (1 Minute)	2.5	kV
T_J T_{STG}	Operating Junction and Storage Temperature Range	-40 to 150	°C

Thermal Resistance

	Parameter	Min.	Typ.	Max.	Units
R_{JC}	Junction-to-Case	—	—	0.20	°C/W
R_{CS}	Case-to-Sink, Flat, Greased Surface	—	0.10	—	
T	Mounting torque +10%, M6 Screw \ominus	—	5.0	—	Nm
	Busbar to HEXPAK with M5 Screw	—	3.0	—	
WT	Approximate Weight	—	140 (5.0)	—	g (oz)

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Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

Parameter	Min.	Typ.	Max.	Units	Conditions
V_{BDSS}	50	—	—	V	$V_{GS} = 0\text{V}$, $I_D = 1.0\text{mA}$
$R_{DS(on)}$	2.2	2.3	3.3	mΩ	$V_{GS} = 10\text{V}$, $I_D = 110\text{A}$ ④
$I_{D(on)}$	150	—	—	A	$V_{DS} > I_{D(on)} \times R_{DS(on)}/\text{max}$, $V_{GS} = 10\text{V}$
$ V_{GS(th)}$	2.0	—	4.0	V	$V_{DS} = V_{GS}$, $I_D = 1.0\text{mA}$
g_f	186	282	—	S	$V_{DS} = 50\text{V}$, $I_D = 225\text{A}$
I_{DSS}	—	—	1.5	mA	$V_{DS} = 60\text{V}$, $V_{GS} = 0\text{V}$
I_{GSS}	—	—	6.0	mA	$V_{DS} = 48\text{V}$, $V_{GS} = 0\text{V}$, $T_J = 125^\circ\text{C}$
I_{GSS}	—	—	600	nA	$V_{GS} = 20\text{V}$
I_{GSS}	—	—	600	nA	$V_{GS} = -20\text{V}$
Q_g	780	900	—	—	$I_D = 225\text{A}$
Q_{gs}	150	220	—	nC	$V_{DS} = 48\text{V}$
Q_{gd}	270	400	—	—	$V_{GS} = 10\text{V}$ ④
t_{on}	110	—	—	ns	
t_r	125	—	—	ns	
t_r	700	—	—	ns	$V_{GS} = 25\text{V}$
t_{off}	800	—	—	ns	$I_D = 225\text{A}$
t_{off}	400	—	—	ns	$V_{GS} = 10\text{V}$
t_{off}	530	—	—	ns	$R_S = 3.3\Omega$ ④
t_f	260	—	—	ns	
t_f	300	—	—	ns	
L_D	18	—	—	nH	
C_{iss}	14	—	—	—	$V_{GS} = 0\text{V}$
C_{oss}	2.5	—	—	RF	$V_{DS} = 25\text{V}$
C_{rss}	0.75	—	—	—	$f = 1.0\text{MHz}$

Source-Drain Ratings and Characteristics

Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	300	—	—	A	Modified MOSFET symbol showing the integral reverse p-n junction diode.
(Body Diode)	—	—	—	—	
I_{SM}	1100	—	—	—	
(Body Diode) ④	—	—	—	—	
$ V_{SD} $	2.5	—	—	V	$T_J = 25^\circ\text{C}$, $I_S = 225\text{A}$, $V_{GS} = 0\text{V}$ ④
Diode Forward Voltage	—	—	—	—	
t_r	71	150	320	ns	$T_J = 25^\circ\text{C}$, $I_F = 225\text{A}$
Reverse Recovery Time	—	—	—	—	
Q_r	4.4	10	23	μC	$dI/dt = 400\text{A}/\mu\text{s}$ ④
Reverse Recovery Charge	—	—	—	—	

Notes:

- ① Repetitive rating; pulse width limited by maximum junction temperature.
- ② A mounting compound is recommended and the torque should be rechecked after a period of three hours to allow for the spread of the compound.
- ③ Limited by package to 200 amperes maximum continuous current.
- ④ Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.