

# Digital transistors (built-in resistors)

## DTD133HK / DTD133HS

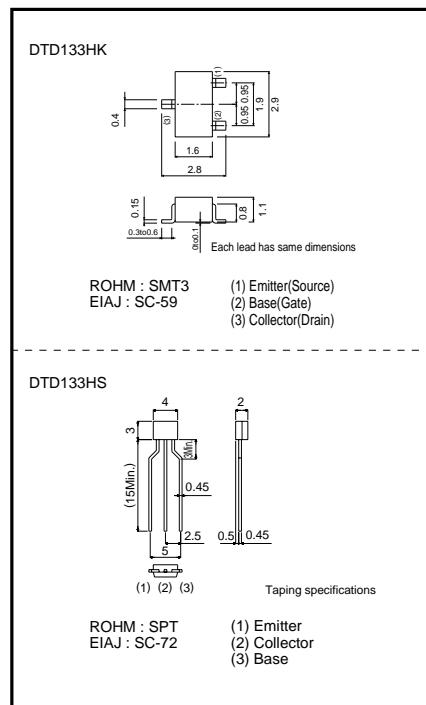
### ●Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input, and parasitic effects are almost completely eliminated.
- 3) Only the on / off conditions need to be set for operation, making device design easy.
- 4) Higher mounting densities can be achieved.

### ●Absolute maximum ratings ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Limits	Unit
Supply voltage	$V_{cc}$	50	V
Input voltage	$V_i$	20 -6	V
Output current	$I_c$	500	mA
Power dissipation	$P_d$	200 300	mW
Junction temperature	$T_j$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55~+150	$^\circ\text{C}$

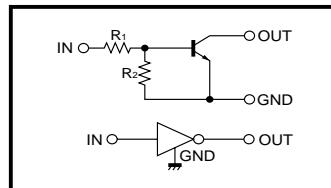
### ●External dimensions (Units : mm)



### ●Package, marking, and packaging specifications

Part No.	DTD133HK	DTD133HS
Package	SMT3	SPT
Marking	G08	-
Packaging code	T146	TP
Basic ordering unit (pieces)	3000	5000

### ●Circuit schematic



### ●Electrical characteristics ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	$V_{i(\text{off})}$	-	-	0.3	V	$V_{cc} = 5\text{V}$ , $I_o = 100\mu\text{A}$
	$V_{i(\text{on})}$	2	-	-		$V_o = 0.3\text{V}$ , $I_o = 20\text{mA}$
Output voltage	$V_{o(\text{on})}$	-	0.1	0.3	V	$I_o = 50\text{mA}$ , $I_i = 2.5\text{mA}$
Input current	$I_i$	-	-	2.4	mA	$V_i = 5\text{V}$
Output current	$I_o(\text{off})$	-	-	0.5	$\mu\text{A}$	$V_{cc} = 50\text{V}$ , $V_i = 0\text{V}$
DC current gain	$G_i$	56	-	-		$I_o = 50\text{mA}$ , $V_o = 5\text{V}$
Input resistance	$R_i$	2.31	3.3	4.29	k $\Omega$	-
Resistance ratio	$R_2/R_1$	2.4	3	3.7	-	-
Transition frequency	$f_T$	-	200	-	MHz	$V_{ce} = 10\text{V}$ , $I_e = -5\text{mA}$ , $f = 100\text{MHz}$ *

\* Transition frequency of the device.