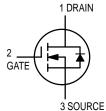
# **TMOS Switching**

### N-Channel — Enhancement





## BS107 BS107A



### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	200	Vdc
Gate–Source Voltage — Continuous — Non–repetitive (t <sub>p</sub> ≤ 50 μs)	V <sub>GS</sub> V <sub>GSM</sub>	±20 ±30	Vdc Vpk
Drain Current Continuous <sup>(1)</sup> Pulsed <sup>(2)</sup>	I <sub>D</sub>	250 500	mAdc
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	350	mW
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C

**ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit		
OFF CHARACTERISTICS							
Zero-Gate-Voltage Drain Current (V <sub>DS</sub> = 130 Vdc, V <sub>GS</sub> = 0)	I <sub>DSS</sub>	_	_	30	nAdc		
Drain–Source Breakdown Voltage ( $V_{GS} = 0$ , $I_D = 100 \mu Adc$ )	V(BR)DSX	200	_	_	Vdc		
Gate Reverse Current (V <sub>GS</sub> = 15 Vdc, V <sub>DS</sub> = 0)	I <sub>GSS</sub>	_	0.01	10	nAdc		
ON CHARACTERISTICS(2)							
Gate Threshold Voltage (I <sub>D</sub> = 1.0 mAdc, V <sub>DS</sub> = V <sub>GS</sub> )	V <sub>GS(Th)</sub>	1.0	_	3.0	Vdc		
Static Drain—Source On Resistance  BS107 (V <sub>GS</sub> = 2.6 Vdc, I <sub>D</sub> = 20 mAdc)	<sup>r</sup> DS(on)		_ _ 4.5	28 14 6.0	Ohms		
(I <sub>D</sub> = 250 mAdc)			4.8	6.4			
SMALL-SIGNAL CHARACTERISTICS	1 . 1		I	ı			
Input Capacitance (V <sub>DS</sub> = 25 Vdc, V <sub>GS</sub> = 0, f = 1.0 MHz)	C <sub>iss</sub>	_	60	_	pF		
Reverse Transfer Capacitance $(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz})$	C <sub>rss</sub>	_	6.0	_	pF		
Output Capacitance $(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz})$	C <sub>oss</sub>	_	30	_	pF		
Forward Transconductance (V <sub>DS</sub> = 25 Vdc, I <sub>D</sub> = 250 mAdc)	9fs	200	400	_	mmhos		
SWITCHING CHARACTERISTICS							
Turn-On Time	ton		6.0	15	ns		
Turn-Off Time	<sup>t</sup> off	_	12	15	ns		

- 1. The Power Dissipation of the package may result in a lower continuous drain current.
- 2. Pulse Test: Pulse Width  $\leq 300~\mu s$ , Duty Cycle  $\leq 2.0\%$ .



### **RESISTIVE SWITCHING**

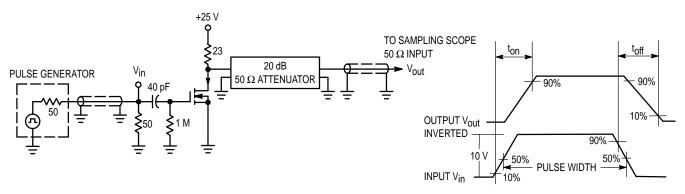


Figure 1. Switching Test Circuit

Figure 2. Switching Waveforms

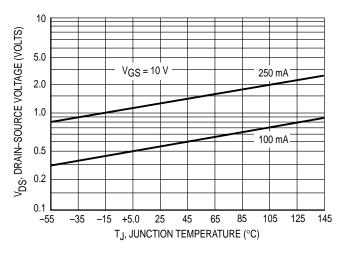


Figure 3. On Voltage versus Temperature

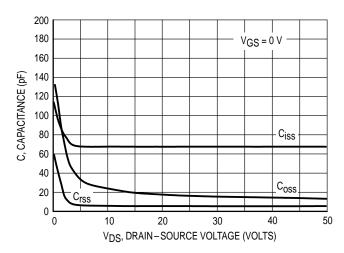


Figure 4. Capacitance Variation

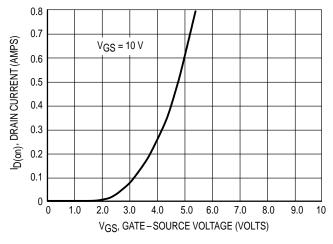


Figure 5. Transfer Characteristic

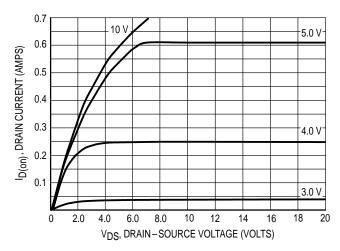


Figure 6. Output Characteristic

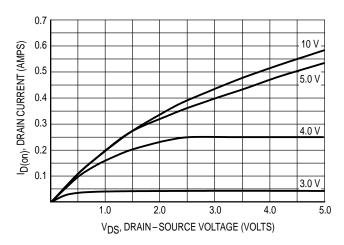
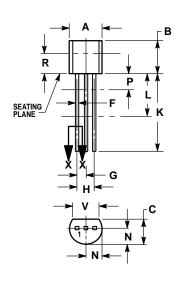


Figure 7. Saturation Characteristic

#### PACKAGE DIMENSIONS



SECTION X-X

**CASE 029-04** (TO-226AA) **ISSUE AD** 

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
  CONTOUR OF PACKAGE BEYOND DIMENSION R
- IS UNCONTROLLED.

  DIMENSION F APPLIES BETWEEN P AND L. DIMENSION D AND J APPLY BETWEEN L AND K
  MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.20	
В	0.170	0.210	4.32	5.33	
C	0.125	0.165	3.18	4.19	
D	0.016	0.022	0.41	0.55	
F	0.016	0.019	0.41	0.48	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
7	0.015	0.020	0.39	0.50	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0.115		2.93		
٧	0.135		3.43		

STYLE 30:

PIN 1. DRAIN

2. GATE SOURCE

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 $\Diamond$ BS107/D