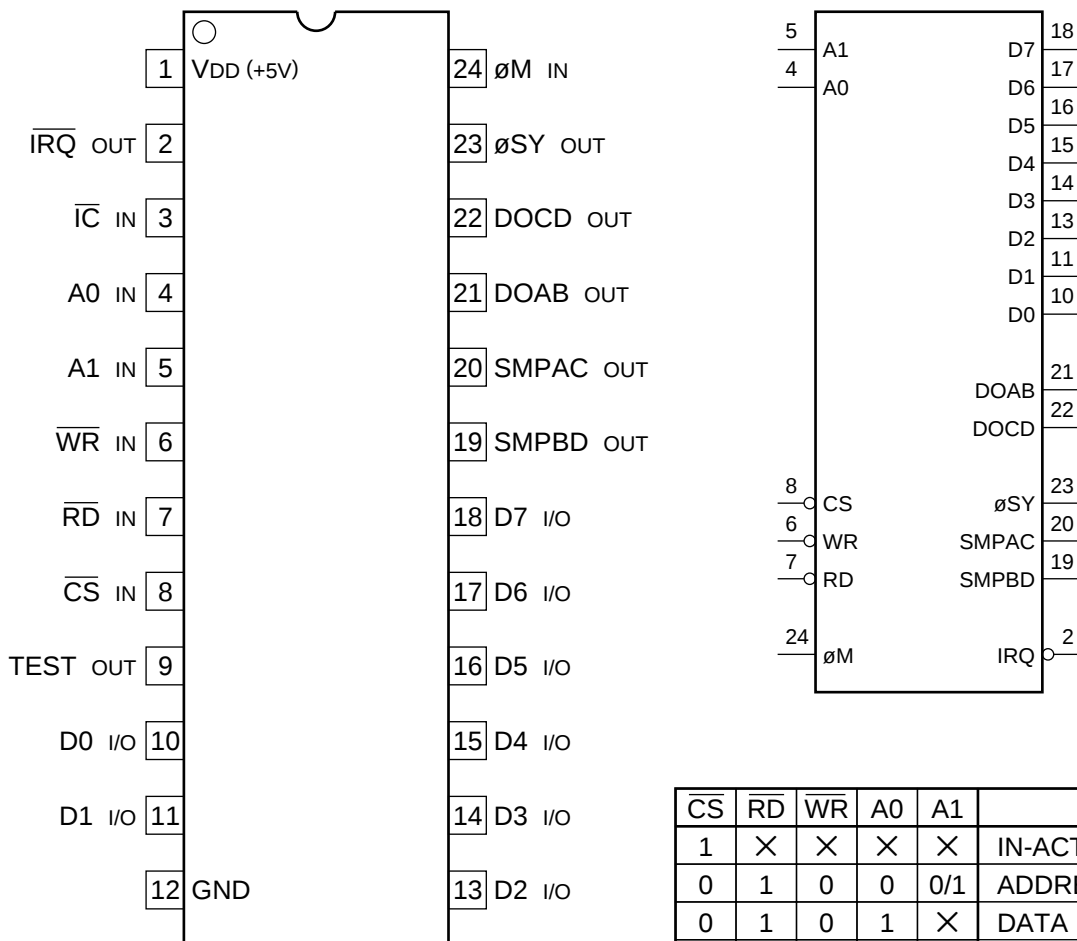

C-MOS 4-OPERATOR FM SOUND GENERATOR

-TOP VIEW-



$\overline{CS}$	$\overline{RD}$	$\overline{WR}$	A0	A1	MODE
1	X	X	X	X	IN-ACTIVE MODE
0	1	0	0	0/1	ADDRESS WRITE MODE
0	1	0	1	X	DATA WRITE MODE
0	0	1	0	0	STATUS READ MODE

0 ; LOW LEVEL

1 ; HIGH LEVEL

X ; DON'T CARE

INPUT

$\emptyset M$; MASTER CLOCK (14.32 MHz)
 A0, A1 ; CPU INTERFACE ADDRESS SELECT
 $\overline{CS}$; CHIP SELECT
 $\overline{RD}$; READ ENABLE
 $\overline{WR}$; WRITE ENABLE
 $\overline{IC}$; INITIAL CLEAR INPUT

OUTPUT

$\emptyset SY$; DAC INTERFACE DATA LATCH SIGNAL
 $\overline{IRQ}$; TIMER INTERRUPT SIGNAL
 DOAB ; DAC INTERFACE CH-A, CH-B SERIAL DATA
 DOCD ; DAC INTERFACE CH-C, CH-D SERIAL DATA
 SMPAC ; DAC INTERFACE CH-A, CH-C SAMPLE/HOLD
 SMPBD ; DAC INTERFACE CH-B, CH-D SAMPLE/HOLD

INPUT/OUTPUT

D0 - D7 ; CPU INTERFACE DATA BUS

