

# Digital Line Interface Transceiver

## GENERAL DESCRIPTION

The XR-T6164 is a bipolar analog chip intended for general purpose line interface applications at bit rates up to 1.544 Mbps (T1). It contains both receive and transmit circuitry in a 16 pin dual-in-line plastic package. The receiver is designed for short line applications having a cable loss up to 10dB measured at the half bit rate. The transmitter has open collector line driver outputs that are capable of handling up to 40mA.

When used in conjunction with either XR-T6165 or XR-T6166, the chip set provides a 64 Kbps codirectional interface as specified in CCITT G.703.

## FEATURES

- Single 5V supply
- CCITT G.703 compatible when used with either XR-T6165 or XR-T6166
- Low power
- TTL compatible digital inputs and outputs
- Links remote equipment at distances up to 500 meters without equalization
- Receive data comparator threshold storage provides ping-pong operation capability
- Loss of signal alarm
- Dual matched driver outputs

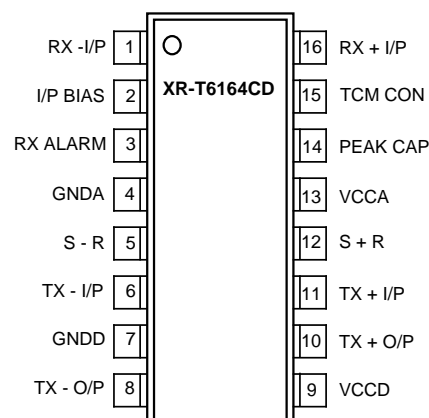
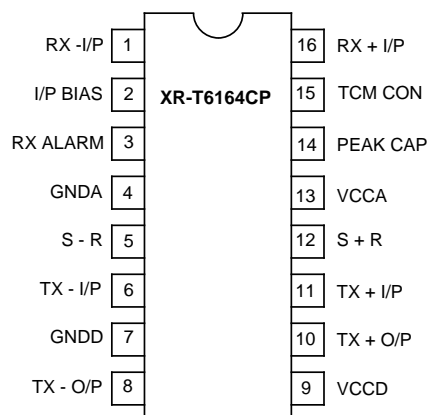
## APPLICATIONS

Data Adaption Unit (DAU)  
General purpose TTL compatible line interface

## ABSOLUTE MAXIMUM RATINGS

Supply Voltage	20V
Storage Temperature	-65°C to +150°C

## PIN ASSIGNMENT



## ORDERING INFORMATION

Part Number	Package	Operating Temperature
XR-T6164CP	PDIP	0°C to +70°C
XR-T6164CD	SOIC	0°C to +70°C

# XR-T6164

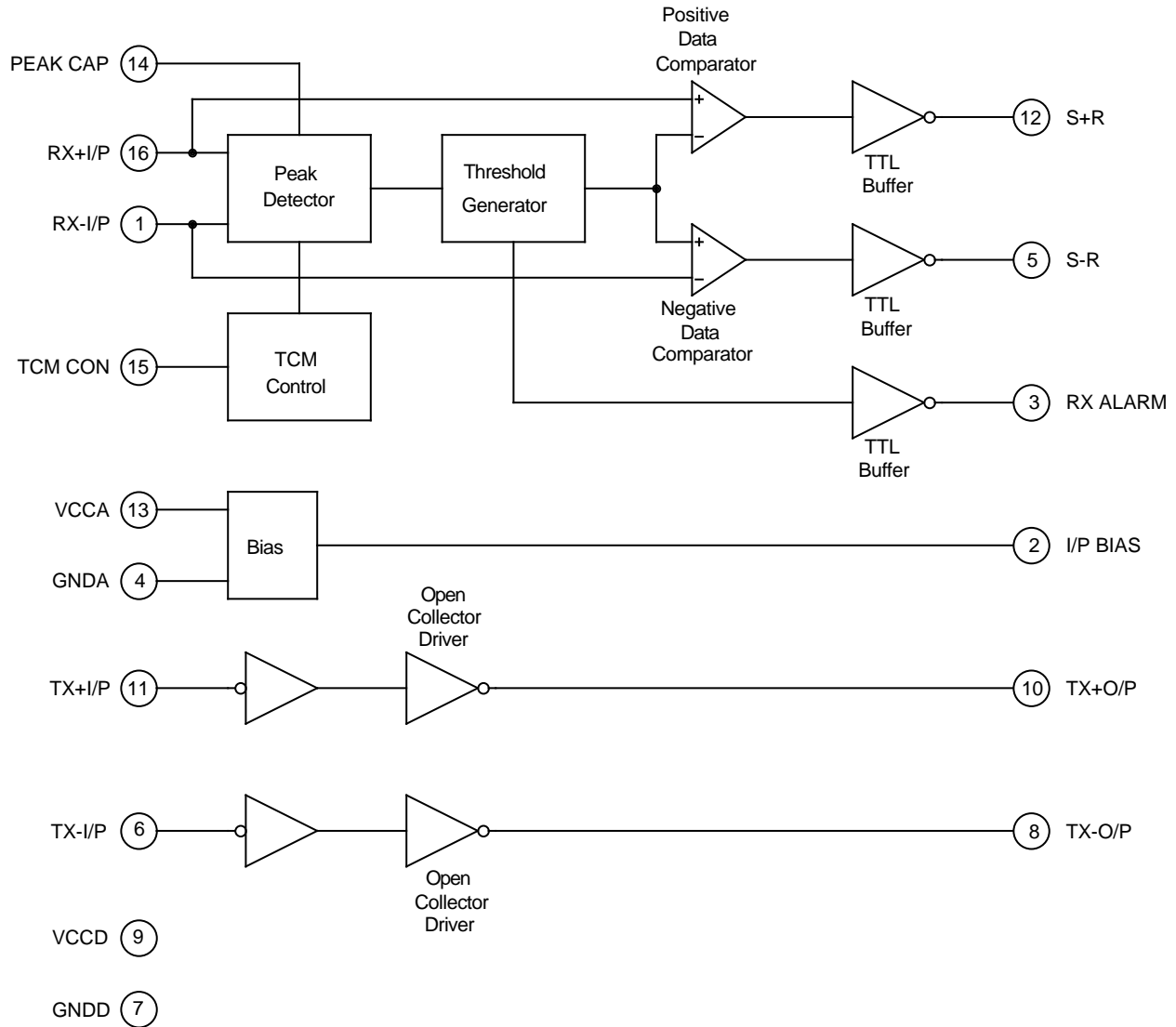


Figure 1. XR-T6164 Block Diagram

## PIN DESCRIPTION

PIN#	SYMBOL	TYPE	DESCRIPTION
1	RX – I/P	I	<b>RECEIVER NEGATIVE BIPOLAR INPUT.</b> Line analog input.
2	I/P BIAS	O	<b>RECEIVE INPUT BIAS.</b> Connects to center tap of input transformer secondary winding.
3	RX ALARM	O	<b>LOSS OF SIGNAL ALARM.</b> (active low).
4	GNDA	-	<b>ANALOG GROUND.</b>
5	S – R	O	<b>RECEIVE NEGATIVE DATA OUTPUT.</b> (active low), Output from negative bipolar input pulses.
6	TX – I/P	I	<b>TRANSMIT NEGATIVE INPUT DATA.</b> (active low), Input for negative output driver.
7	GNDD	-	<b>DIGITAL GROUND.</b>
8	TX – O/P	O	<b>TRANSMIT NEGATIVE OUTPUT DRIVER.</b> Open collector, Drives output transformer primary.
9	VCCD	-	<b>+5V ± 5% DIGITAL SUPPLY.</b>
10	TX + O/P	O	<b>TRANSMIT POSITIVE OUTPUT DRIVER.</b> Open collector, Drives output transformer primary.
11	TX + I/P	I	<b>TRANSMIT POSITIVE INPUT DATA.</b> (active low) Input for positive output driver.
12	S + R	O	<b>RECEIVE POSITIVE DATA OUTPUT.</b> (active low) Output from positive bipolar input pulses.
13	VCCA	-	<b>+5V ± 5% ANALOG SUPPLY.</b>
14	PEAK CAP	-	<b>PEAK DETECTOR CAPACITOR.</b> Stores peak detector voltage.
15	TCM CON	I	<b>TIME COMPRESSION MULTIPLEX CONTROL.</b> (active low) When active, disconnects peak detector charge and discharge paths.
16	RX + I/P	I	<b>RECEIVER POSITIVE BIPOLAR INPUT.</b> Line analog input.

# XR-T6164

## ELECTRICAL CHARACTERISTICS

Test Conditions:  $V_{CC} = 5V \pm 5\%$ ,  $T_A = 25^\circ C$ , unless otherwise specified.

PARAMETERS	MIN	TYP	MAX	UNITS	CONDITIONS
Supply Voltage	4.75	5	5.25	V	
Analog Supply Current	<b>4</b>		<b>6.5</b>	mA	
Digital Supply Current	<b>13</b>		<b>20</b>	mA	
<b>RECEIVER</b>					
Input Signal		1	2.2	Vp	Measured from pins 1 or 16 with respect to pin 2
Dynamic Range			10	dB	Maximum cable loss range
Input Impedance		20		K $\Omega$	Measured between pins 1 and 16
Input Slicing Threshold		50		%	Percent of peak input signal amplitude
Input Bias Voltage		<b>1.45</b>		V	Measured at pin 2
Loss of Signal Alarm Threshold		150		mVp	Measured from pins 1 or 16 with respect to pin 2
Loss of Signal Alarm Level Hysteresis		+/-1.5		dB	Difference between alarm-on and alarm off levels
Peak Detector Leakage		-80		$\mu A$	
Data Output Low			<b>0.4</b>	V	Measured at pins 5 or 12, $I_{OUT} = +1.6mA$
Data Output High	<b>3.6</b>			V	Measured at pins 5 or 12, $I_{OUT} = -40\mu A$
Alarm Output Low			<b>0.4</b>	V	Measured at pin 3; $I_{OUT} = +1.6mA$
Alarm Output High	<b>V<sub>CC</sub> - 0.5</b>			V	Measured at pin 3; $I_{OUT} = -40\mu A$
TCM Input Low Voltage			<b>0.8</b>	V	Measured at pin 15; $I_{IN} \text{ min} = -500\mu A, I_{IN} \text{ max} = +5\mu A$

**Note 1:** Higher input voltages are possible if a resistive input attenuator is used.

**Note 2:** Bold face parameters are covered by production test.

## AC ELECTRICAL CHARACTERISTICS

Test Conditions:  $V_{CC} = 5\text{ V} \pm 5\%$ ,  $T_A = 25^\circ\text{C}$ , unless otherwise specified.

TRANSMITTER					
PARAMETERS	MIN	TYP	MAX	UNITS	CONDITIONS
Input Low Voltage			0.8	V	Measured at pins 6, 11; $I_{IN} = -700\mu\text{A}$
Input High Voltage	2.2			V	Measured at pins 6, 11; $I_{IN} = +5\mu\text{A}$
Output Low Voltage			<b>1</b>	V	Measured at pins 8, 10; $I_{OUT} = -40\text{mA}$
Output Low Current			40	mA	Measured at pins 8, 10; $V_{OUT} = 1\text{V}$
Output Leakage Current	<b>-100</b>			$\mu\text{A}$	Measured at pins 8, 10; $V_{OUT} = 10\text{V}$ Outputs in off state
RECEIVER					
Input Level		1	2.2	Vp	Pin 1, 16 with Respect to Pin 2 (See Note 1)
Output Rise Time			<b>50</b>	ns	Pins 5, 12; $C_L = 15\text{pF}$ , 10% to 90%
Output Fall Time			50	ns	Pins 5, 12; $C_L = 15\text{pF}$ , 90% to 10%
TRANSMITTER					
Output Rise Time			<b>50</b>	ns	Pins 8, 10; $R_L = 130$ , $C_L = 15\text{pF}$ , 10% to 90%
Output Fall Time			<b>50</b>	ns	Pins 8, 10; $R_L = 130$ , $C_L = 15\text{pF}$ , 90% to 10%
Rising Edge Delay			<b>100</b>	ns	Pins 8, 10; $R_L = 130$ , $C_L = 15\text{pF}$ , 50% to 50% (I/P to O/P)
Falling Edge Delay			<b>100</b>	ns	Pins 8, 10; $R_L = 130$ , $C_L = 15\text{pF}$ , 50% to 50% (I/P to O/P)

**Note 1:** Higher input voltages are possible if a resistive input attenuator is used.

**Note 2:** Bold face parameters are covered by production test.

## Magnetic Supplier Information:

Pulse  
Telecom Product Group  
P.O. Box 12235  
San Diego, CA 92112  
Tel. (619)674-8100  
Fax. (619)674-8262

## SYSTEM DESCRIPTION

The XR-T6164 is a general purpose line interface chip that contains the receive and transmit circuitry necessary to convert TTL logic levels to a bipolar signal both to and from a twisted pair cable.

### Receiver

The XR-T6164 receiver section converts a balanced bipolar signal that has been attenuated and distorted by up to 10 dB of twisted pair cable to active-low TTL compatible logic levels.

The cable is transformer coupled to the receiver differential inputs (RX+IP, RX-IP) which are biased through the input transformer secondary winding by a voltage generated on-chip (I/P BIAS). The bipolar receive signal is applied to a peak detector, and to a pair of data comparators. The peak detector output voltage charges an external capacitor connected to PEAK CAP. This voltage generates a data comparator bias level that is approximately 50% of the peak input pulse amplitude. Thus, data slicing is automatically accomplished at the optimum level over the full cable loss range. TTL compatible output stages buffer the receiver digital outputs (S+R, S-R) and provide active low signals corresponding to received positive and negative input pulses.

Loss of input signal is detected by a comparator that monitors input signal level. An active-low TTL compatible logic level (RX ALARM) indicates signal loss. Comparator hysteresis prevents chatter on this output.

Ping-pong operation is made possible by the time compression multiplex control input (TCM CON). A logic 0 applied to this pin during transmission stores the peak detector output voltage by disconnecting

the peak detector storage capacitor charge and discharge paths. Since the receive data comparator bias voltage is stored during transmit mode, it is immediately available when receive mode resumes.

### Transmitter

The XR-T6164 transmitter section contains two matched open collector output drivers that are capable of driving the line transformer directly with a current up to 40mA. The transmitter output drivers include diode clamps to ensure non-saturating operation. Transmitter digital inputs, which are active-low, are TTL compatible. External resistors are used between the transmitter outputs and the output transformer primary to set the output pulse amplitude.

## APPLICATION INFORMATION

Figure 2 shows a general line driver application circuit using the XR-T6164. This device converts bipolar transmit and receive signals in the 64Kbps to 1.544Mbps range to active-low TTL compatible logic levels.

Bipolar signals that have been attenuated and distorted by twisted pair cable are transformer-coupled to the line side of the XR-T6164 as shown on the left side of Figure 2. Suggested transformers for both the input and output applications are the Pulse types PE-65535 for 64Kbps use and the PE-65835 for 1.544Mbps applications.

The right side of Figure 2 shows the TTL compatible digital inputs and outputs. Please refer to the Pin Description section of this data sheet for detailed information about each signal.

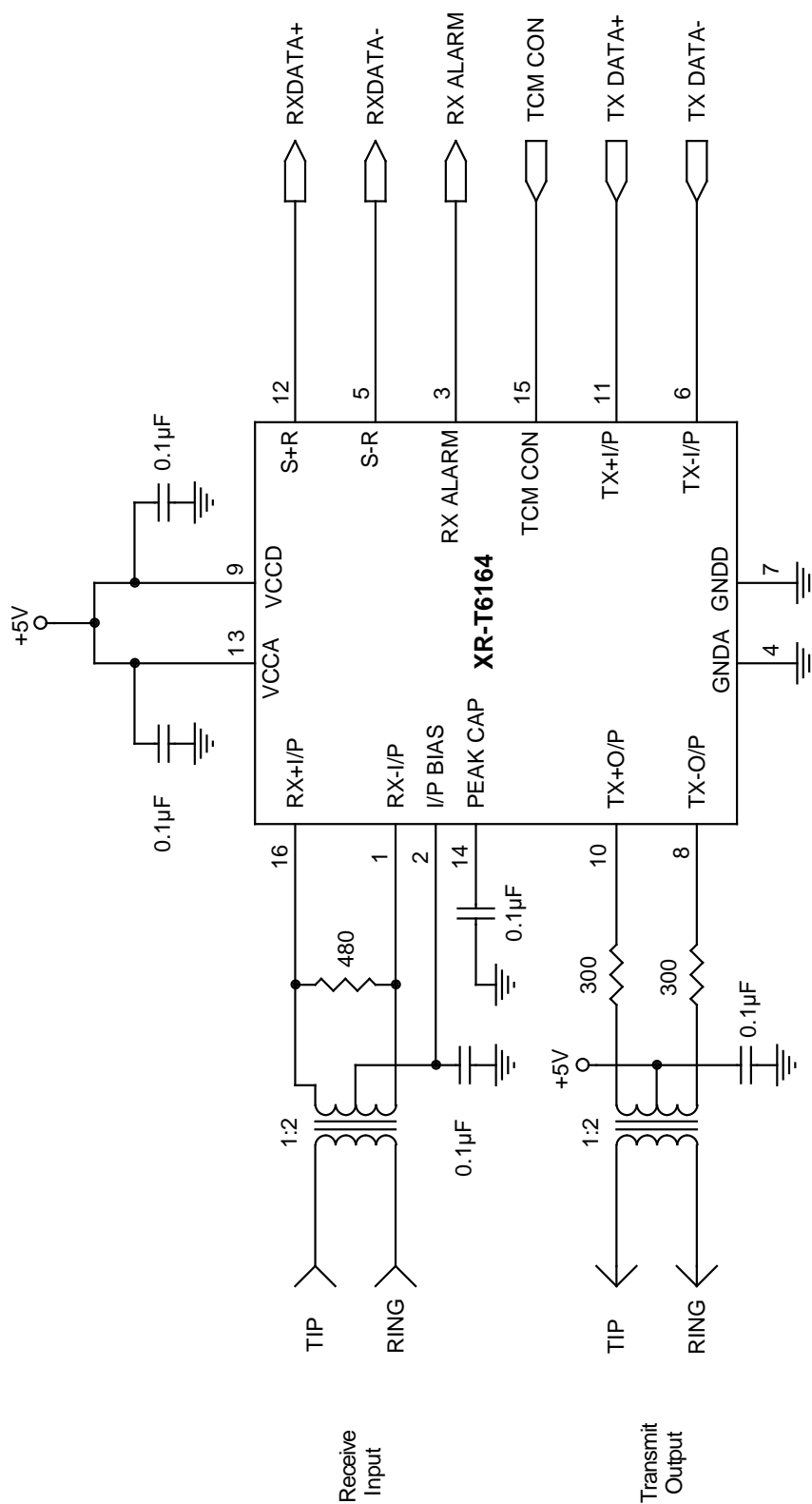


Figure 2. XR-T6164 Line Driver Application

## Notes



## Notes

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