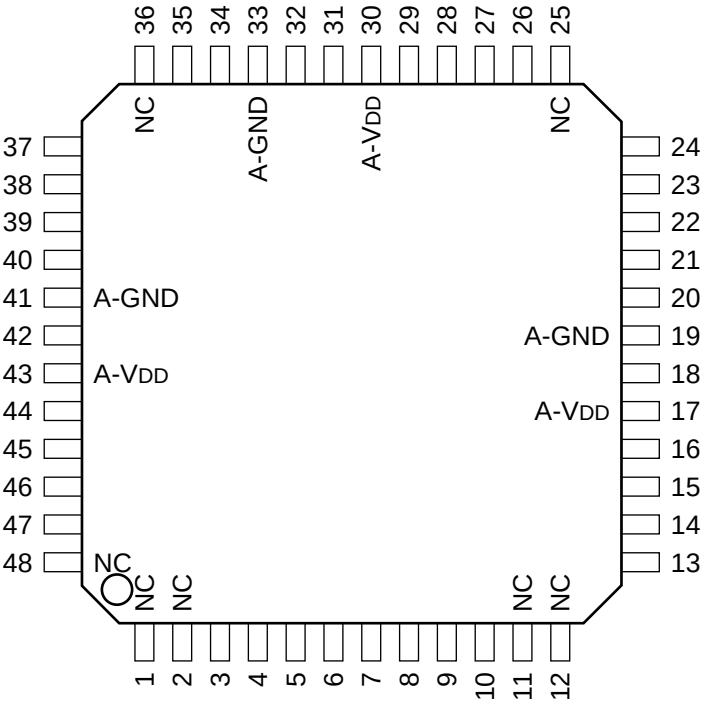


C-MOS CCD IMAGE DIGITIZERS

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	NC	13	O	DB8	25	—	NC	37	I	CLAMP
2	—	NC	14	O	DB9	26	I	LOAD	38	I	SHD
3	O	DB2	15	O	OVER	27	I	SDI	39	I	SHP
4	O	DB3	16	I	OE	28	I	VRT	40	I	RSTCCD
5	O	DB4	17	—	A-VDD	29	I	VRTO	41	—	A-GND
6	—	D-GND	18	I	ENABLECAL	30	—	A-VDD	42	I	CLK POL
7	—	D-VDD	19	—	A-GND	31	I	IN NEG	43	—	A-VDD
8	O	DB5	20	—	TEST	32	I	IN POS	44	O	SYNC
9	O	DB6	21	I	STBY1	33	—	A-GND	45	O	UNDER
10	O	DB7	22	I	STBY2	34	I	VRBO	46	O	DB0
11	—	NC	23	I	RESET	35	I	VRB	47	O	DB1
12	—	NC	24	I	SCLK	36	—	NC	48	—	NC

INPUT

CLAMP	: CDS CLAMP CONTROL
CLK POL	: CLOCK POLARITY
ENABLECAL	: CALIBRATION ENABLE
IN NEG	: CDS INVERTING INPUT
IN POS	: CDS NON-INVERTING INPUT
LOAD	: DATA LOAD
$\overline{\text{OE}}$	: DIGITAL OUTPUT ENABLE
RESET	: CHIP RESET
RSTCCD	: CCD RESET PULSE DISCONNECT
SCLK	: SHIFT CLOCK FOR SERIAL REGISTER
SDI	: DATA INPUT FOR SERIAL REGISTER
SHD	: CDS CLOCK
SHP	: CDS CLOCK
$\overline{\text{STBY1}}$, $\overline{\text{STBY2}}$	: STANDBY CONTROL
VRB	: BOTTOM ADC REFERENCE
VRBO	: INTERNAL BIAS FOR VRB
VRT	: TOP ADC REFERENCE
VRTO	: INTERNAL BIAS FOR VRT

OUTPUT

DB0 - DB9	: ANALOG-TO-DIGITAL CONVERTED DATA
OVR	: OVER RANGE OUTPUT BIT
SYNC	: DIGITAL OUTPUT FOR TESTING
UNDER	: UNDER RANGE OUTPUT BIT

