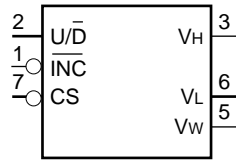
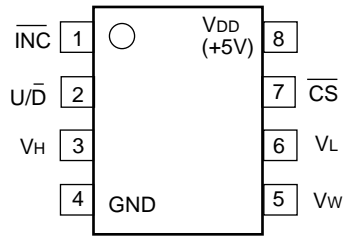

C-MOS DIGITALLY CONTROLLED POTENTIOMETER -TOP VIEW-



V_H ; HIGH TERMINAL
V_W ; WIPER TERMINAL
V_L ; LOW TERMINAL
U/ \bar{D} ; UP/DOWN INPUT
 $\bar{I}N\bar{C}$; INCREMENT INPUT
 $\bar{C}S$; CHIP SELECT INPUT

