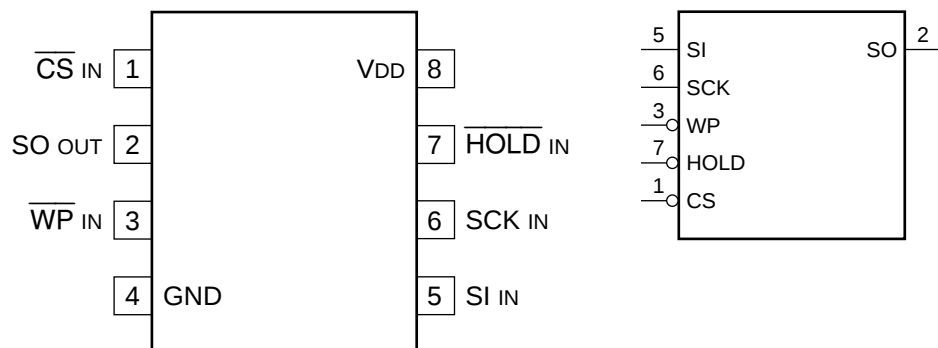


C-MOS 4 K (512 × 8)-BIT EEPROM

—TOP VIEW—

**INPUT**

$\overline{\text{CS}}$: CHIP SELECT
 $\overline{\text{HOLD}}$: HOLD
 SCK : SERIAL CLOCK
 $\overline{\text{SI}}$: SERIAL DATA
 $\overline{\text{WP}}$: WRITE PROTECT

OUTPUT

SO : SERIAL DATA

