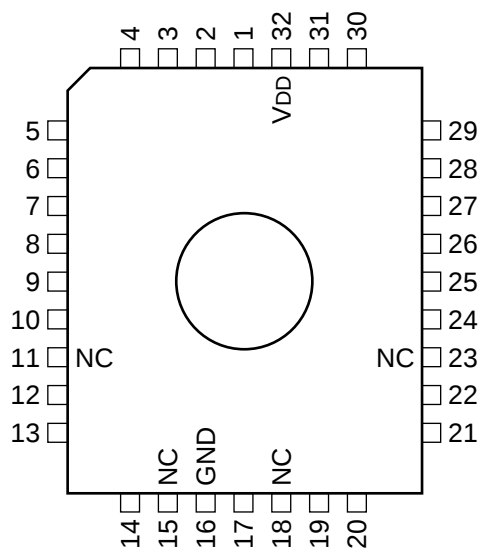


C-MOS 128 K (16 K × 8)-BIT EPROM

—TOP VIEW—



PINS				MODE	MODE
CS1/VPP	CS2	CS3	CS4		
0	0	1	0	READ	DATA OUT
0	0	1	0	PROGRAM VERIFY	DATA OUT
VPP	1	x	x	PROGRAM	DATA IN
1	x	x	x	OUTPUT DISABLE	HI-Z
x	1	x	x	OUTPUT DISABLE	HI-Z
x	x	0	x	OUTPUT DISABLE	HI-Z
x	x	x	1	OUTPUT DISABLE	HI-Z

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	A9	17	O	D3
2	I	A8	18	—	NC
3	I	A7	19	O	D4
4	I	A6	20	O	D5
5	I	A5	21	O	D6
6	I	A4	22	O	D7
7	I	A3	23	—	NC
8	I	A2	24	I	CS4
9	I	A1	25	I	CS3
10	I	A0	26	I	CS2
11	—	NC	27	I	CS1/VPP
12	O	D0	28	I	A13
13	O	D1	29	I	A12
14	O	D2	30	I	A11
15	—	NC	31	I	A10
16	—	GND	32	—	VDD

INPUT

A0 - A13 : ADDRESS

CS1 - CS4 : CHIP SELECT

VPP : PROGRAM SUPPLY VOLTAGE

INPUT/OUTPUT

D0 - D7 : DATA

