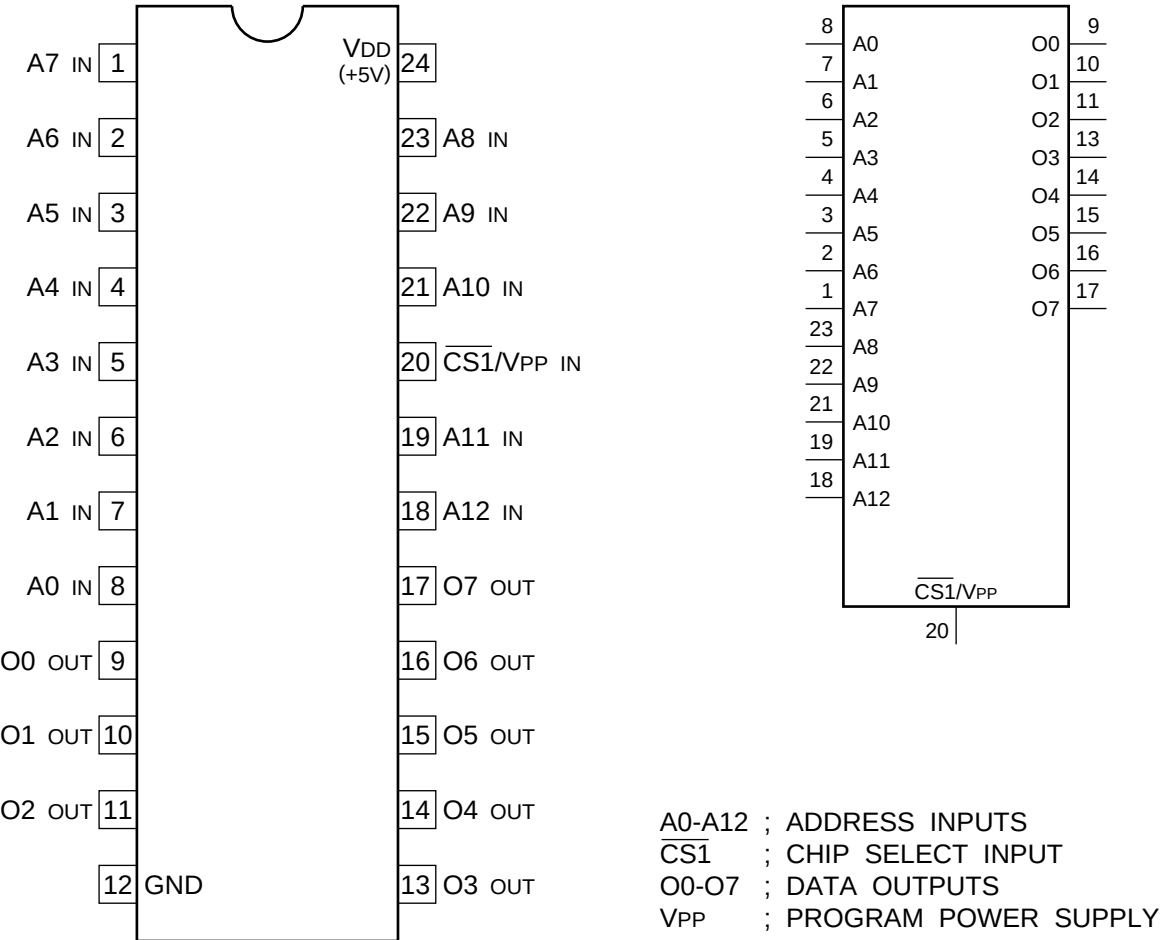

C-MOS 64K (8,192 X 8)-BIT HIGH SPEED ERASABLE PROM
-TOP VIEW-



MODE SELECTION

MODE \ PINS	$\overline{CS1}/V_{PP}$	OUTPUTS
READ	0	DATA OUT
OUTPUT DISABLE	1	HI-Z
PROGRAM	V _{PP}	DATA IN
PROGRAM VERIFY	0	DATA OUT

0 ; LOW LEVEL
1 ; HIGH LEVEL
HI-Z ; HIGH IMPEDANCE