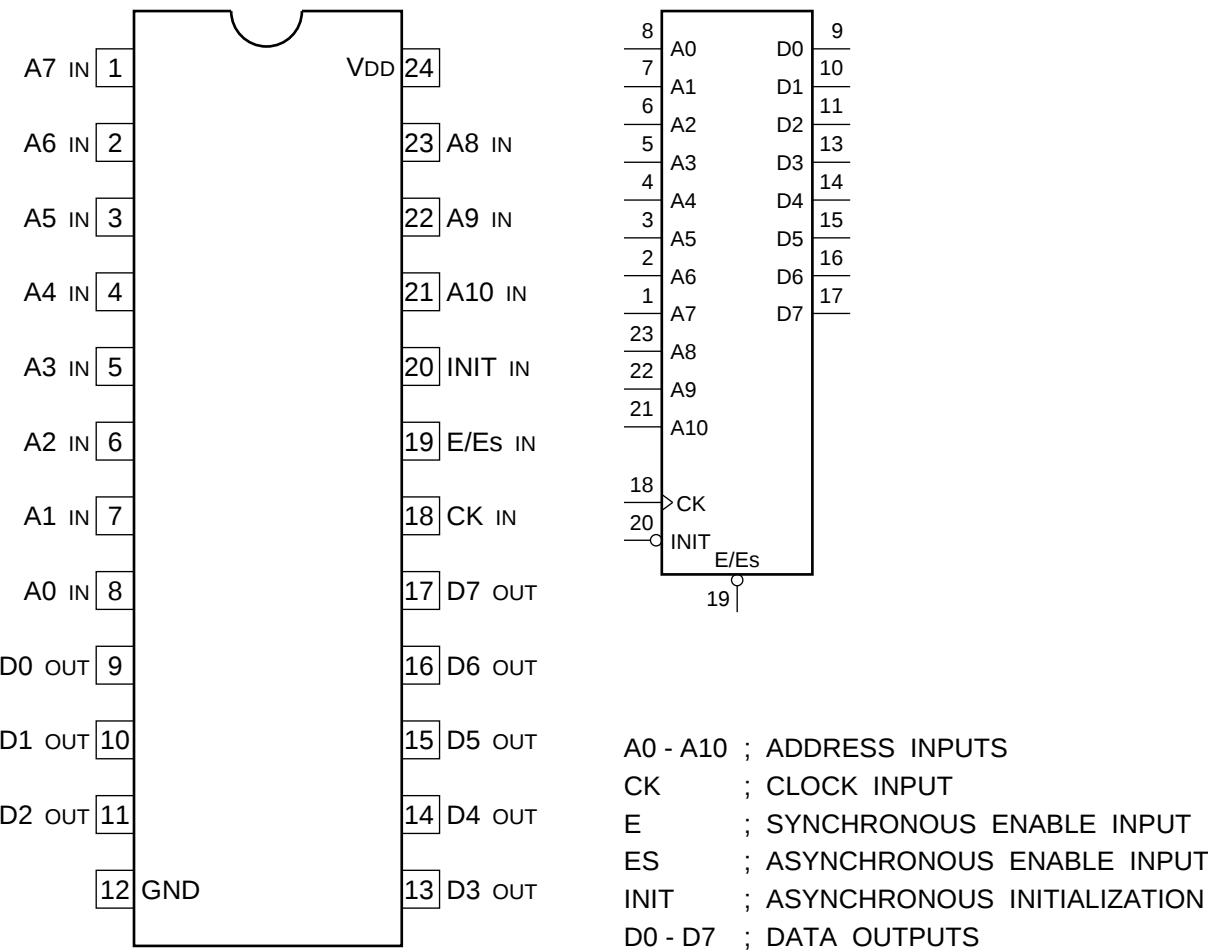


C-MOS 16 K (2,048 × 8)-BIT EPROM (WITH REGISTER)

—TOP VIEW—



A1	A2	CK	E/Es	INIT	OUTPUTS	FUNCTION
x	x	x	0	1	DATA OUT	READ
x	x	x	1	1	HI-Z	OUTPUT DISABLE
x	x	0	1	VPP	DATA IN	PGM
x	x	1	0	VPP	DATA OUT	PGM VERIFY
x	x	1	1	VPP	HI-Z	PGM INH
x	x	0	1	VPP	DATA IN	INTELLIGENT PGM
VPP	1	0	1	VPP	HI-Z	PGM SYNCH ENABLE
VPP	0	0	1	VPP	DATA IN	PGM INITIAL BYTE
x	x	VPP	1	0	ZEROS	BLANK CHECK ZEROS

0 : LOW LEVEL HI-Z : HIGH IMPEDANCE

1 : HIGH LEVEL VPP : PROGRAM POWER SUPPLY

x : DON'T CARE (+13 V to +14 V)

