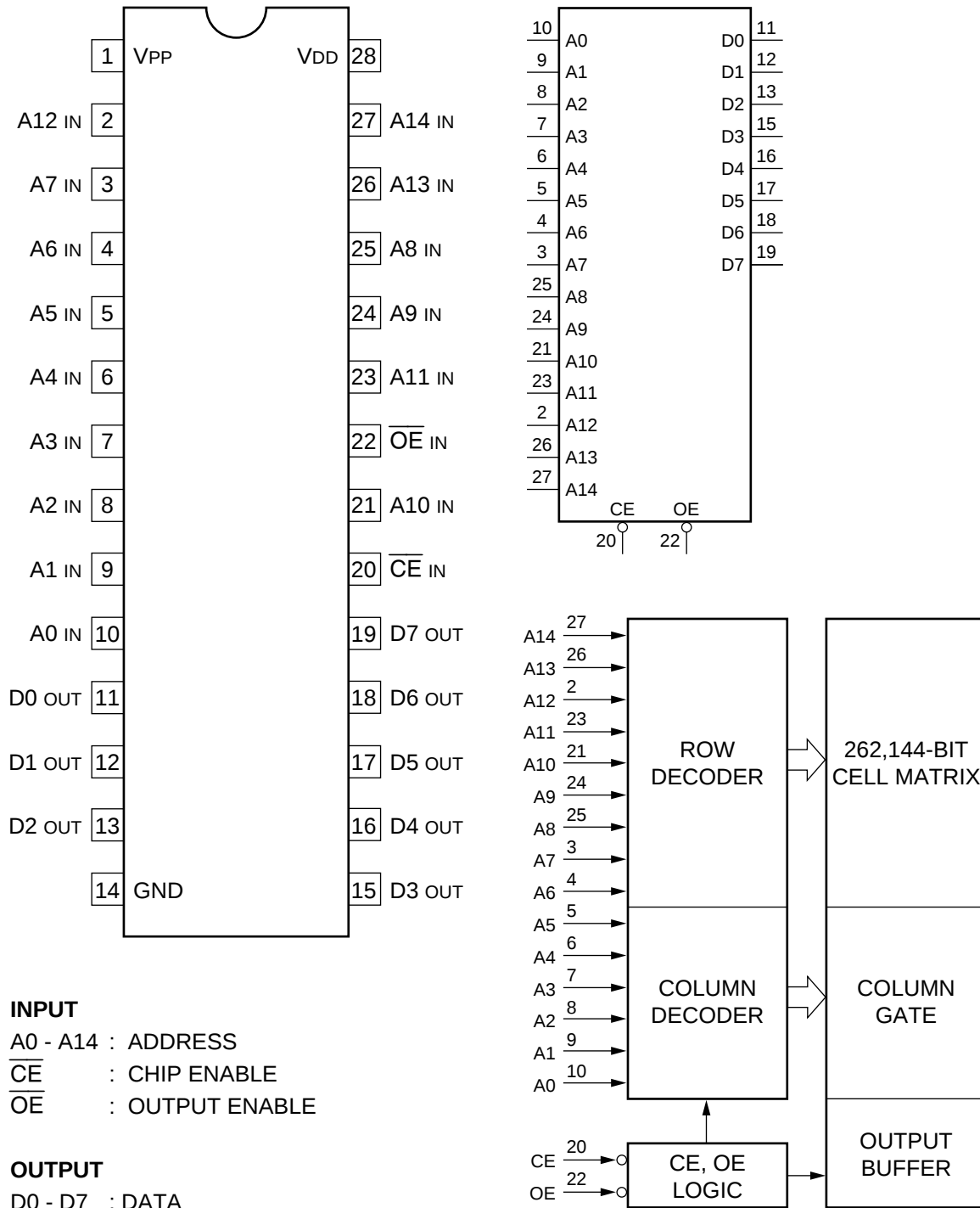


C-MOS 256 K (32 K × 8)-BIT ONE TIME PROM WITH 3-STATE OUTPUTS

—TOP VIEW—



An	$\overline{\text{CE}}$	$\overline{\text{OE}}$	V _{DD}	V _{PP}	Dn	FUNCTION
An	0	0	+5 V	+5 V	DOUT	READ
An	x	1	+5 V	+5 V	HI-Z	OUTPUT DISABLE
x	1	x	+5 V	+5 V	HI-Z	STANDBY
An	0	1	+5 V	+13.5 V	DIN	PGM
An	x	0	+5 V	+13.5 V	DOUT	PGM VERIFY
x	1	1	+5 V	+13.5 V	HI-Z	PGM INH
A0	0	0	+5 V	+5 V	CODE OUT*	ELECTRONIC SIGNATURE*

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE

* SEE FOLLOWING DESCRIPTION.

ELECTRONIC SIGNATURE FOR PROM WRITER
ADDRESS CETTINGS IN READ MODE

A1 - A8	A9	A10 - A14	V _{PP}
0	12.75 V	0	+5 V

	A0	CODE DATA								
		D7	D6	D5	D4	D3	D2	D1	D0	
MAKER CODE	0	0	0	0	0	0	0	1	0	23H
DEVICE CODE	1	1	1	0	0	0	0	0	0	COH