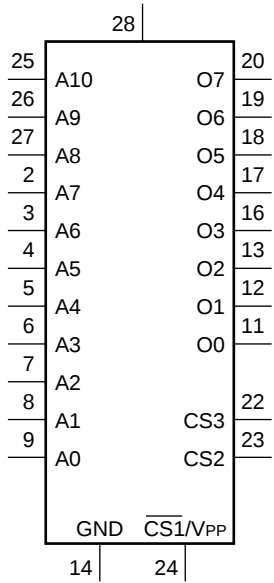
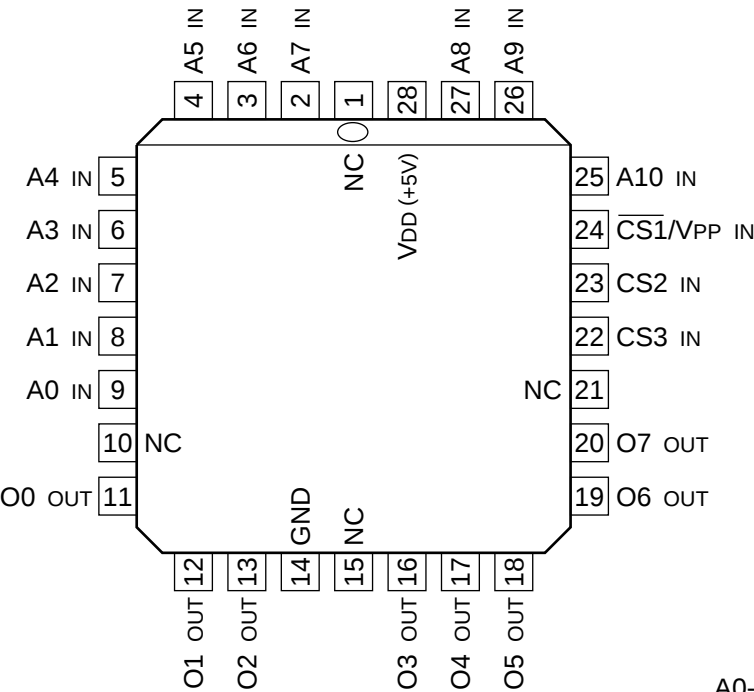
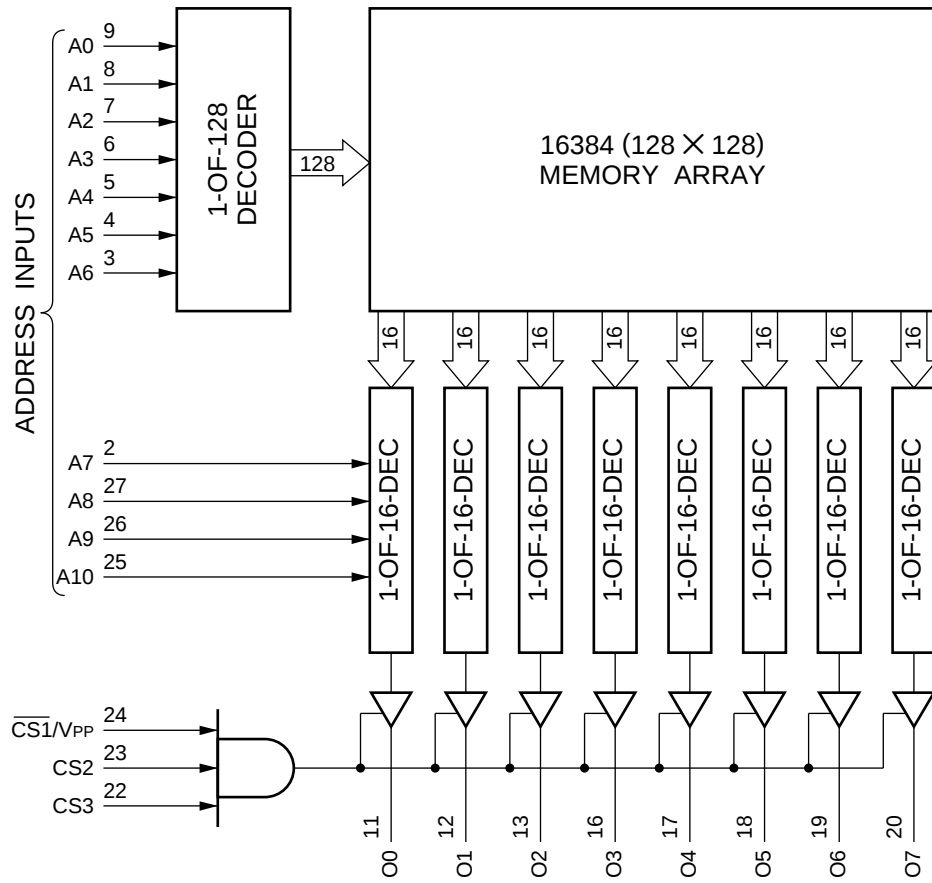

C-MOS 16K (2K X 8)-BIT HIGH SPEED PROM
-TOP VIEW-



A0-A10 ; ADDRESS INPUTS
O0-O7 ; DATA OUTPUTS
CS1, CS2, CS3 ; CHIP SELECT INPUTS
VPP ; PROGRAM POWER SUPPLY

PINS MODE	CS1/ VPP	CS2	CS3	OUTPUTS
READ	0	1	1	DATA OUT
OUTPUT DISABLE	1	X	X	HI-Z
OUTPUT DISABLE	X	0	X	HI-Z
PROGRAM	VPP	X	X	DATA IN
PROGRAM VERIFY	0	1	1	DATA OUT
OUTPUT DISABLE	X	X	0	HI-Z

0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; DON'T CARE
HI-Z ; HIGH IMPEDANCE



* ABOVE DIAGRAM SHOWS CONDITIONS BEFORE PROGRAMMING.