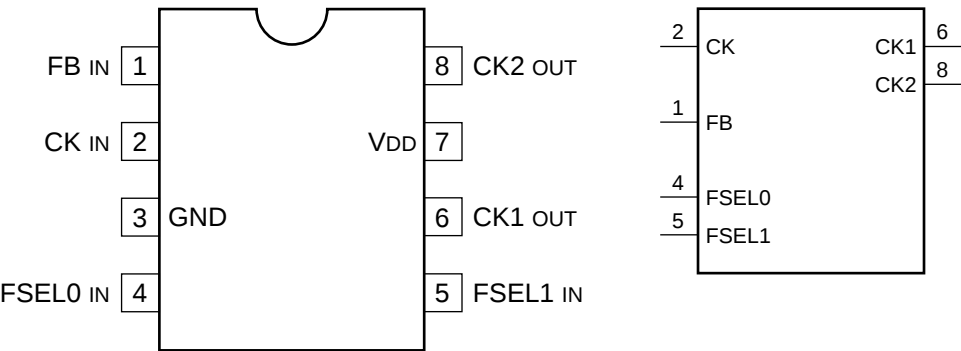


C-MOS FREQUENCY MULTIPLIER

—TOP VIEW—



FREQUENCY SELECTION

FSEL1	FSEL0	FB FREQ.
0	0	$2 \cdot f_{IN}$
0	1	$4 \cdot f_{IN}$
1	0	f_{IN}
1	1	$8 \cdot f_{IN}$

0 : LOW LEVEL
1 : HIGH LEVEL

INPUT

CK : REFERENCE CLOCK
FB : FEEDBACK
FSEL0, FSEL1 : FREQUENCY SELECT

OUTPUT

CK1, CK2 : CLOCK

