

# UTC 78LXXM LINEAR INTEGRATED CIRCUIT

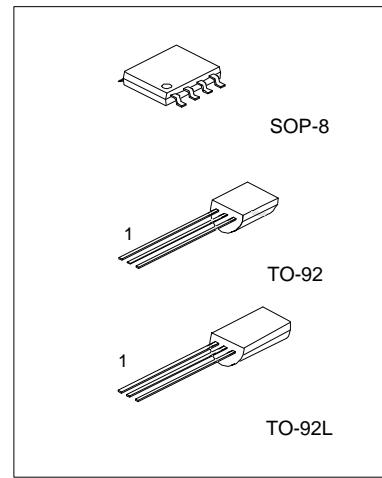
## 3-TERMINAL 0.2A POSITIVE VOLTAGE REGULATOR

### DESCRIPTION

The UTC 78LXXM family is monolithic fixed voltage regulator integrated circuit. They are suitable for applications that required supply current up to 200mA.

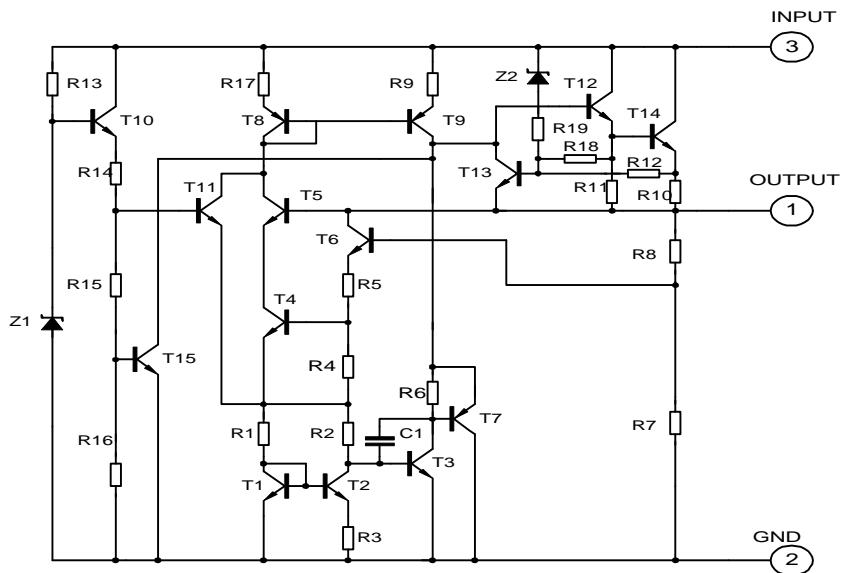
### FEATURES

- \*Output current up to 200mA
- \*Fixed output voltage of 5V, 6V, 8V, 9V, 10V, 12V, 15V, 18V and 24V available
- \*Thermal overload shutdown protection
- \*Short circuit current limiting



SOP-8: 1:Output 2,3,6,7:GND 8:Input  
4,5:N.C.  
TO-92: 1:Output 2:GND; 3:Input  
TO-92L: 1:Output 2:GND; 3:Input

### TEST CIRCUIT



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## ABSOLUTE MAXIMUM RATINGS

( Operating temperature range applies unless otherwise specified )

PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Input voltage(for $V_o=5\sim 9V$ ) (for $V_o=12\sim 24V$ )	$V_I$		30	V
	$V_I$		35	V
Output Current	$I_o$		200	mA
Power Dissipation				
SOP-8	PD		300	mW
TO-92			300	
TO-92L			500	
Operating Junction Temperature Range	TOPR	-20	+150	°C
Storage Temperature Range	TSTG	-55	+150	°C

## UTC78L05M ELECTRICAL CHARACTERISTICS

(  $V_I=10V$ ,  $I_o=40mA$ ,  $0^\circ C < T_j < 125^\circ C$ ,  $C_1=0.33\mu F$ ,  $C_0=0.1\mu F$ , unless otherwise specified )(Note 1)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_o$	$T_j=25^\circ C$	4.9	5.0	5.1	V
		$7V \leq V_I \leq 20V, I_o = 1mA - 40mA$	4.9		5.1	V
		$7V \leq V_I \leq V_{MAX}$	4.9		5.1	V
		$I_o = 1mA - 200mA$				(note 2)
Load Regulation	$\Delta V_o$	$T_j=25^\circ C, I_o = 1mA - 100mA$		11	60	mV
		$T_j=25^\circ C, I_o = 1mA - 40mA$		5.0	30	mV
Line regulation	$\Delta V_o$	$7V \leq V_I \leq 20V, T_j = 25^\circ C$		8	150	mV
		$8V \leq V_I \leq 20V, T_j = 25^\circ C$		6	100	mV
Quiescent Current	$I_q$			2.0	5.5	mA
Quiescent Current Change	$\Delta I_q$	$8V \leq V_I \leq 20V$			1.5	mA
		$1mA \leq V_I \leq 40mA$			0.1	mA
Output Noise Voltage	$V_N$	$10Hz \leq f \leq 100kHz$		40		uV
Temperature coefficient of $V_o$	$\Delta V_o / \Delta T$	$I_o = 5mA$		-0.65		mV/°C
Ripple Rejection	RR	$8V \leq V_I \leq 20V, f = 120Hz, T_j = 25^\circ C$	41	80		dB
Dropout Voltage	$V_d$	$T_j = 25^\circ C$		1.7		V

## UTC78L06M ELECTRICAL CHARACTERISTICS

(  $V_I=12V$ ,  $I_o=40mA$ ,  $0^\circ C < T_j < 125^\circ C$ ,  $C_1=0.33\mu F$ ,  $C_0=0.1\mu F$ , unless otherwise specified )( Note 1 )

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_o$	$T_j=25^\circ C$	5.75	6.0	6.25	V
		$8.5V \leq V_I \leq 20V, I_o = 1mA - 40mA$	5.7		6.3	V
		$8.5V \leq V_I \leq V_{MAX}, I_o = 1mA - 200mA$	5.7		6.3	V
		(note 2)				
Load Regulation	$\Delta V_o$	$T_j=25^\circ C, I_o = 1mA - 100mA$		12.8	80	mV
		$T_j=25^\circ C, I_o = 1mA - 70mA$		5.8	40	mV
Line regulation	$\Delta V_o$	$8.5V \leq V_I \leq 20V, T_j = 25^\circ C$		64	175	mV
		$9V \leq V_I \leq 20V, T_j = 25^\circ C$		54	125	mV
Quiescent Current	$I_q$			3.9	6.0	mA
Quiescent Current Change	$\Delta I_q$	$9V \leq V_I \leq 20V$			1.5	mA

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
	$\Delta Iq$	$1mA \leq V_i \leq 40mA$			0.1	mA
Output Noise Voltage	$V_N$	$10Hz \leq f \leq 100kHz$		49		uV
Temperature coefficient of $V_o$	$\Delta V_o/\Delta T$	$I_o=5mA$		0.75		mV/°C
Ripple Rejection	RR	$10V \leq V_i \leq 20V, f=120Hz, T_j=25^{\circ}C$	40	46		dB
Dropout Voltage	$V_d$	$T_j=25^{\circ}C$		1.7		V

## UTC78L08M ELECTRICAL CHARACTERISTICS

( $V_i=14V, I_o=40mA, 0^{\circ}C < T_j < 125^{\circ}C, C_1=0.33\mu F, C_0=0.1\mu F$ , unless otherwise specified)(Note 1)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_o$	$T_j=25^{\circ}C$	7.7	8.0	8.3	V
		$10.5V \leq V_i \leq 23V, I_o=1mA - 40mA$	7.6		8.4	V
		$10.5V \leq V_i \leq V_{MAX}, I_o=1mA - 200mA$	7.6		8.4	V (note 2)
Load Regulation	$\Delta V_o$	$T_j=25^{\circ}C, I_o=1mA - 100mA$		15	80	mV
		$T_j=25^{\circ}C, I_o=1mA - 70mA$		8.0	40	mV
Line regulation	$\Delta V_o$	$10.5V \leq V_i \leq 23V, T_j=25^{\circ}C$		10	175	mV
		$11V \leq V_i \leq 23V, T_j=25^{\circ}C$		8	125	mV
Quiescent Current	$I_q$			2.0	5.5	mA
Quiescent Current Change	$\Delta Iq$	$11V \leq V_i \leq 23V$			1.5	mA
	$\Delta Iq$	$1mA \leq V_i \leq 40mA$			0.1	mA
Output Noise Voltage	$V_N$	$10Hz \leq f \leq 100kHz$		49		uV
Temperature coefficient of $V_o$	$\Delta V_o/\Delta T$	$I_o=5mA$		0.75		mV/°C
Ripple Rejection	RR	$11V \leq V_i \leq 23V, f=120Hz, T_j=25^{\circ}C$	39	70		dB
Dropout Voltage	$V_d$	$T_j=25^{\circ}C$		1.7		V

## UTC78L09M ELECTRICAL CHARACTERISTICS

( $V_i=15V, I_o=40mA, 0^{\circ}C < T_j < 125^{\circ}C, C_1=0.33\mu F, C_0=0.1\mu F$ , unless otherwise specified)(Note 1)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	$V_o$	$T_j=25^{\circ}C$	8.64	9.0	9.36	V
		$11.5V \leq V_i \leq 24V, I_o=1mA - 40mA$	8.55		9.45	V
		$11.5V \leq V_i \leq V_{MAX}, I_o=1mA - 200mA$	8.55		9.45	V (note 2)
Load Regulation	$\Delta V_o$	$T_j=25^{\circ}C, I_o=1mA - 100mA$		20	90	mV
		$T_j=25^{\circ}C, I_o=1mA - 40mA$		10	45	mV
Line regulation	$\Delta V_o$	$11.5V \leq V_i \leq 24V, T_j=25^{\circ}C$		90	200	mV
		$13V \leq V_i \leq 24V, T_j=25^{\circ}C$		100	150	mV
Quiescent Current	$I_q$			2.0	6.0	mA
Quiescent Current Change	$\Delta Iq$	$13V \leq V_i \leq 24V$			1.5	mA
	$\Delta Iq$	$1mA \leq V_i \leq 40mA$			0.1	mA
Output Noise Voltage	$V_N$	$10Hz \leq f \leq 100kHz$		49		uV
Temperature coefficient of $V_o$	$\Delta V_o/\Delta T$	$I_o=5mA$		0.75		mV/°C
Ripple Rejection	RR	$12V \leq V_i \leq 23V, f=120Hz, T_j=25^{\circ}C$	38	44		dB
Dropout Voltage	$V_d$	$T_j=25^{\circ}C$		1.7		V

# UTC78LXXM LINEAR INTEGRATED CIRCUIT

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## UTC78L10M ELECTRICAL CHARACTERISTICS

(VI=16V, Io=40mA, 0°C < Tj < 125°C, C1=0.33uF, Co=0.1uF, unless otherwise specified)(Note 1)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	VO	Tj=25°C	9.6	10.0	10.4	V
		12.5V <= VI <= 23V, Io=1mA - 40mA	9.5		10.5	V
		12.5V <= VI <= VMAX, Io=1mA - 200mA	9.5		10.5	V (note 2)
Load Regulation	$\Delta V_o$	Tj=25°C, IO=1mA - 100mA		20	94	mV
		Tj=25°C, IO=1mA - 70mA		10	47	mV
Line regulation	$\Delta V_o$	12.5V <= VI <= 23V, Tj=25°C		100	220	mV
		14V <= VI <= 23V, Tj=25°C		200	170	mV
Quiescent Current	Iq			4.2	6.5	mA
Quiescent Current Change	$\Delta I_q$	12.5V <= VI <= 23V			1.5	mA
	$\Delta I_q$	1mA <= VI <= 40mA			0.1	mA
Output Noise Voltage	VN	10Hz <= f <= 100kHz		74		uV
Temperature coefficient of Vo	$\Delta V_o/\Delta T$	Io=5mA		0.95		mV/°C
Ripple Rejection	RR	15V <= VI <= 23V, f=120Hz, Tj=25°C	38	43		dB
Dropout Voltage	Vd	Tj=25°C		1.7		V

## UTC78L12M ELECTRICAL CHARACTERISTICS

(VI=19V, Io=40mA, 0°C < Tj < 125°C, C1=0.33uF, Co=0.1uF, unless otherwise specified)(Note 1)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	VO	Tj=25°C	11.5	12	12.5	V
		14.5V <= VI <= 27V, Io=1mA - 40mA	11.4		12.6	V
		14.5V <= VI <= VMAX, Io=1mA - 200mA	11.4		12.6	V (note 2)
Load Regulation	$\Delta V_o$	Tj=25°C, IO=1mA - 100mA		25	150	mV
		Tj=25°C, IO=1mA - 40mA		12	75	mV
Line regulation	$\Delta V_o$	14.5V <= VI <= 27V, Tj=25°C		25	300	mV
		16V <= VI <= 27V, Tj=25°C		20	250	mV
Quiescent Current	Iq			2.0	6.0	mA
Quiescent Current Change	$\Delta I_q$	16V <= VI <= 27V			1.5	mA
	$\Delta I_q$	1mA <= VI <= 40mA			0.1	mA
Output Noise Voltage	VN	10Hz <= f <= 100kHz		80		uV
Temperature coefficient of Vo	$\Delta V_o/\Delta T$	Io=5mA		-1.0		mV/°C
Ripple Rejection	RR	15V <= VI <= 25V, f=120Hz, Tj=25°C	37	65		dB
Dropout Voltage	Vd	Tj=25°C		1.7		V

# UTC78LXXM LINEAR INTEGRATED CIRCUIT

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## UTC78L15M ELECTRICAL CHARACTERISTICS

(VI=23V, Io=40mA, 0°C < Tj < 125°C, C1=0.33uF, Co=0.1uF, unless otherwise specified)(Note 1)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	VO	Tj=25°C	14.4	15	15.6	V
		17.5V <= VI <= 30V, Io=1mA - 40mA	14.25		15.75	V
		17.5V <= VI <= VMAX, Io=1mA - 200mA	14.25		15.75	V (note 2)
Load Regulation	$\Delta V_o$	Tj=25°C, IO=1mA - 100mA		20	150	mV
		Tj=25°C, IO=1mA - 70mA		25	150	mV
Line regulation	$\Delta V_o$	17.5V <= VI <= 30V, Tj=25°C		25	150	mV
		20V <= VI <= 30V, Tj=25°C		15	75	mV
Quiescent Current	Iq			2.2	6.5	mA
Quiescent Current Change	$\Delta I_q$	20V <= VI <= 30V			1.5	mA
	$\Delta I_q$	1mA <= VI <= 40mA			0.1	mA
Output Noise Voltage	VN	10Hz <= f <= 100kHz		90		uV
Temperature coefficient of Vo	$\Delta V_o/\Delta T$	Io=5mA		-1.3		mV/°C
Ripple Rejection	RR	18.5V <= VI <= 28.5V, f=120Hz, Tj=25°C	34	63		dB
Dropout Voltage	Vd	Tj=25°C		1.7		V

## UTC78L18M ELECTRICAL CHARACTERISTICS

(VI=27V, Io=40mA, 0°C < Tj < 125°C, C1=0.33uF, Co=0.1uF, unless otherwise specified)(Note 1)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	VO	Tj=25°C	17.3	18	18.7	V
		21V <= VI <= 33V, Io=1mA - 40mA	17.1		18.9	V
		21V <= VI <= VMAX, Io=1mA - 200mA	17.1		18.9	V (note 2)
Load Regulation	$\Delta V_o$	Tj=25°C, IO=1mA - 100mA		30	170	mV
		Tj=25°C, IO=1mA - 40mA		15	85	mV
Line regulation	$\Delta V_o$	21V <= VI <= 33V, Tj=25°C		145	300	mV
		22V <= VI <= 33V, Tj=25°C		135	250	mV
Quiescent Current	Iq			2.0	6.0	mA
Quiescent Current Change	$\Delta I_q$	21V <= VI <= 33V			1.5	mA
	$\Delta I_q$	1mA <= VI <= 40mA			0.1	mA
Output Noise Voltage	VN	10Hz <= f <= 100kHz		150		uV
Temperature coefficient of Vo	$\Delta V_o/\Delta T$	Io=5mA		-1.8		mV/°C
Ripple Rejection	RR	23V <= VI <= 33V, f=120Hz, Tj=25°C	34	48		dB
Dropout Voltage	Vd	Tj=25°C		1.7		V

# UTC78LXXM LINEAR INTEGRATED CIRCUIT

## UTC78L24M ELECTRICAL CHARACTERISTICS

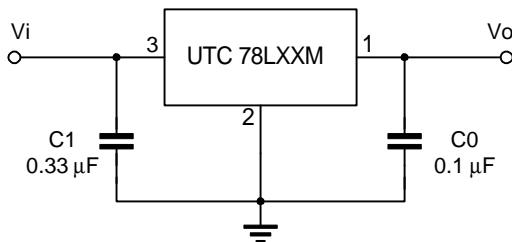
(VI=33V, IO=40mA, 0°C < Tj < 125°C, C1=0.33μF, Co=0.1μF, unless otherwise specified) (Note 1)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Voltage	VO	Tj=25°C	23	24	25	V
		27V <= VI <= 38V, IO=1mA - 40mA	22.8		25.2	V
		27V <= VI <= VMAX, IO=1mA - 200mA	22.8		25.2	V (note 2)
Load Regulation	ΔVo	Tj=25°C, IO=1mA - 100mA		40	200	mV
		Tj=25°C, IO=1mA - 40mA		20	100	mV
Line regulation	ΔVo	27V <= VI <= 38V, Tj=25°C		160	300	mV
		28V <= VI <= 38V, Tj=25°C		150	250	mV
Quiescent Current	Iq			2.2	6.0	mA
Quiescent Current Change	ΔIq	27V <= VI <= 38V			1.5	mA
	ΔIq	1mA <= VI <= 40mA			0.1	mA
Output Noise Voltage	VN	10Hz <= f <= 100kHz		200		uV
Temperature coefficient of Vo	ΔVo/ΔT	IO=5mA		-2.0		mV/°C
Ripple Rejection	RR	27V <= VI <= 38V, f=120Hz, Tj=25°C	34	45		dB
Dropout Voltage	Vd	Tj=25°C		1.7		V

Note 1: The Maximum steady state usable output current are dependent on input voltage, heat sinking, lead length of the package and copper pattern of PCB. The data above represent pulse test conditions with junction temperatures specified at the initiation of test.

Note 2: Power dissipation < 0.5W

## APPLICATION CIRCUIT



Note 1: To specify an output voltage, substitute voltage value for "XXM".

Note 2: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

# UTC78LXXM LINEAR INTEGRATED CIRCUIT

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## TYPICAL PERFORMANCE CHARACTERISTICS

Fig.1 Ambient temperature vs. Power dissipation

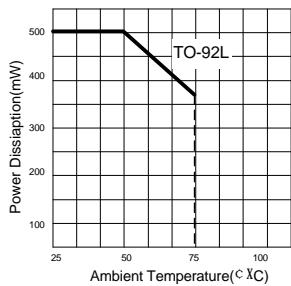


Fig.2 UTC78L05M Output Voltage vs. Ambient temperature

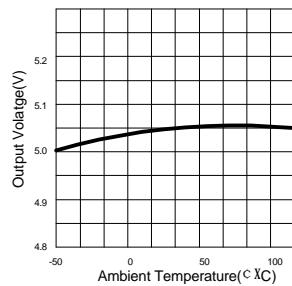


Fig.3 UTC78L12M Power dissipation vs. Ambient temperature

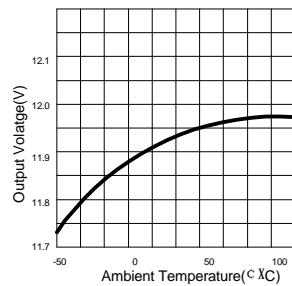


Fig.4 Output Characteristics ( $I_p=0\text{mA}, T_j=25^\circ\text{C} XC$ )

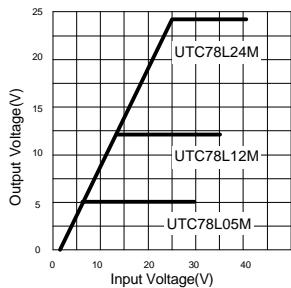


Fig.5 UTC78L05M Dropout Characteristics ( $T_j=25^\circ\text{C} XC$ )

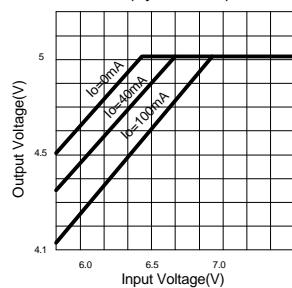


Fig.6 Short Circuit output current ( $T_j=25^\circ\text{C} XC$ )

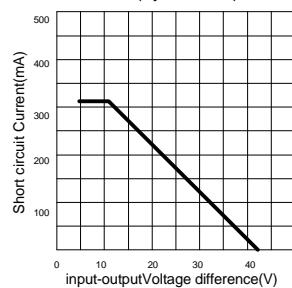


Fig.7 UTC78L12/24M quiescent current vs output current ( $T_j=25^\circ\text{C} XC$ )

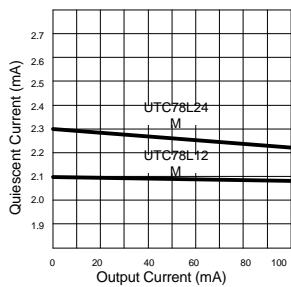


Fig.8 UTC78L05M Quiescent Current vs. Input Voltage ( $I_o=0\text{mA}, T_j=25^\circ\text{C} XC$ )

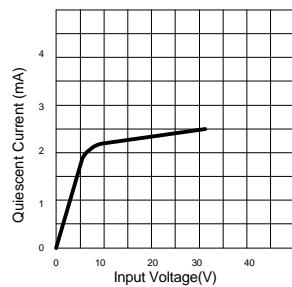


Fig.9 Peak output current vs Dropout voltage difference

