

IC INFORMATION

Function		UPD63760GJ	
Digital Servo/Data Processor		1/2	E
Type	CMOS	Model	CDS-4027ZM/E

● Pin Functions (UPD63760GJ)

Pin No.	Pin Name	I/O	Function and Operation
1	R.GND		GND for DRAM I/F
2	RST	I	Input of reset
3-7	AB12-8	I	Address bus 12-8 from the microcomputer
8-15	AD7-0	I/O	Address/data bus 7-0 to the microcomputer
16	$\overline{CS}$	I	Chip selection
17	ASTB	I	Address strobe
18	READ	I	Control signals (read)
19	WRITE	I	Control signals (write)
20	WAIT	O	Control signals (wait)
21	INTQ		Interruption signals to the external microcomputer
22	IFMODE	I	Switching between the data buses (16bit/8bit)
23	D.VDD		Power supply for digital circuits
24	XTALEN1	I	Permission to oscillate 16.9344MHz
25	XTALEN2	I	Permission to oscillate 24.576MHz
26	DA.VDD		Power supply for DAC
27	ROUT	O	Output of audio for the right channel
28	DA.GND		GND for DAC
29	R+	O	Output of the right channel audio PWM
30	R-	O	Output of the right channel audio PWM
31	REGC		Connected to the capacitor for band gap
32	L-	O	Output of the left channel audio PWM
33	L+	O	Output of the left channel audio PWM
34	DA.GND		GND for DAC
35	LOUT	O	Output of audio for the left channel
36	DA.VDD		Power supply for DAC
37	X.VDD		Power supply for the crystal oscillator
38	XTAL1		Connected to the crystal oscillator (16.9344MHz)
39	$\overline{XTAL1}$		Connected to the crystal oscillator (16.9344MHz)
40, 41	X.GND		Ground for the crystal oscillator
42	$\overline{XTAL2}$		Connected to the crystal oscillator (24.576MHz)
43	XTAL2		Connected to the crystal oscillator (24.576MHz)
44	X.VDD		Power supply for the crystal oscillator
45	D.GND		GND for digital circuits
46	DIN	I	Input of audio data
47	DOUT	O	Output of audio data
48	SCKIN	I	Clock input for audio data
49	SCKO	O	Clock output for audio data
50	LRCKIN	I	Input of LRCK for audio data
51	LRCK	O	Output LRCK for audio data
52	TESTX	O	Output for tests
53	RFOK	O	Output of RFOK
54	C16M	O	Output of 16.9344MHz
55	TESTEN	I	Connected to GND
56	TEST4	I	Connected to GND
57	D.VDD		Power supply for digital circuits
58	RFCK/HOLD	O	Output of RFCK/HOLD signal
59	WFCK/MIRR	O	Output of WFCK/MIRR signal
60	PLCK	O	Output of PLCK
61	LOCK	O	Output of LOCK
62	C1D1	O	Information on error correction
63	C1D2	O	Information on error correction
64	C2D1(RMUTE)	O	Information on error correction (mute for Rch)
65	C2D2(LMUTE)	O	Information on error correction (mute for Lch)
66	C2D3	O	Information on error correction
67	D.GND		Ground for digital circuits
68	RAS	O	Output of DRAM RAS

IC INFORMATION

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Pin No.	Pin Name	I/O	Function and Operation
69	CAS0	O	Output of DRAM Lower CAS
70	CAS1	O	Output of DRAM Upper CAS
71	WE	O	Output of DRAM WE
72	OE	O	Output of DRAM OE
73-88	RDB0-15	I/O	Input/output of DRAM Data0-15
89	D.GND		Ground for digital circuits
90-99	RA0-9	O	Output of DRAM Address0-9
100	D.VDD		Power supply for digital circuits
101-104	TEST0-3	I	Connected to GND
105	FD	O	Output of focus drive PWM
106	TD	O	Output of tracking drive PWM
107	SD	O	Output of thread drive PWM
108	MD	O	Output of spindle drive PWM
109	A.VDD		Power supply for the analog system
110	ATEST	O	Analog tests
111	EFM	O	Output of EFM signals
112	ASY	I	Input of asymmetry
113	C3T		Connection to the capacitor for detecting 3T
114	A.GND		Ground for the analog system
115	RFI	I	Input of RF
116	AGCO	O	Output of RF
117	AGCI	I	Input of AGC
118	RFO	O	Output of RF(AGC)
119, 120	EQ2, 1		Equalizer 2, 1
121	RF2-	I	Reversal input of RF2
122	RF-	I	Reversal input of RF
123	A.GND		Ground for the analog system
124	A	I	Input of A
125	C	I	Input of C
126	B	I	Input of B
127	D	I	Input of D
128	F	I	Input of F
129	E	I	Input of E
130	A.VDD		Power supply for the analog system
131	REFOUT	O	Output of reference voltage
132	REFC		Connected to the capacitor for output of REFOUT
133	FE-	I	Reversal input of FE
134	FEO	O	Output of FE
135	TE-	I	Reversal input of TE
136	TEO	O	Output of TE
137	TE2	O	TE2
138	TEC	I	TEC
139	A.GND		Ground for the analog system
140	LDREGO	O	Output of REG voltage for APC
141	PD	I	Input of PD
142	LD	O	Output of LD
143	PN	I	Assignment of pickup polarity
144	A.VDD		Power supply for the analog system

UPD63760GJ

