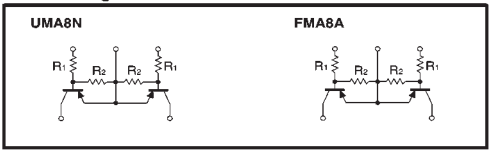
Emiter common (dual digital transistors)

UMA8N / FMA8A

●Features

1） Two DTA114Y chips in a UMT or SMT package.

●Circuit diagrams



●Absolute maximum ratings（Ta=25℃）

Parameter	Symbol	Limits	Unit
Supply voltage	V_{CC}	−50	V
Input voltage	V_{IN}	−40	V
		6	
Output current	I_O	−100	mA
Power dissipation	Pd	300 (TOTAL)	mW *
Storage temperature	Tstg	−50～+150	℃

* 200mW per element must not be exceeded.

●Package, marking, and packaging specifications

Part No.	UMA8A	FMA8A
Package	UMT5	SMT5
Marking	A8	A8
Code	TR	T148
Basic ordering unit (pieces)	3000	3000

●Electrical characteristics（Ta=25℃）

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	$V_{I(off)}$	—	—	−0.3	V	V_{CC}=−5V , I_O=−100 μA
	$V_{I(on)}$	−1.4	—	—		V_O=−0.3V , I_O=−1mA
Output voltage	$V_{O(on)}$	—	−0.1	−0.3	V	I_O=−10mA , I_I=−0.5mA
Input current	I_I	—	—	−0.88	mA	V_I=−5V
Output current	$I_{O(off)}$	—	—	−0.5	μA	V_{CC}=−50V , V_I=0V
DC current gain	G_I	68	—	—	—	I_O=−5mA , V_O=−5V
Input resistance	R_1	—	10	—	kΩ	—
Resistance ratio	R_2/R_1	3.7	4.7	5.7	—	—