Ordering number : ENN7084

ExPD (Excellent Power Device)

TND302S

General Purpose Driver for PDP Sustain Pulse Drive, DC / AC Motor Drive, Switching Power Supply, and DC / DC Converter Applications

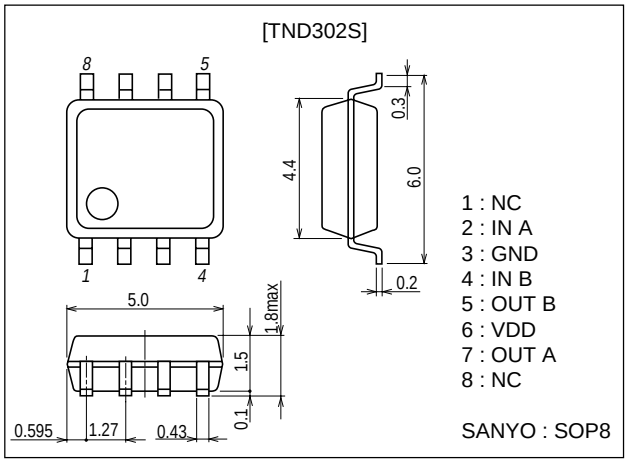
Features

- Dual buffer.
- Monolithic structure(High voltage CMOS process adopted).
- Withstand voltage of 25V is assured.
- Wide range of operating voltage : 4.5V to 25V.
- Peak outpout current : 2A.
- Fast switching time(25ns typical at 1000pF load).
- Fully compatible input to TTL/CMOS. (V_{IH}=not more than 2.6V, at V_{DD}=4.5 to 25V)

Package Dimensions

unit : mm

2199



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply Voltage	V_{DD}		0 to 25	V
Input Voltage	V_{IN}		GND–0.3 to V_{DD}+0.3	V
Allowable Power Dissipation	P_D max		0.3	W
Operating Temperature	Topr		–40 to +125	°C
Junction Temperature	Tj		–55 to +150	°C
Storage Temperature	Tstg		–55 to +150	°C

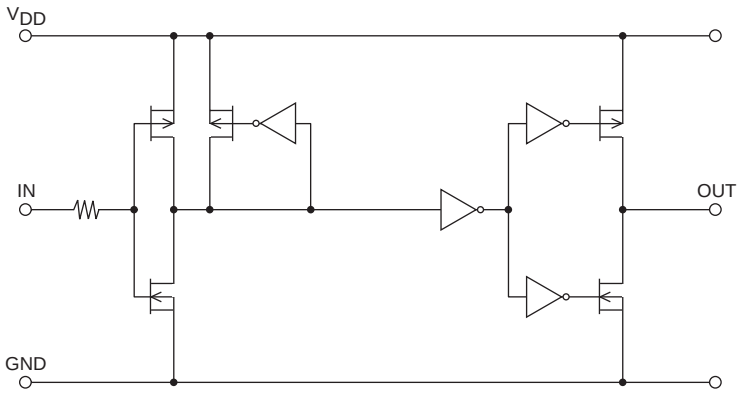
Electrical Characteristics (AC Characteristics) at Ta=25°C, V_{DD}=18V, V_{IN}=5V

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Turn-On Rise Time	t_r	C_L=1000pF		20	35	ns
Turn-Off Fall Time	t_f	C_L=1000pF		25	40	ns
Delay Time	t_D1	C_L=1000pF		30	45	ns
	t_D2	C_L=1000pF		45	60	ns

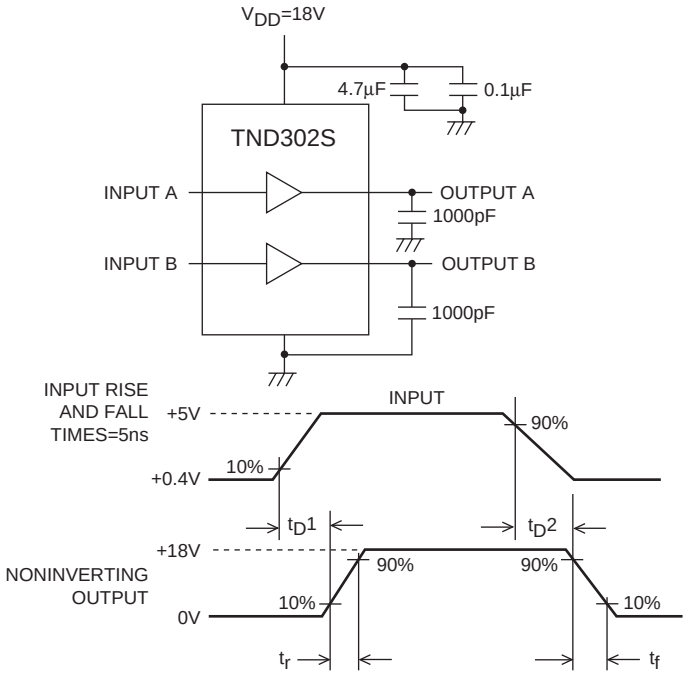
Electrical Characteristics (DC Characteristics) at Ta=25°C, V_{DD}=4.5 to 25V

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Logic “1” Input Voltage	V_{IH}		2.6			V
Logic “0” Input Voltage	V_{IL}				0.8	V
Input Bias Current	I_{IN}	V_{IN}=0 or V_{DD}	–1		1	μA
High Level Output Voltage	V_{OH}	I_O=0	V_{DD}–0.1			V
Low Level Output Voltage	V_{OL}	I_O=0			0.1	V
V_{DD} Supply Current	Isupp	V_{DD}=10V, V_{IN}=3V, (both inputs)		1.0	4.5	mA
		V_{DD}=10V, V_{IN}=0, (both inputs)			0.2	mA
Output High Short Circuit Pulsed Current	I_O+	V_{DD}=18V, PW≤10μs, V_{OUT}=0		2.0		A
Output Low Short Circuit Pulsed Current	I_O-	V_{DD}=18V, PW≤10μs, V_{OUT}=18V		2.0		A
Output On Resistance	ROUT	V_{DD}=18V, Iload=10mA, V_{OUT}=“H”		4	6	Ω
		V_{DD}=18V, Iload=10mA, V_{OUT}=“L”		3	5	Ω

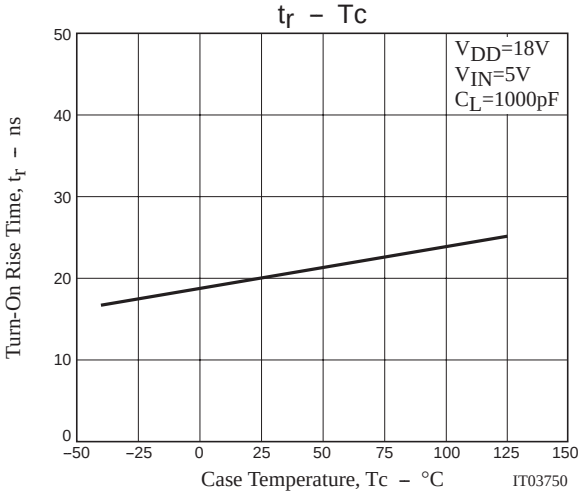
Block Diagram



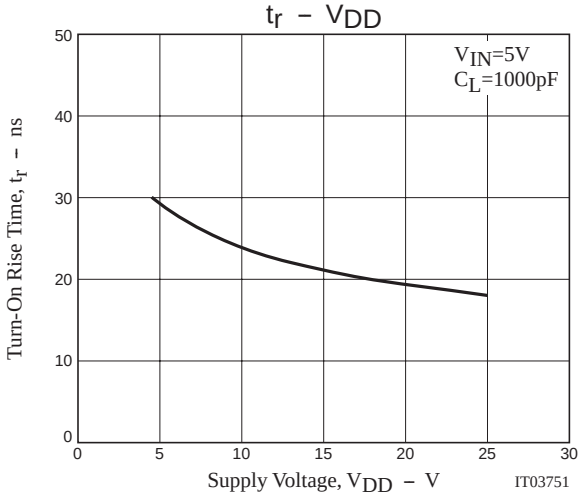
Switching Time Measuring Circuit



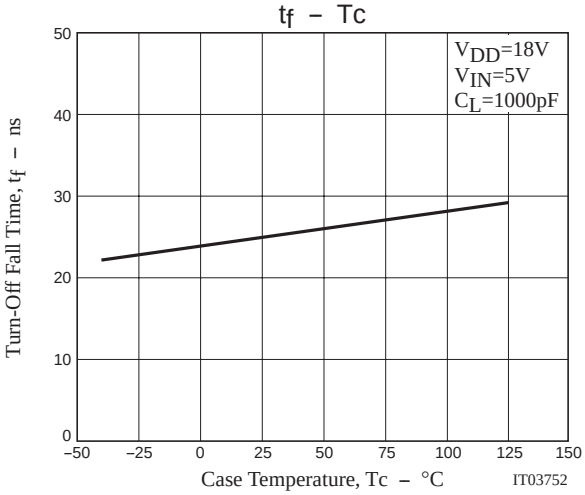
t_r – Tc



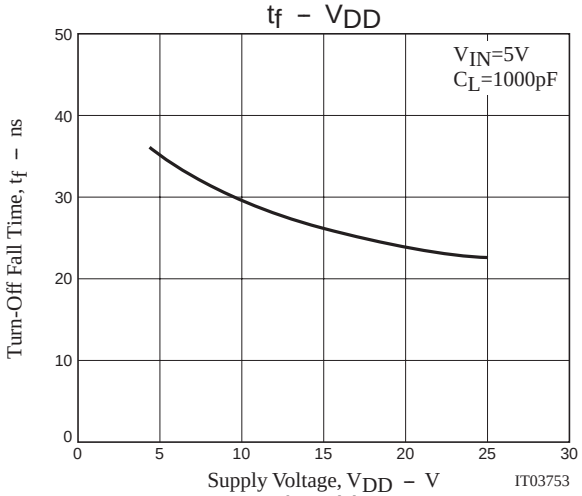
t_r – V_{DD}



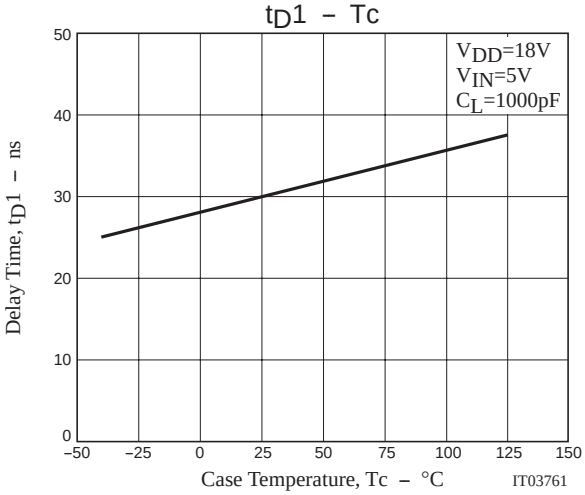
t_f – Tc



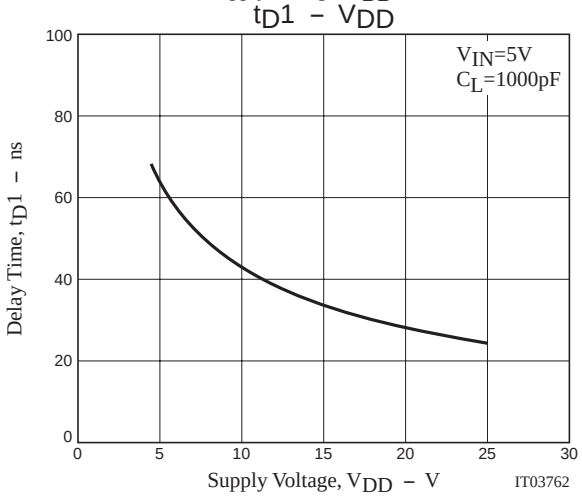
t_f – V_{DD}



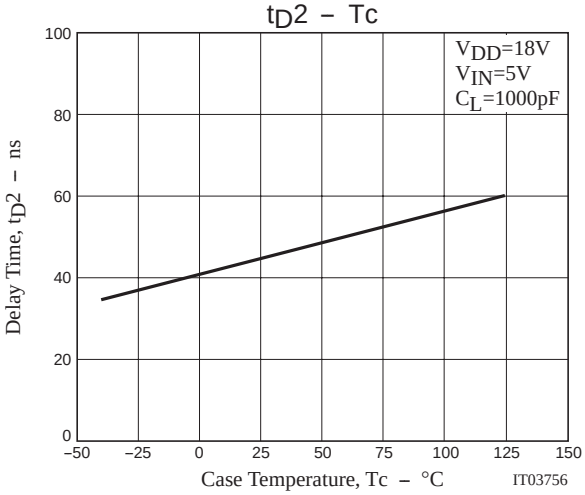
$t_{D}1$ – Tc



$t_{D}1$ – V_{DD}



$t_{D}2$ – Tc



$t_{D}2$ – V_{DD}