



TN1215-600B

STANDARD SCR

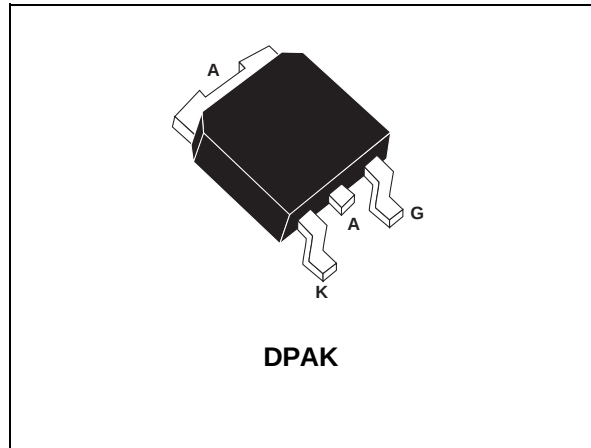
FEATURES

- $I_{T(RMS)} = 12\text{ A}$
- $V_{DRM} / V_{RRM} = 600\text{ V}$
- $I_{GT} < 15\text{ mA}$
- $I_{TSM} = 110\text{ A}$

DESCRIPTION

The TN1215-600B SCR uses a high performance TOPGLASS PNP technology.

This part is intended for general purpose applications using surface mount technology and requiring high surge capability (power tools, crowbar protection, voltage regulation, etc...).



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter		Value	Unit
V_{DRM} V_{RRM}	Repetitive peak off-state voltage	$T_j = 125^\circ\text{C}$	600	V
$I_{T(RMS)}$	RMS on-state current (180° conduction angle)	$T_c = 105^\circ\text{C}$	12	A
$I_{T(AV)}$	Average on-state current (180° conduction angle)	$T_c = 105^\circ\text{C}$	8	A
I_{TSM}	Non repetitive surge peak on-state current (T_j initial = 25°C)	$t_p = 8.3\text{ ms}$	115	A
		$t_p = 10\text{ ms}$	110	
I^2t	I^2t Value for fusing	$t_p = 10\text{ ms}$	60	A^2s
di/dt	Critical rate of rise of on-state current $I_G = 20\text{ mA}$ $dI_G/dt = 1\text{ A}/\mu\text{s}$.		50	$\text{A}/\mu\text{s}$
T_{stg} T_j	Storage junction temperature range Operating junction temperature range		- 40 to + 150 - 40 to + 125	$^\circ\text{C}$
T	Maximum temperature for soldering during 10s		260	$^\circ\text{C}$

TN1215-600B

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
Rth(j-a)	Junction to ambient (S=0.5cm ²)	70	°C/W
Rth(j-c)	Junction to case for D.C	1.5	°C/W

GATE CHARACTERISTICS

$P_{G(AV)} = 1W$ $P_{GM} = 10W$ ($t_p = 20\mu s$) $I_{GM} = 4A$ ($t_p = 20\mu s$) $V_{RGM} = 5V$

ELECTRICAL CHARACTERISTICS

Symbol	Test Conditions		Type	Value	Unit
I_{GT}	$V_D = 12V$ (DC) $R_L = 33\Omega$	$T_j = 25^\circ C$	MIN	2	mA
			MAX	15	
V_{GT}	$V_D = 12V$ (DC) $R_L = 33\Omega$	$T_j = 25^\circ C$	MAX	1.5	V
V_{GD}	$V_D = V_{DRM}$ $R_L = 3.3k\Omega$	$T_j = 125^\circ C$	MIN	0.2	V
I_H	$I_T = 100mA$ Gate open	$T_j = 25^\circ C$	MAX	40	mA
V_{TM}	$I_{TM} = 24A$ $t_p = 380\mu s$	$T_j = 25^\circ C$	MAX	1.6	V
I_{DRM}	$V_D = V_{DRM}$	$T_j = 25^\circ C$	MAX	10	μA
I_{RRM}	$V_R = V_{RRM}$	$T_j = 125^\circ C$	MAX	2	mA
dV/dt	Linear slope up to $V_D = 67\%V_{DRM}$ Gate open	$T_j = 125^\circ C$	MIN	200	V/ μs

ORDERING INFORMATION

TN	12	15	-	600	B	(-TR)
STANDARD SCR	CURRENT	SENSITIVITY		V_{DRM} / V_{RRM}	PACKAGES : B: DPAK	Tape & Reel

Fig. 1: Maximum average power dissipation versus average on-state current .

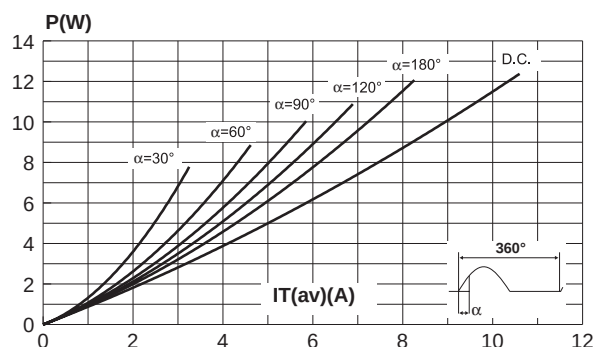


Fig. 2 : Correlation between maximum average power dissipation and maximum allowable temperatures (T_{amb} and T_{case}) for different thermal resistances.

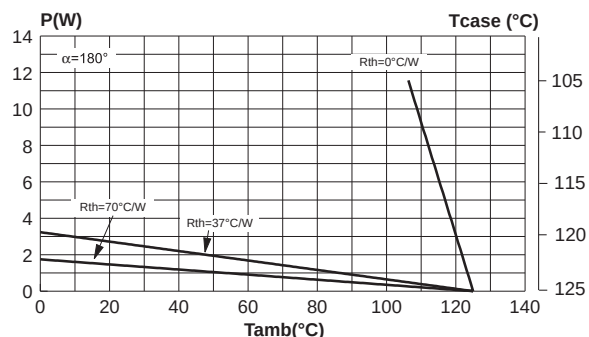


Fig. 3: Average and D.C. on-state current versus case temperature.

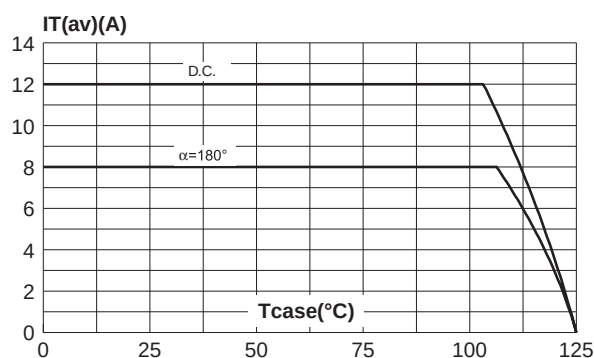


Fig. 4: Average and D.C. on-state current versus ambient temperature (Printed circuit board FR4, SCu=0.5cm2).

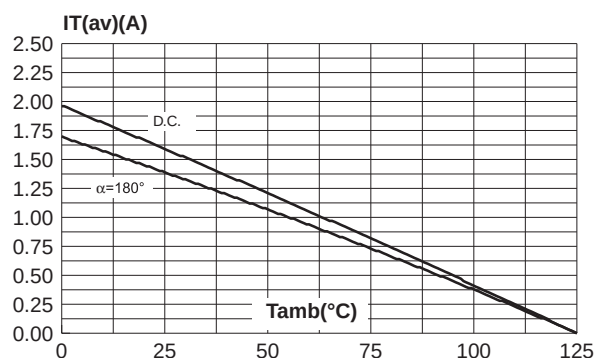


Fig. 5: Relative variation of thermal impedance junction to ambient versus pulse duration (recommended pad layout).

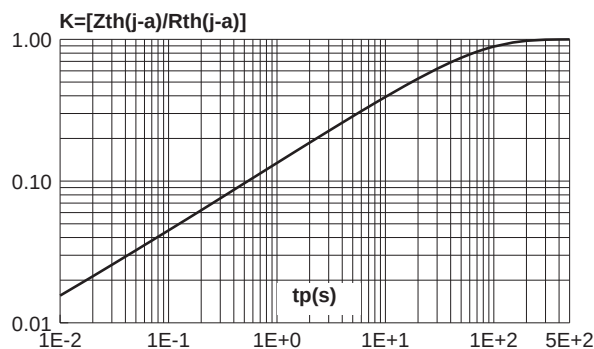


Fig. 6: Relative variation of gate trigger current and holding current versus junction temperature

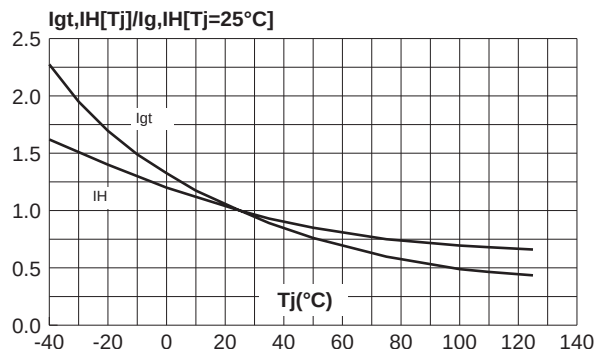


Fig 7: Non repetitive surge peak on-state current versus number of cycles.

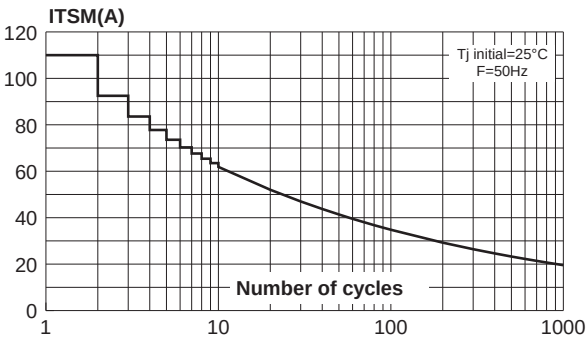


Fig 8: Non repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10$ ms, and corresponding value of I^2t .

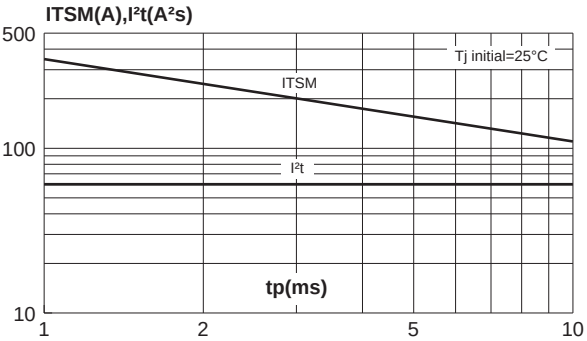


Fig 9: On-state characteristics (maximum values).

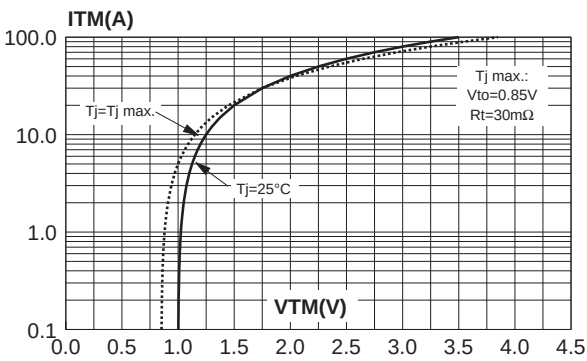
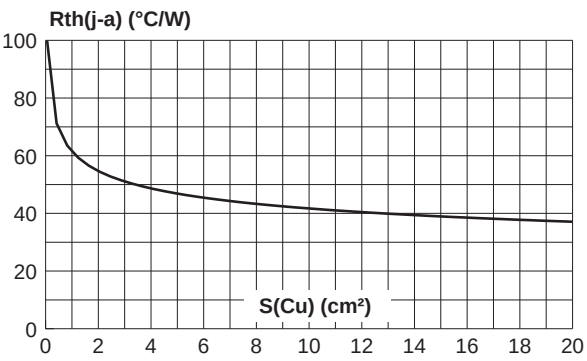
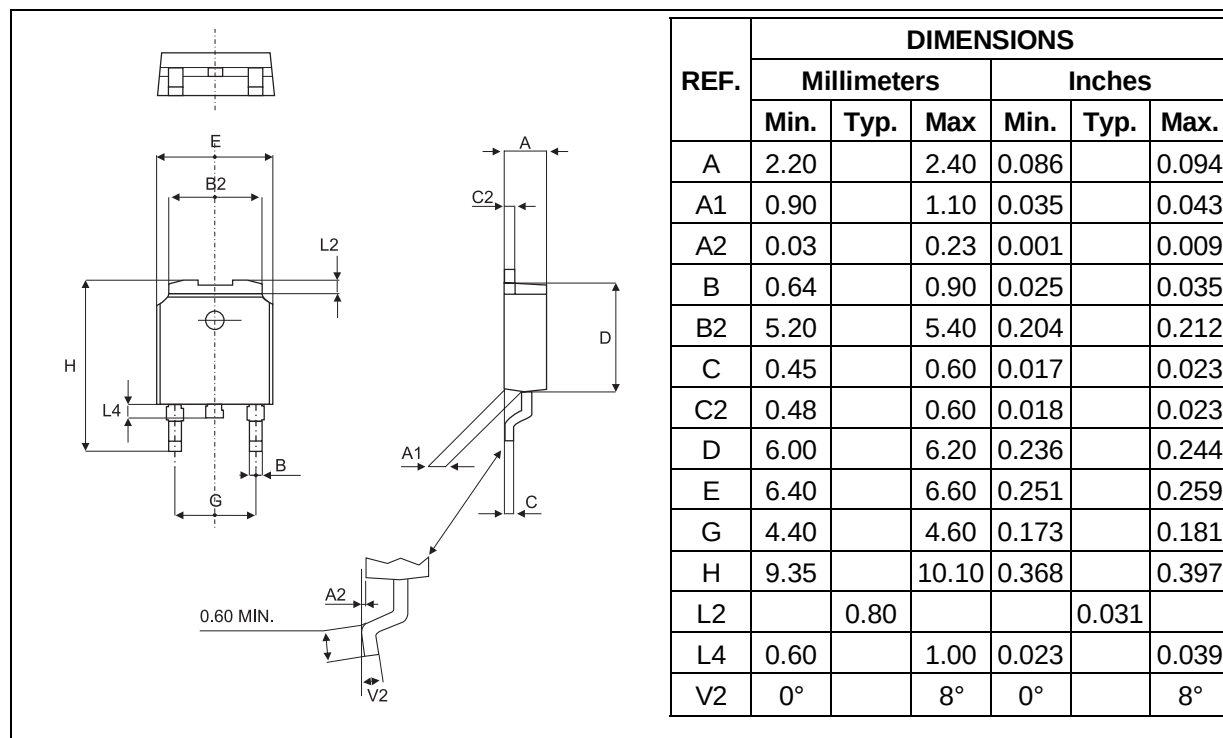


Fig 10: Thermal resistance junction to ambient versus copper surface under tab (Epoxy printed circuit board FR4, copper thickness: 35 μ m).

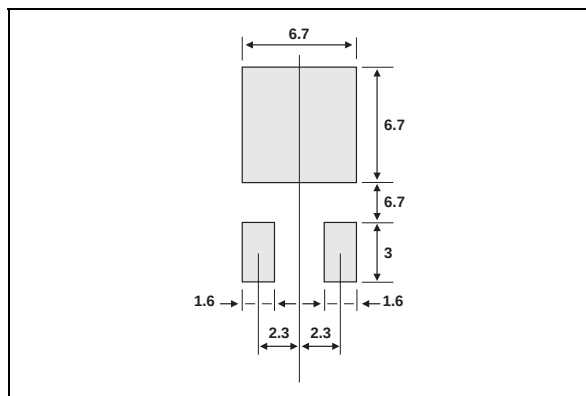


PACKAGE MECHANICAL DATA

DPAK



FOOT PRINT DIMENSIONS (in millimeters)



WEIGHT : 0.30g

MARKING

TYPE	MARKING
TN1215-600B	TN 1215 6

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