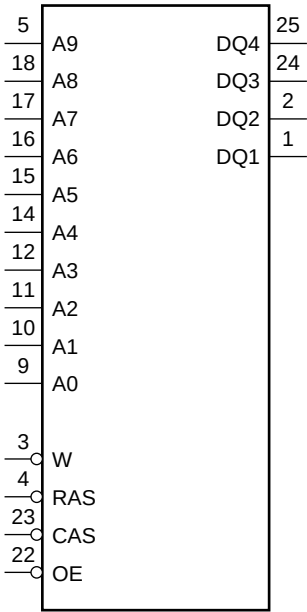
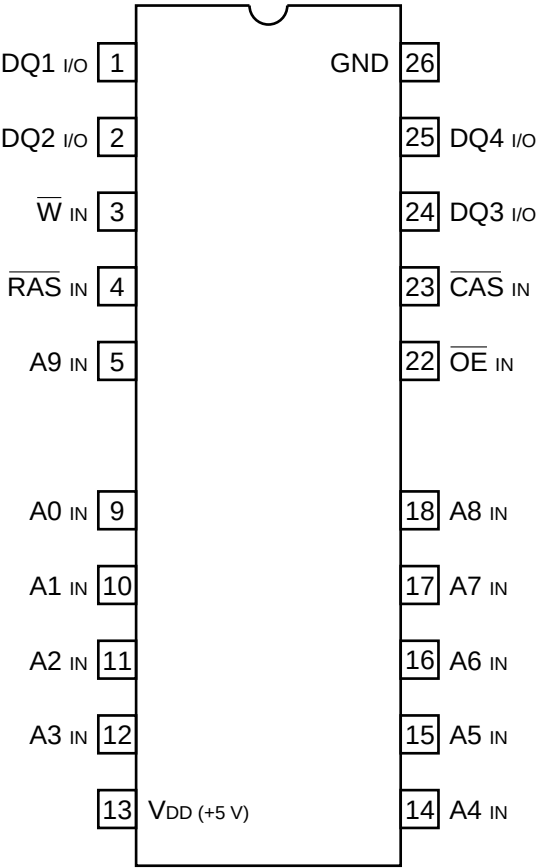

C-MOS 4M (1,048,576×4)-BIT DYNAMIC RAM WITH SELF-REFRESH
– TOP VIEW –



INPUT
A0 - A9 ; ADDRESS
CAS ; COLUMN ADDRESS STROBE
OE ; OUTPUT ENABLE
RAS ; ROW ADDRESS STROBE
W ; WRITE ENABLE

INPUT/OUTPUT
DQ1 - DQ4 ; DATA