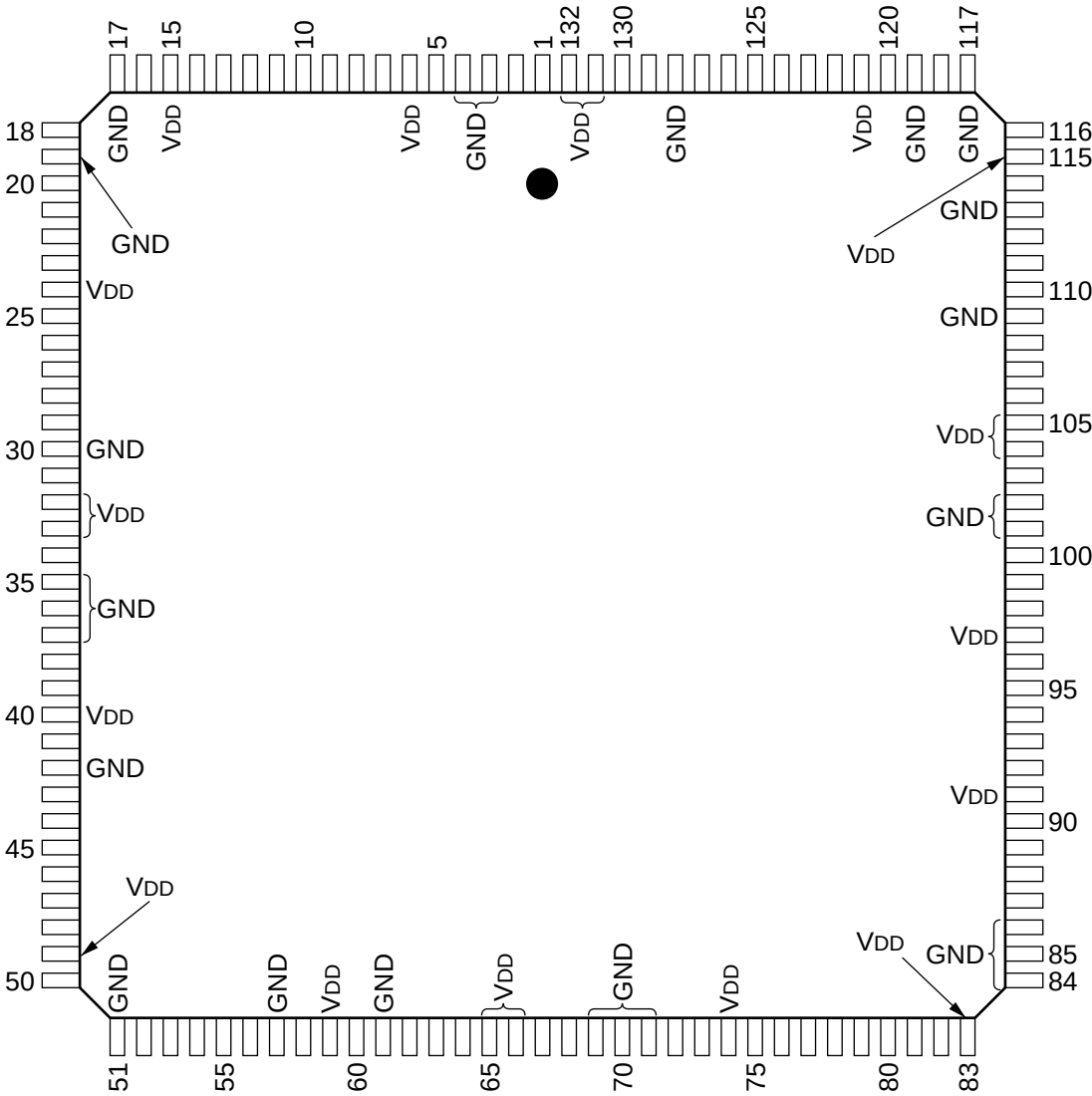
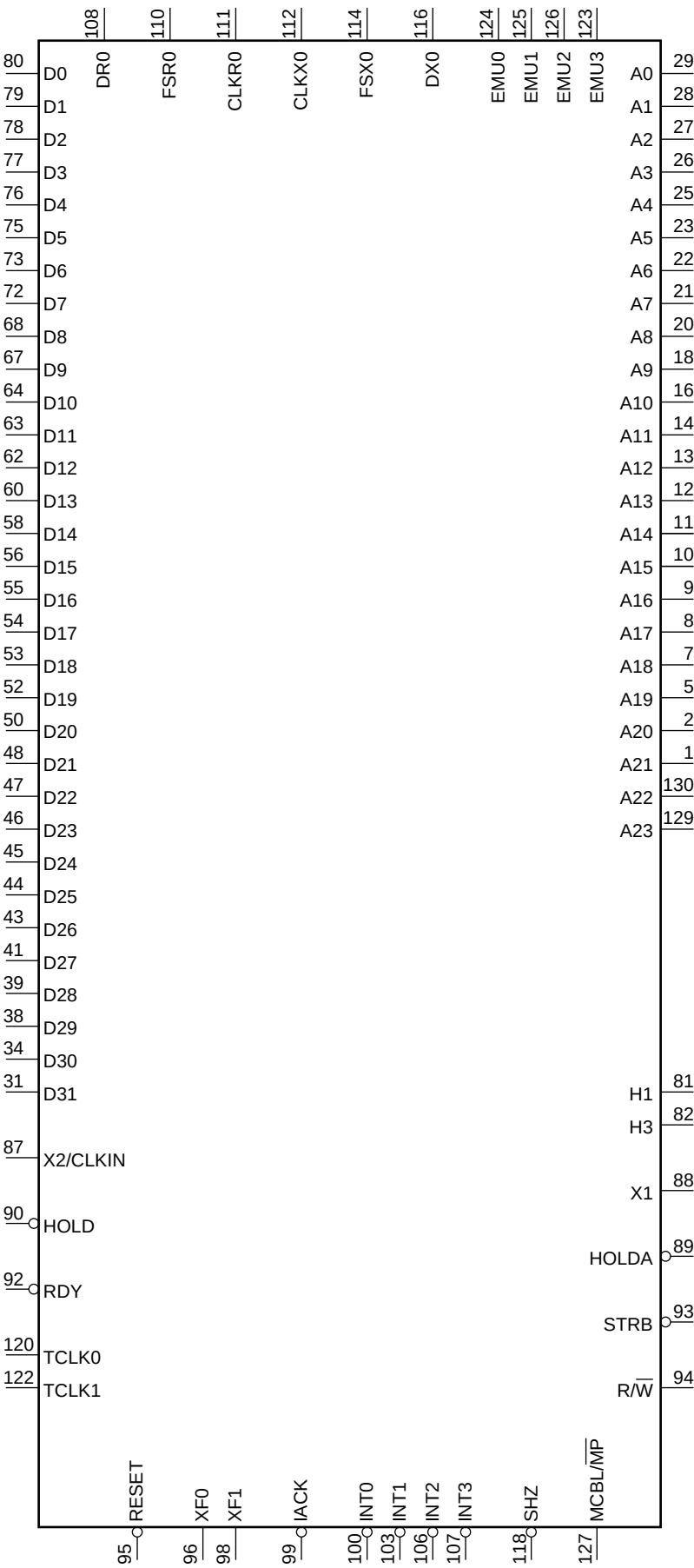


C-MOS DIGITAL SIGNAL PROCESSOR

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	O	A21	28	O	A1	55	I/O	D16	82	O	H3	109	—	GND
2	O	A20	29	O	A0	56	I/O	D15	83	—	V _{DD}	110	I/O	FSR0
3	—	GND	30	—	GND	57	—	GND	84	—	GND	111	I/O	CLKR0
4	—	GND	31	I/O	D31	58	I/O	D14	85	—	GND	112	I/O	CLKX0
5	O	A19	32	—	V _{DD}	59	—	V _{DD}	86	—	GND	113	—	GND
6	—	V _{DD}	33	—	V _{DD}	60	I/O	D13	87	I	X2/CLKIN	114	I/O	FSX0
7	O	A18	34	I/O	D30	61	—	GND	88	O	X1	115	—	V _{DD}
8	O	A17	35	—	GND	62	I/O	D12	89	O	HOLDA	116	I/O	DX0
9	O	A16	36	—	GND	63	I/O	D11	90	I	HOLD	117	—	GND
10	O	A15	37	—	GND	64	I/O	D10	91	—	V _{DD}	118	O	SHZ
11	O	A14	38	I/O	D29	65	—	V _{DD}	92	I	RDY	119	—	GND
12	O	A13	39	I/O	D28	66	—	V _{DD}	93	O	STRB	120	I/O	TCLK0
13	O	A12	40	—	V _{DD}	67	I/O	D9	94	O	R/W	121	—	V _{DD}
14	O	A11	41	I/O	D27	68	I/O	D8	95	I	RESET	122	I/O	TCLK1
15	—	V _{DD}	42	—	GND	69	—	GND	96	I/O	XF0	123	O	EMU3
16	O	A10	43	I/O	D26	70	—	GND	97	—	V _{DD}	124	I	EMU0
17	—	GND	44	I/O	D25	71	—	GND	98	I/O	XF1	125	I	EMU1
18	O	A9	45	I/O	D24	72	I/O	D7	99	O	IACK	126	I	EMU2
19	—	GND	46	I/O	D23	73	I/O	D6	100	I	INT0	127	I	MCBL/MP
20	O	A8	47	I/O	D22	74	—	V _{DD}	101	—	GND	128	—	GND
21	O	A7	48	I/O	D21	75	I/O	D5	102	—	GND	129	O	A23
22	O	A6	49	—	V _{DD}	76	I/O	D4	103	I	INT1	130	O	A22
23	O	A5	50	I/O	D20	77	I/O	D3	104	—	V _{DD}	131	—	V _{DD}
24	—	V _{DD}	51	—	GND	78	I/O	D2	105	—	V _{DD}	132	—	V _{DD}
25	O	A4	52	I/O	D19	79	I/O	D1	106	I	INT2			
26	O	A3	53	I/O	D18	80	I/O	D0	107	I	INT3			
27	O	A2	54	I/O	D17	81	O	H1	108	I/O	DR0			



INPUT

EMU0 - EMU2 ; RESERVED
HOLD ; HOLD
INT0-INT3 ; EXTERNAL INTERRUPTS
MCBL/MP ; MICROCOMPUTER BOOT LOADER/MICROPROCESSOR MODE
SHZ ; SHUT DOWN HIGH-IMPEDANCE
RDY ; READY
RESET ; RESET
 X2/CLKIN ; CRYSTAL/CLOCK

OUTPUT

A0 - A23 ; 24-BIT ADDRESS PORT
 EMU3 ; RESERVED
 H1 ; EXTERNAL H1 CLOCK
 H3 ; EXTERNAL H3 CLOCK
HOLDA ; HOLD ACKNOWLEDGE
IACK ; INTERRUPT ACKNOWLEDGE
STRB ; EXTERNAL ACCESS STROBE
 R/W ; READ/WRITE SIGNAL
 X1 ; CRYSTAL

INPUT/OUTPUT

CLKR0 ; SERIAL PORT 0 RECEIVE CLOCK
 CLKX0 ; SERIAL PORT 0 TRANSMIT CLOCK
 D0 - D31 ; 32-BIT DATA PORT
 DR0 ; DATA RECEIVE
 DX0 ; DATA TRANSMIT
 FSR0 ; FRAME SYNCHRONIZATION PULSE FOR RECEIVE
 FSX0 ; FRAME SYNCHRONIZATION PULSE FOR TRANSMIT
 TCLK0 - TCLK1 ; TIMER CLOCK
 XF0, XF1 ; EXTERNAL FLAG

