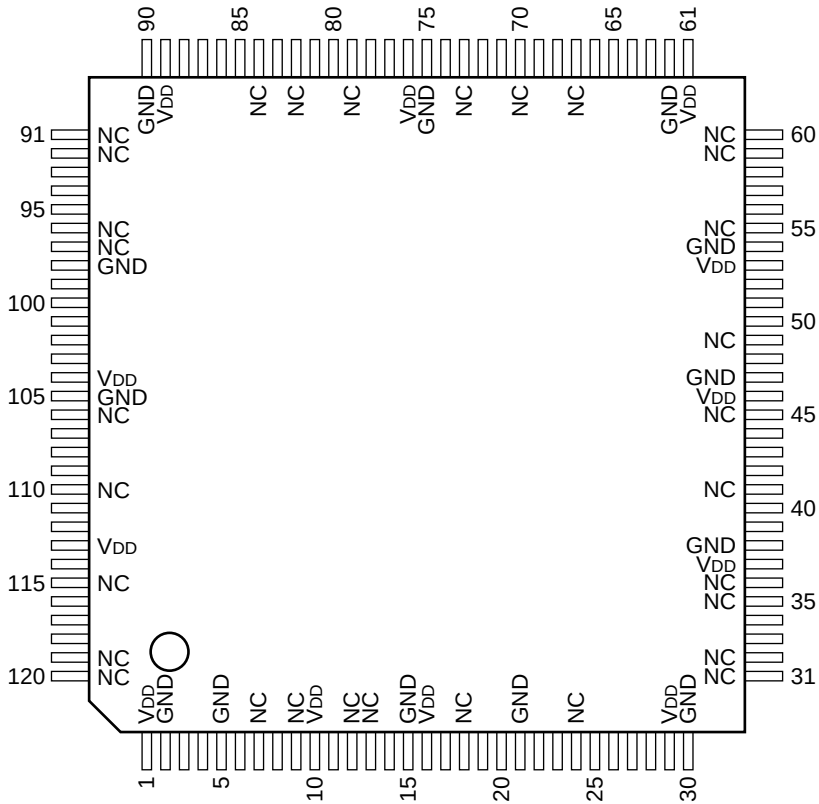


C-MOS MPEG AUDIO DECODER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	31	—	NC	61	—	V _{DD}	91	—	NC
2	—	GND	32	—	NC	62	—	GND	92	—	NC
3	I	OSCIN	33	O	TDO	63	O	A8	93	I	$\overline{\text{DSTRB}}$
4	O	OSCOOUT	34	I	TCK	64	O	A7	94	I	$\overline{\text{DCS}}$
5	—	GND	35	—	NC	65	O	A6	95	I	R/W
6	I	$\overline{\text{OSCEN}}$	36	—	NC	66	O	A5	96	—	NC
7	—	NC	37	—	V _{DD}	67	—	NC	97	—	NC
8	O	CLKOUT	38	—	GND	68	O	A4	98	—	GND
9	—	NC	39	I	TMS	69	O	A3	99	O	$\overline{\text{REQ}}$
10	—	V _{DD}	40	I	TDI	70	—	NC	100	O	$\overline{\text{WAIT}}$
11	O	$\overline{\text{PTS}}$	41	—	NC	71	O	A2	101	O	$\overline{\text{IRQ}}$
12	—	NC	42	I	TEST	72	O	A1	102	O	$\overline{\text{IRQOD}}$
13	—	NC	43	O	$\overline{\text{WE}}$	73	—	NC	103	I	SADDR0
14	I	PCMCLK	44	O	$\overline{\text{OE}}$	74	O	A0	104	—	V _{DD}
15	—	GND	45	—	NC	75	—	GND	105	—	GND
16	—	V _{DD}	46	—	V _{DD}	76	—	V _{DD}	106	—	NC
17	O	LRCLK	47	—	GND	77	I/O	SDATA7	107	I	SADDR1
18	—	NC	48	O	$\overline{\text{CAS}}$	78	I/O	SDATA6	108	I	SADDR2
19	O	SCLK	49	—	NC	79	—	NC	109	I	SADDR3
20	O	PCMDATA	50	O	$\overline{\text{RAS}}$	80	I/O	SDATA5	110	—	NC
21	—	GND	51	I/O	D3	81	I/O	SDATA4	111	I	SADDR4
22	O	FS0	52	I/O	D2	82	—	NC	112	I	SADDR5
23	O	FS1	53	—	V _{DD}	83	I/O	SDATA3	113	—	V _{DD}
24	—	NC	54	—	GND	84	—	NC	114	I	SADDR6
25	O	DMPH0	55	—	NC	85	I/O	SDATA2	115	—	NC
26	O	DMPH1	56	I/O	D1	86	I/O	SDATA1	116	I	$\overline{\text{RESET}}$
27	O	$\overline{\text{BOF}}$	57	I/O	D0	87	I/O	SDATA0	117	I	$\overline{\text{MUTE}}$
28	I	CLK90	58	O	A9	88	I	SIN	118	I	PLAY
29	—	V _{DD}	59	—	NC	89	—	V _{DD}	119	—	NC
30	—	GND	60	—	NC	90	—	GND	120	—	NC

INPUT

CLK90	: 90 kHz REFERENCE CLOCK
$\overline{\text{DCS}}$	: CHIP SELECT
$\overline{\text{DSTRB}}$	: DATA STROBE
$\overline{\text{MUTE}}$	: PCM DATA OUTPUT MUTING
OSCEN	: OSCILLATOR ENABLE
OSCIN	: CRYSTAL OSCILLATOR OR EXTERNAL CLOCK
PCMCLK	: PCM CLOCK
PLAY	: DECODED AUDIO OUTPUT ENABLE
$\overline{\text{R/W}}$	: READ/WRITE SELECT
RESET	: RESET
SADDR0 - SADDR6	: REGISTER ADDRESS BUS
SIN	: SERIAL COMPRESSED AUDIO DATA
TCK	: JTAG CLOCK
TDI	: JTAG DATA
TEST	: TEST ENABLE
TMS	: JTAG MODE SELECT

OUTPUT

A0 - A9	: DRAM ADDRESS
$\overline{\text{BOF}}$	: BEGINNING OF FRAME
$\overline{\text{CAS}}$	: COLUMN ADDRESS STROBE
CLKOUT	: BUFFERED SYSTEM CLOCK
DMPH0, DMPH1	: DECODED DE-EMPHASIS SELECT
FS0, FS1	: DECODED SAMPLING FREQUENCY
$\overline{\text{IRQ}}$	: INTERRUPT REQUEST
$\overline{\text{IRQOD}}$	: INTERRUPT REQUEST
LRCLK	: L/R CHANNEL SELECT
$\overline{\text{OE}}$	: DRAM OUTPUT ENABLE
OSCOUT	: CRYSTAL OSCILLATOR
PCMDATA	: PCM SERIAL DATA
$\overline{\text{PTS}}$	: PRESENTATION TIME STAMP
$\overline{\text{RAS}}$	: ROW ADDRESS STROBE
REQ	: DATA REQUEST
SCLK	: SERIAL PCM BIT CLOCK
$\overline{\text{TDO}}$	: JTAG DATA
$\overline{\text{WAIT}}$	: WAIT REQUEST
$\overline{\text{WE}}$	: DRAM READ/WRITE CONTROL

INPUT/OUTPUT

D0 - D3	: DRAM DATA
SDATA0 - SDATA7	: PARALLEL DATA

