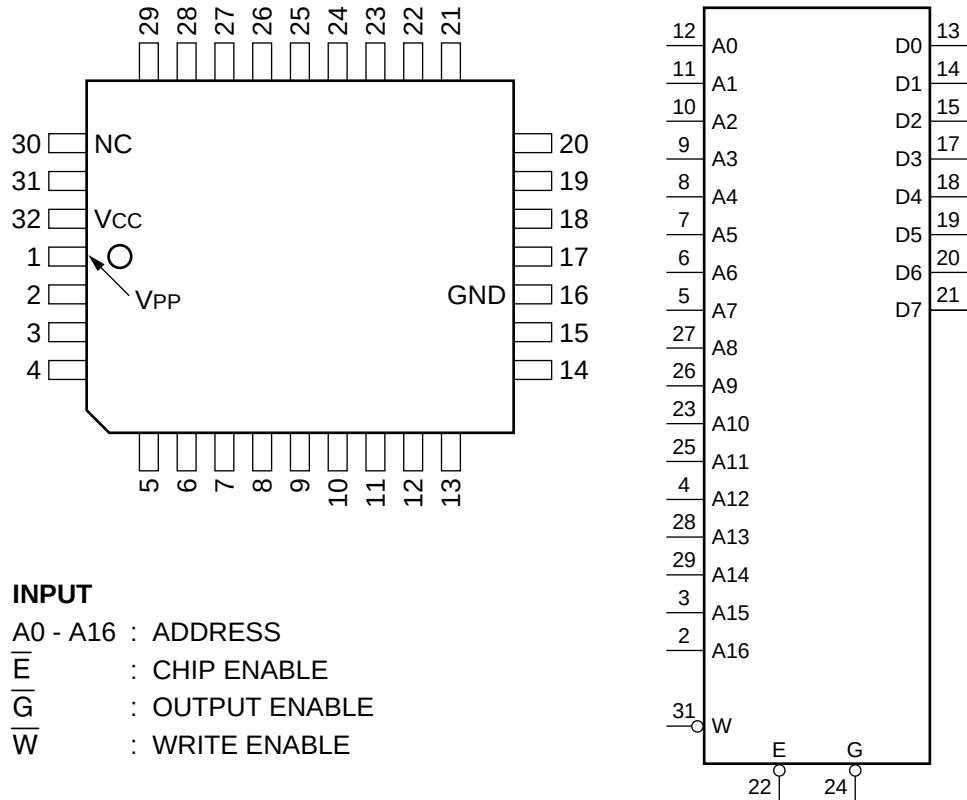


C-MOS 1 M (131,072 × 8)-BIT FLASH EEPROM

—TOP VIEW—

**INPUT**

A0 - A16 : ADDRESS

 $\overline{E}$: CHIP ENABLE $\overline{G}$: OUTPUT ENABLE $\overline{W}$: WRITE ENABLE**OUTPUT**

D0 - D7 : DATA

