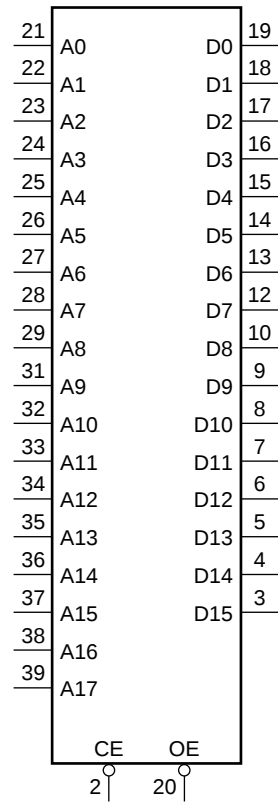
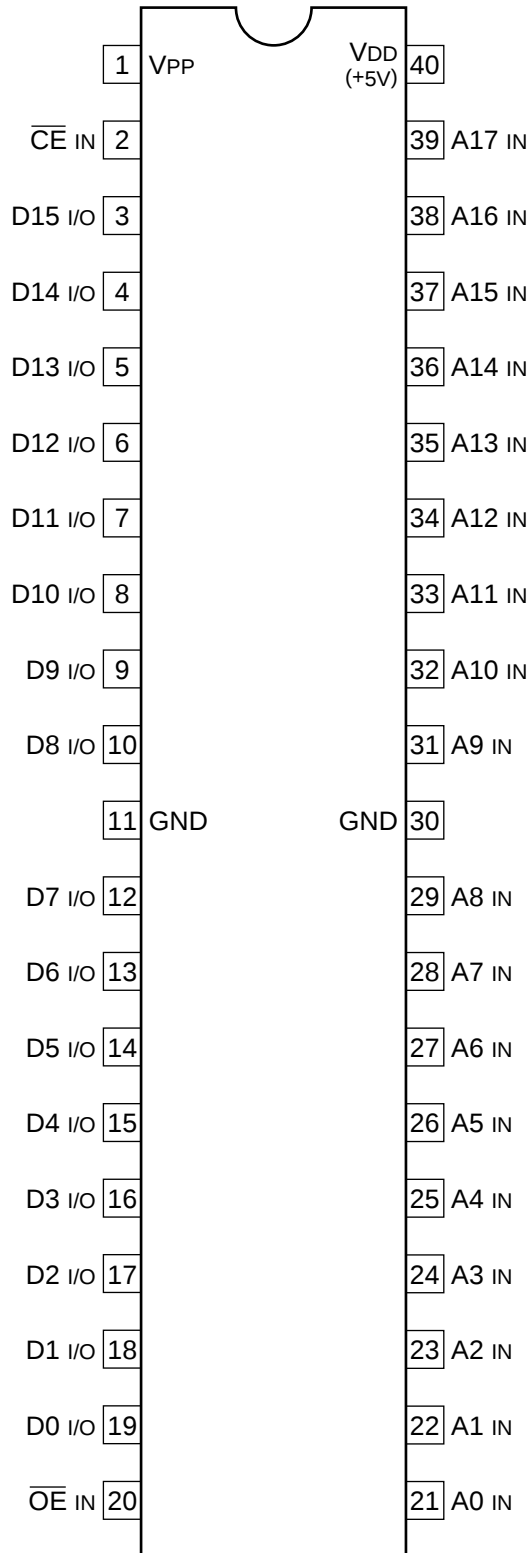
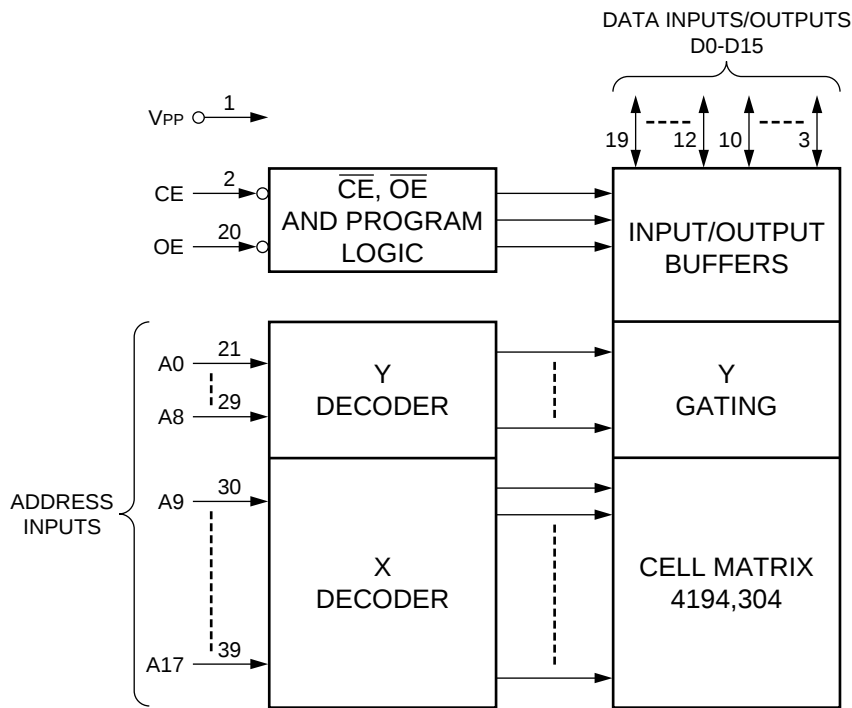

C-MOS 4M (262k×16)-BIT UV EPROM

-TOP VIEW-



A0-A17 ; ADDRESS INPUTS
 CE ; CHIP ENABLE
 D0-D15 ; DATA INPUTS/OUTPUTS
 OE ; OUTPUT ENABLE
 VPP ; PROGRAMMING VOLTAGE
 (PROGRAM : 13.0V)



ABOVE DIAGRAM SHOWS CONDITIONS BEFORE PROGRAMMING.

CE	OE	VPP	VDD	A9	A0	D0-D15	FUNCTION
0	0	VDD	VDD	X	X	DOUT	READ
0	1	VDD	VDD	X	X	HI-Z	OUTPUT DISABLE
0	1	VPP	VDD	X	X	DIN	PROGRAMMING
1	0	VPP	VDD	X	X	DOUT	VERIFY
1	1	VPP	VDD	X	X	HI-Z	PROGRAM INHIBIT
1	X	VDD	VDD	X	X	HI-Z	STANDBY
0	0	VDD	VDD	VH	0	97 (MAKER CODE)	SIGNATURE MODE
				VH	1	30 (DEVICE CODE)	

1 ; HIGH LEVEL
0 ; LOW LEVEL
X ; DON'T CARE
HI-Z ; HIGH IMPEDANCE
VH ; 12.0 ± 0.5V